

IBM POUGHKEEPSIE

Diagnostic Engineering Publications

1410/7010

March 31, 1964

Subject: Diagnostic Program CU01C

Sequence Number #051

Replaces CU01B

1. CU01C is applicable to all 1410/7010 machines with a minimum memory size of 40000 addresses. (Arithmetic errors will occur if EC#253480 is not yet installed.)
2. This program is a reliability test for the proper operation of all CPU instructions. It uses random data and random addresses. It also checks (where applicable) for the proper interrupt of all the various types of CPU instructions if overlap and priority are present.
3. Revision to CU01B to create CU01C.
 - (a) Program modified to prevent the interrupt check routine from operating if overlap is not available.
 - (b) Program modified so that PASS typeouts accumulate total number of passes and successful passes up to 100,000 instead of being reset at the end of each 1000passes.

Enclosures: Pages
Card Deck for CARD ONLY SYSTEM (as punched by UP51)
8 cards - card loader (1-7) and 1 core clear
559 cards no. 001 - 559 data cards
1 card execute card

Distribution: X 1410 With 40K memory or larger
X 7010
Other

CU01C

Page 001

3/31/64

CU01 C

RELIABILITY TEST OF THE 7010 CPU AND ANY 1410 CPU
WITH A MEMORY SIZE OF AT LEAST 40000 ADDRESSES

CONTENTS OF CU01 WRITEUP AND LISTING

2.01 .00.0	Test Description	Page 003
2.01 .01.0	Loading Procedures	Page 006
2.01 .02.0	Operating Procedures	Page 007
2.01 .03.0	Operating Hints, Comments	Page 008
2.01 .04.0	Program Stops and Restarts	Page 009
2.01 .05.0	Typeouts	Page 010
2.01 .06.1	Program Flow Chart	Page 012
2.01 .06.2	Typical Routine Flow Chart	Page 013
2.01 .07.0	Appendix I (List Of Constants)	Page 1-3
2.01 .08.0	Listing	Page 1-136
	Summary Page	

069

2.01 .00 TEST DESCRIPTION

2. 01.00.1 MODIFICATIONS

See Release Sheet

2.01 .00.2 Description

This program is designed to completely test and prove the reliability of the central processing unit of the 7010 computer and of any 1410 computer with a memory size of 40K or larger.

This program is written in a sequential routine format. See section 2. 01 .06. 1 for an overall flow diagram of the program and section 2. 01 .06. 2 for a flow diagram of a typical routine.

Routine zero is a basic test of a few basic instructions. An error in this routine should always result in an error halt with no programmed typeouts. Routine one sets up initial conditions for cycling the program. These two routines operate on the first pass only.

Routines 2 through 45 generate six constants that normally vary on each succeeding pass of the program. These constants are as follows:

Constants AA and BB	Signed numeric numbers from 1 to 10 characters long.
Constants CC and DD	Alphanumeric constants from 1 to 10 characters long. CC and DD are derived from AA and BB respectively by adding zones and eliminating any "8 bit" special characters. As a result CC and DD will be the same length, and be the numeric equal, of AA and BB respectively.

Constant EE

A five digit address derived from constant AA. EE will always be at least 150 higher than the last address of the program and at least 23 lower than the last address of your memory.

Constant FF

A five digit address derived from constant BB. FF will always be at least 50 higher than the last address of this program and at least 350 lower than the last address of your memory. It will also be at least 100 addresses away from address EE.

These six constants are used by routines 46 and up, to check each and every CPU instruction for proper operation.

If overlap and priority alert modes are available on your system, the program will also check for the proper interruption of all types of CPU instructions. To accomplish this, it types one character at the end of every 50 successful program passes and checks to see that the interrupt does not occur during a non-interruptable instruction, and that it does occur at the proper time of the interruptible instruction being checked. It also checks to ensure that BA1 and BXPA instructions will not be interrupted and that they will turn off the interrupt request. The character typed is the op code of the instruction that is currently being checked for proper interrupt, except in the cases of BA 1 and BXPA. It is then an R or Y respectively, indicating the instruction being checked should not be interrupted at all, and the interrupt request should be turned off.

These Interrupts will occur at a different address in memory on each successive check.

When CU01 runs in the RELIABILITY MODE from your System Diagnostic Tape, it will make only 100 passes. Interrupts will be checked every 5 passes of the program. This quick pass represents a compromise between thoroughness and speed.

The program will normally make 1000 passes before returning to the load routine. If TAD3 is set to request repeating of the program, the constants will vary indefinitely, and never actually "repeat" themselves as TAD3 might seem to indicate.

2.01.00.3 Equipment Required

CPU, CONSOLE PRINTER, Memory Of At Least 40K.

2.01.00.4 Card Deck

7 Cards -----Load Program

1 Card ----- Core Clear Card

Cards numbered 001-559 Program

Card numbered 006 contains all TADS

Card numbered 001 is STANDARD SYSTEM
CONTROL CARD

1 Card ----- Execute Card (Branch to 2000)

2.01.00.5 Machine E.C. Level

253480

2.01.00.6 Pass Length

1410 4 1/2 minutes

1410 ACC 3 3/4 minutes

7010 1 1/2 minutes

These times represent the approximate times required to run 1000 passes. 1000 passes should provide a satisfactory reliability check of the CPU.

2.01.01 LOADING PROCEDURES

2.01.01.1 FROM CARDS

1. Ready CU01 deck in the card reader.

2. (a) If reader is on a 7010 E channel:

Depress the CARD LOAD SWITCH

- (b) Otherwise:

Display and alter memory location 00000 to:

v v v
RL%11C0011\$. For E channel reader

v v v
XL 1100011\$. For F channel reader

Set to RUN, COMPUTER RESET, START

2.01.01.2 FROM TAPE (This procedure will load the current diagnostic tape control program. Refer to the tape control writeup for methods of selecting CU01.)

1. Ready your diagnostic tape on tape drive 0.

2. (a) If your diagnostic tape is on a 7010 E channel:

Depress the TAPE LOAD SWITCH

- (b) Otherwise:

Display and alter memory location 00000 to:

v v v
RL%B000011\$. For E channel tape

v v v
XL B000011\$. For F channel tape

v v v
3L ? B000011\$. For G channel tape

v v v
1L ! B000011\$. For H channel tape

Set to RUN, COMPUTER RESET, START

2.01 .02.0 OPERATING PROCEDURESLoad Program

Program will normally type its identity, run for 1000 passes, type success or failure indications and return to the load routine.

Normal program operations may be altered at any time by using the "Program Alter Routine" to set one or several of the following TAD locations to "1".

<u>TAD</u>	<u>ADDRESS</u>	<u>IF NOT 1 (NORMAL)</u>	<u>IF SET TO ONE</u>
0	01000	Normal typeouts	Bypass all typeouts for scoping
1	01001	No loops	Loop on present routine
2	01002	No halts	Halt on error
3	01003	1000 passes only	Cycle program indefinitely
4	01004	No error loops	On error, <u>program</u> will set TAD1 to cause looping of error routine.
5	01005	No extra typeouts	On error, program will print pass number, contents of applicable index registers, and the six constants now being used.
6	01006	Normal constants	Program will request the operator to enter his own six constants. <u>Program</u> will then clear TAD6 and set TAD7 to a one. (Caution: constants CC and DD must be the same length as AA and BB respectively. Constants EE and FF must be 5 digit addresses within the same limits used by the program. See section 2.XX.00.2.)

<u>TAD</u>	<u>ADDRESS</u>	<u>IF NOT 1 (NORMAL)</u>	<u>IF SET TO ONE</u>
7	01007	Normal constants	Program will maintain its present six constants and bypass routines 2-45.
8	01008	Check interrupt	Program will bypass the interrupt check.

2. 01 .03.0 OPERATING HINTS AND COMMENTS

This program was designed to be a rigorous test of the entire Central Processing Unit. Due to the varying constants used, no two program passes are the same. Therefore, the longer the test is run, the more complete is the check of the CPU.

This program is meant to be used for two purposes:

1. To test the reliability of the Central Processing Unit.
2. As an aid in isolating intermittent CPU failures that the current "Error Detection" program cannot find.

The following paragraphs may be of assistance in the diagnoses of failures:

1. Intermittent CPU failures - where cycling this program in an attempt to isolate intermittent failures, setting TADS 2, 3, 4 and 5 should provide the most information when the error occurs. If a malfunction causes the machine to stop on an alarm condition, placing the check control switch to RESTART may provide more information by allowing a typeout.
2. Loss of Program Control - If a CPU malfunction causes the program to lose control so that no logical error indications can be provided, try reloading and cycling the program with TADS 0 and 2 set. If the failure is solid enough that variable constants are not needed to induce an error, also set TAD 7. The setting of these TADS will cause only the essential portions of the test to run, thereby decreasing the chances of loss of control.

3. Erroneous Error Indications - Generally speaking, the first error indication to occur in the program should provide the most accurate information. However, when more than one routine provides error indications and these indications conflict with each other, discretion should be used in deciding which routine should be used to diagnose the error. The comments about TADS made in the last paragraph may apply here also.
4. Appendix I - This appendix contains a list of the constants used on the first 150 passes. Constants EE and FF, are listed for a 100K memory. If your memory is smaller, many of the EE and FF constants will be smaller than those listed.

2.01 .04.0 PROGRAM STOPS AND RESTARTS

2.01 .04.1 Program Stops

All programmed stops are error halts. When a halt occurs, refer to the IAR stop address in the program listing. Directly following the halt in the listing will be a statement indicating the reason for the halt.

2.01 .04.2 Program Restarts

- 00001 The program may be restarted from location one at any time. The result of restarting at 00001 is the same as if the program were reloaded, as far as program operation is concerned.
- 00008 Starting at location eight will cause the console printer to type: Present pass number, applicable index register contents, and the six constants as now contained in memory.

**FIRST ADDRESS
OF ANY ROUTINE** You may start at the first address of any routine at any time providing all previous routines have been cycled at least once. (Caution: If any routines are skipped in this manner, or cycled more than once in any one pass in this manner, Routine 142 will indicate a sequence error.)

2.01.05.0 TYPEOUTS

2.01.05.1 Non Error Typeouts:

CU01C

Program identity-typed when program is loaded and whenever program is restarted from location 00001.

XXXXXX PASSES, XXXXX OK

Timeout indicating the completion of the number of passes specified by XX's. Number of passes represented by YY's indicate how many of these passes were completed without error. Count is reset to zero at 100,000 passes.

Single character timeout. (i.e., R A)

At the end of every 50 successful passes, the interrupt check routine operates (unless bypassed by TAD8). In order to cause an interrupt, the program types out the single character op code of the instruction being checked for proper interrupt.

Pass number, index register and constant timeout.

You may request, by starting at address 00008, the typing of the present pass number, present applicable index register contents, and present constants in memory. For timeout format, see "Extra error data" timeout in section 2.01.05.2

2.01.05.2 Error Typeouts

XXXXXX PASSES, YYYYYY OK

- o This timeout is typed at the end of every 1000 passes. XXXXX indicates total number of passes completed. YYYYYY indicates how many of these passes were completed without error.

*RT XXX, ADDR YYYYY, ERR

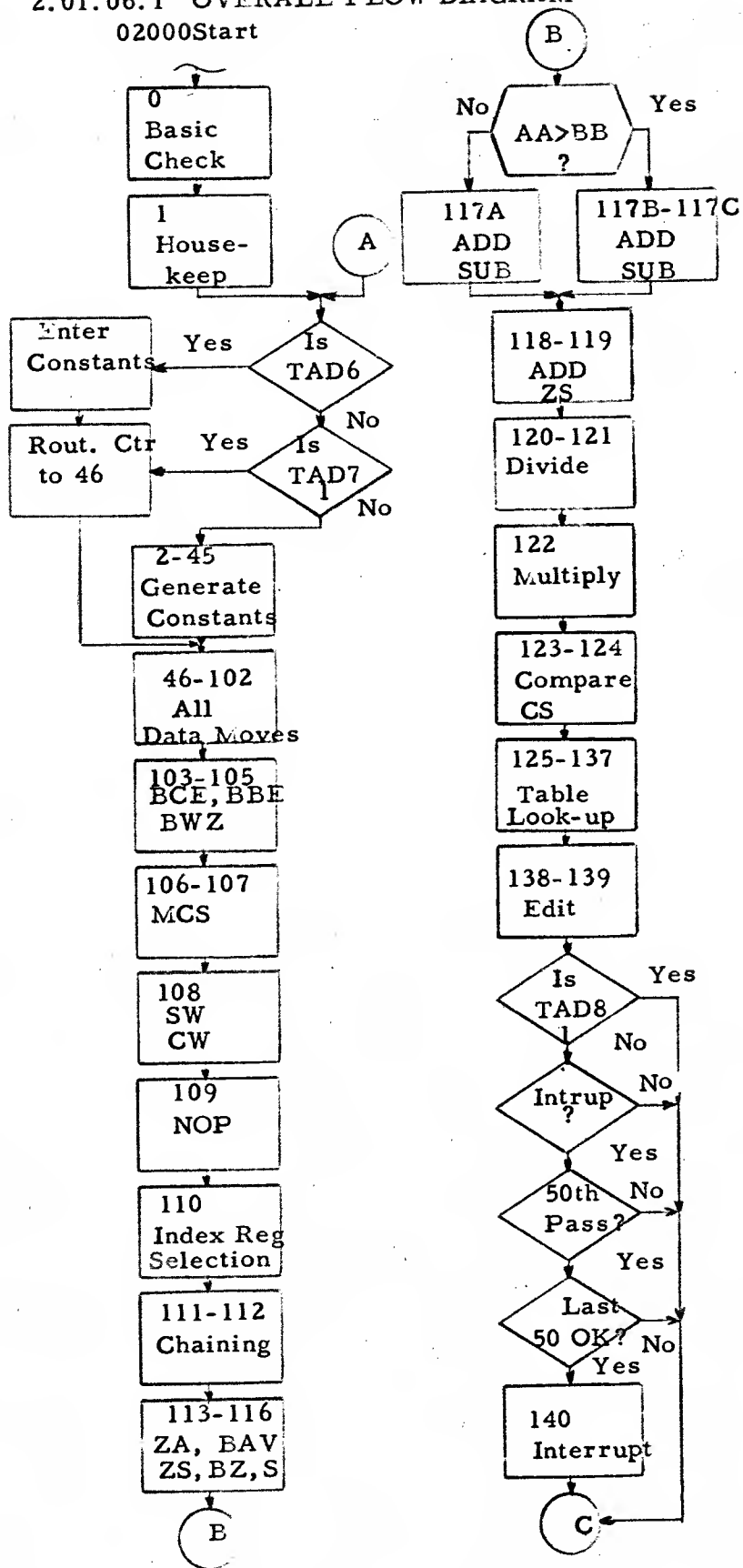
This typeout will normally occur whenever an error is encountered. "XXX" will be the number of the routine that found the error. "YYYYY" will be the address of the error halt within the routine. (Directly following this error halt address in the listing will be a brief paragraph indicating the reason for the error indication.)

PASS ZZZZZ

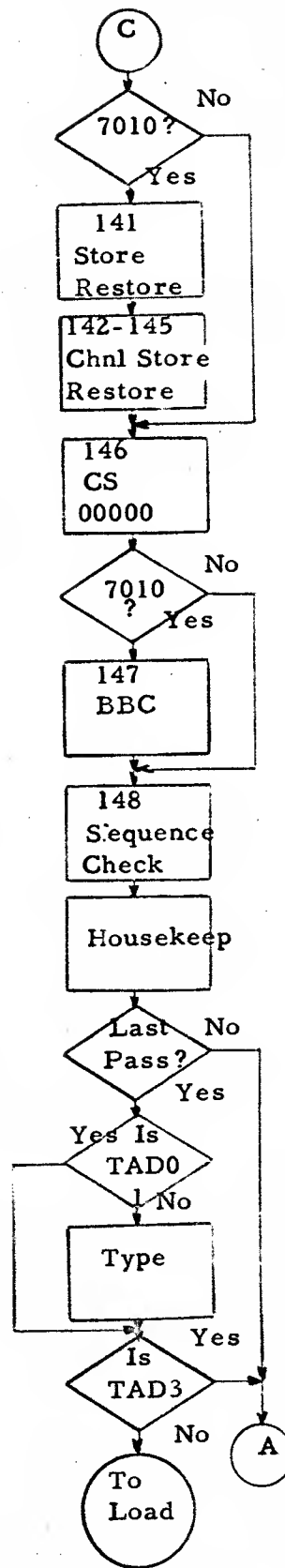
X1-IIII, X2-IIII, X5-IIII, X6-IIII, X7-IIII, X8-IIII, X9-IIII, X-IIII
AA-KKK, BB-KKKK, CC-KKK, DD-KKKK, EE-KKKKK, FF-KKKKK

Extra error data typeout will be typed in addition to the normal error typeout if TAD5 is a "1". ZZZZ will be the number of the present pass (this pass number is reset every 100,000 passes) The IIII's will be the contents of the specified index registers. The K's will be the actual specified constants. The lengths of AA, BB, CC and DD are variable, but EE and FF will always be 5 digits.

2.01.06.1 OVERALL FLOW DIAGRAM
02000Start

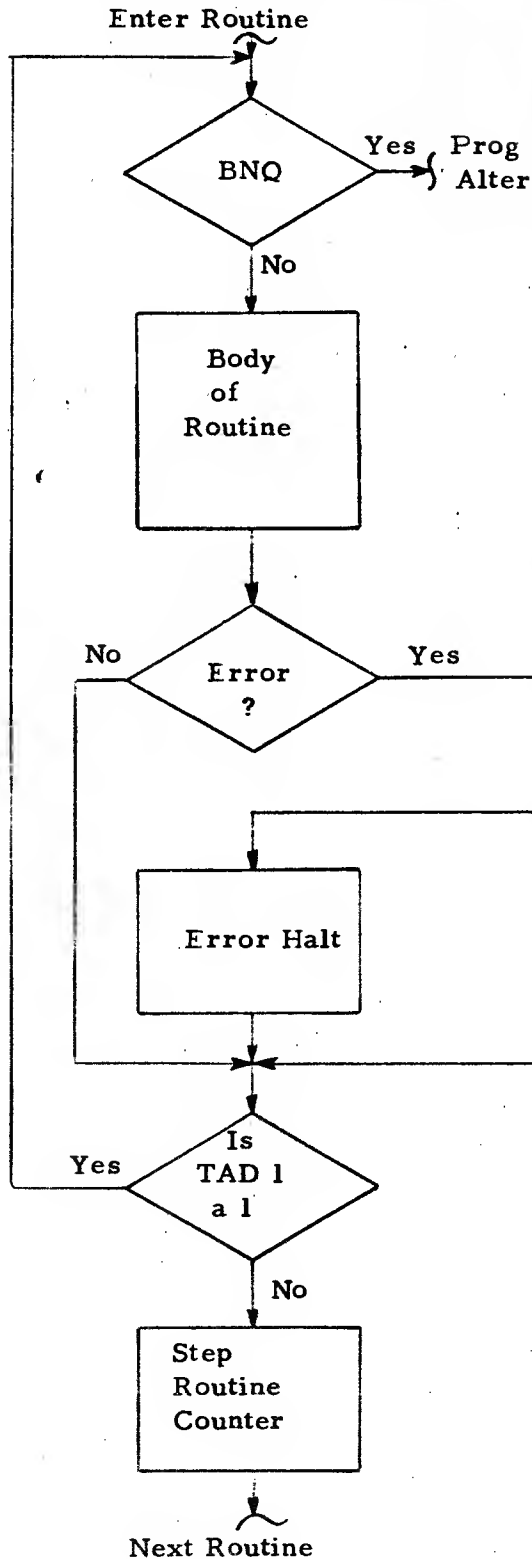


076
CU01
Page 012



2.01.06.2

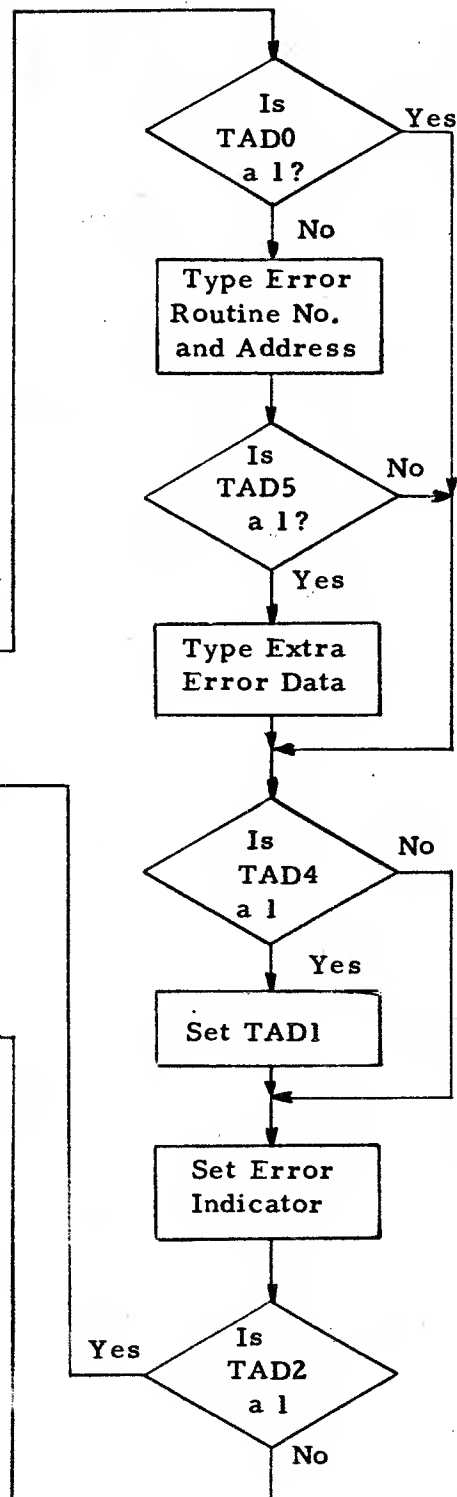
TYPICAL PROGRAM ROUTINE (Varies from routine to routine)



CU01

Page 013

CLOSED ERROR SUBROUTINE (Common to all routines)



Constants generated by and used by CU01 on the first 150 passes of the program.
 Constants EE and FF are listed for a 100K machine. EE and FF will vary on
 machines with smaller memories.

PASS	CONSTANT AA	CONSTANT BB	CONSTANT CC	CONSTANT DD	ADR EE	ADR FF
*****	*****	*****	*****	*****	*****	*****
0001	0000000000	0000000000	0?1+0?10+A	?+0?1?+10?4	27281	27301
0002	0000000000	0000000000	0?10?1+0?L	0+?+0?1?+L	27281	27301
0003	0000000000	0000000000	+!+0?10?1U	?!0+?+0M	27281	27381
0004	0000000000	0000000000	0+!+!+0?17	+0?10/A	27281	27381
0005	0000000000	0000000000	0?10?1+!1/1	?!+0?1	27281	27381
0006	0000000000	0000000000	??+0?10+JY	!0?1V	27282	27181
0007	0000000000	0000000000	!!???+0?K9	0?K6	27282	27181
0008	0000000000	0000000000	+00!!1???%7	!T1	27283	27181
0009	0000000000	0000000000	?!+!+0?!!GF	!+	27285	27181
0010	0000000000	0000000000	0!0?1+!12L	7	27286	27181
0011	0000000000	0000000000	!+0!0?J7Z	+00!!1???Y8	27293	27181
0012	0000000000	0000000000	?0!!+0L2R	!+!+0!!CA	27300	27181
0013	0000000000	0000000000	+?1?0?INS1	0?1+!11M	27313	27181
0014	0000000000	0000000000	0+?+?1H4L	0!0?KTT	27333	27181
0015	0000000000	0000000000	+0!0+ATFM	!+0J5?	27365	27181
0016	0000000000	0000000000	?0+!0K2+0	0!J27	27417	27181
0017	0000000000	0000000000	!??0?TV7J	!F!	27351	27181
0018	0000000000	0000000000	!00!2507Y	V.L	27488	27181
0019	0000000000	0000000000	+!+!+090D9	UD	27700	27181
0020	0000000000	0000000000	!+0+JVVJB7	P	28005	27182
0021	0000000000	0000000000	?0!S40FW	+!0?1?1F0W	28043	27183
0022	0000000000	0000000000	??!.90+3	+!+!+0?D5	29578	27184
0023	0000000000	0000000000	!!+FX!G9	+0?1SR[E	31091	27187
0024	0000000000	0000000000	0+A!LWMC	0!+4WMZ	33538	27191
0025	0000000000	0000000000	!016XC;J	!01PT6	37499	27197
0026	0000000000	0000000000	!0SPA44T	?EQ#1	43907	27200
0027	0000000000	0000000000	!!4L9S!D	KSEF	27294	27424
0028	0000000000	0000000000	?!XJ!GM7	AKJ	43920	27252
0029	0000000000	0000000000	+!ADRYNJ	0W	71064	27295
0030	0000000000	0000000000	0J4W?4IQ	C	42116	27367
0031	0000000000	0000000000	T#1!CUI	?0?1+KQ4R8	86049	27402
0032	0000000000	0000000000	40X+Y4P	?+?00JYRJ	28165	27600
0033	0000000000	0000000000	78811ZW	0!0X?7GK	87004	27709
0034	0000000000	0000000000	SCN20H3	+!0PCXI	88119	28456
0035	0000000000	0000000000	0F\$TBL9	0+0X10	75204	29244
0036	0000000000	0000000000	#0Y5BQS	E34TV	63323	30519
0037	0000000000	0000000000	D+J8JS1	::C0	38528	32582
0038	0000000000	0000000000	PE!CY!3	HJ1	28983	35921
0039	0000000000	0000000000	/DSKCKD	NU	40380	41323
0040	0000000000	0000000000	HQB\ASP	5	42232	50063
0041	0000000000	0000000000	24HMF/	+?+!DI1/U?	82612	37024
0042	0000000000	0000000000	!+457H	!+0E+ADRC	51976	59907
0043	0000000000	0000000000	L2L!S9	0ANI/0UM	34588	90939
0044	0000000000	0000000000	LEFOOP	1520PIZ	32302	50039
0045	0000000000	0000000000	0200,0	0X584B	39700	53777
0046	0000000000	0000000000	0AQF0C	RPY5	72063	37792
0047	0000000000	0000000000	,YYGR	0P0K	38955	64383
0048	0000000000	0000000000	Z571SS	0/E	83007	74495
0049	0000000000	0000000000	000+01	10	95712	99371
0050	0000000000	0000000000	0FTA23	0	79000	41500

PASS	CONSTANT AA	CONSTANT BB	CONSTANT CC	CONSTANT DD	ADR EE	ADR FF
*****	*****	*****	*****	*****	*****	*****
0051	4912M	000537897B	D9JBU	+?05T789XS	75312	53754
0052	0224G	015:3970I	!2KD7	!AV[39X09	54912	68130
0053	513VJ	3388195!	VATA1	C3QH/I50	30224	49066
0054	5361H	372121C	ELO/H	3PB1KAT	85137	90015
0055	04 8R	70044F	?U-HR	X!0MD6	42492	39081
0056	5860C	0360R	NHW?X	+CW!9	27629	29096
0057	63590	437F	63ERF	MLXF	42991	40997
0058	2220C	67I	KK203	OGR	43490	42913
0059	579R	6M	-NXRR	FU	32220	29548
0060	0800B	A	!H0!K	A	48579	45280
0061	380J	002596501Q	LY!A	?0KNZONE18	80800	74829
0062	180C	41229500P	/8?L	UJB2RN+!P	29380	47291
0063	:00M	8421238F	:F+U	HMSJB3Q0	37311	94939
0064	740G	788239P	7U07	X8HKTRG	39560	42230
0065	301J	624990	T0/1	FSU9IO	49740	37170
0066	041H	19V/I	04AY	19+-Z	89301	52220
0067	342R	V18!	3DK9	V/80	39041	35028
0068	384G	59N	CHM7	5Z5	28342	60067
0069	7270	5D	PKXF	EU	67384	95095
0070	12C	R	6/KL	9	95727	55163
0071	#9R	131875 01!	.RZ	/TJQPV0?J0	63112	50258
0072	52B	01 49670C	ESB	?JBUR0P+C	58839	32602
0073	92J	4 41283K	RB1	U0DJSYLK	49083	55680
0074	@4C	315811I	@4L	3AVHAJZ	80792	61102
0075	36M	75141F	LWM	PE1D/F	29875	43964
0076	80G	223>P	Q0G	2KL<7	83536	77886
0077	17J	907?	JXJ	R07!	86280	94669
0078	9VH	71C	I/VY	P/L	69817	72556
0079	14R	5!	/D9	5?	56097	67225
0080	12G	G	1S7	P	53045	39782
0081	70	@42303964M	7W	%4S3+C9F4U	82012	34188
0082	0C	13713701L	03	/3X1T7+A3	35058	46789
0083	7R	5V95716D	G9	5VZ5X1W4	89940	53797
0084	8B	614020E	HB	6A40S0V	97867	27768
0085	6J	234#8K	OJ	KC4.82	87808	54385
0086	@C	5130A	%T	EALF1	85676	54973
0087	0M	324F	?D	LKDF	73484	36540
0088	4G	96R	D7	ZOR	59160	64332
0089	5J	>D	NJ	\U	32644	73691
0090	9H	A	9Q	A	91805	38024
0091	R	4471:4118M	I	D47J[MJ/H4	51580	38897
0092	G	57328309G	P	VPC28L?RP	43385	49740
0093	O	:385323H	W]LYNC23Q	40704	61456
0094	C	8>VJ5 R	3	Y>AΔV-I	56959	38378
0095	R	10737H	9	/!X3PQ	97664	72654
0096	B	2337I	S	B3TPZ	54623	83852
0097	J	270	1	2G6	52288	56506
0098	C	27E	3	BX5	34043	40359
0099	M	8?	D	8+	59200	96866
0100	G	J	P	1	66112	37225

PASS	CONSTANT A7	CONSTANT B8	CONSTANT C9	CONSTANT D0	ADP E1	ADP F2
*****	*****	*****	*****	*****	*****	*****
0101	23409102:J	142099202F	KCM0Z0MK-/	/DK!IR000G	52443	34001
0102	071#16737H	26658648E	!C/\$A07TCH	KWVEQ00QE	91425	71310
0103	3050081>2R	2001924K	C!E#?YJ<K9	KK#/?BHK	43008	32589
0104	#76724900G	117441P	\$76GKDR?4P	1/PCN/X	35293	76724
0105	6821330030	180140	WQSJ33?0CC	AC?/MD	52031	82133
0106	058857904C	2490R	0N8YNXR04C	SHIO9	33003	50057
0107	740991027R	959K	7M49R14KXR	REZK	57904	40991
0108	799848 92B	70F	X198MY R25	P?N	91027	94848
0109	:40840020J	8K	:U?YD00K41	0B	40992	40040
0110	340089012C	N	3446Y14A23	N	40020	40009
0111	815290#2M	820899000R	Y152Z0, BU	YP00RZ9!0S	89012	81529
0112	22218000G	03 59406R	02B/80007	4000940W9	29032	49399
0113	03747077J	9700834Q	!CPD7?X71	Z7Y.YC48	45175	30928
0114	25905121H	8432211	KCZOEJ01H	04T2SAZ	47077	53146
0115	29712198R	188741	SRGJB/RHR	/007U0	65121	29712
0116	55677320G	8182P	RVGXPCX?X	0AY27	39329	55677
0117	853895190	270F	QELQZ5/RF	K08F	77320	85389
0118	0100>840C	30A	%!0<QM43	L?J	89519	41066
0119	2645>35 R	1M	B0DV<LH0R	AU	66040	53637
0120	67523200B	I	67N0LB4?K	I	56359	67523
0121	39/9500J	0:40322 M	LZ/9MF!A	?-D!LBK-t4	50331	93979
0122	1502700C	22914012R	J54KXG0L	BK1/D!JBR	79500	61502
0123	5482320M	82 49710	N40K3S!U	0K0MIXAK	29091	55482
0124	6 05080G	4 3176P	F0YN00!7	U 3JG0G	82320	44166
0125	2467401J	95807M	S4G0U4J1	ZNY07M	85080	72467
0126	9452001H	#95:C	94F000AY	\$9VJT	67401	89452
0127	19190002R	3240	/1/90HS9	3210	52481	61919
0128	1#72304G	30J	A\$7SCW47	TQ1	47013	51372
0129	#2922470	0?	\$S9BK4XF	N4	72364	40073
0130	0004>12C	N	*FFM!1BL	5	92247	00004
0131	9:>859R	#012355510	9L;HERZ	0AK3VEV16	64612	77956
0132	6214/2B	6:7858/4A	OK/4#KB	FJX8EQVUA	56859	42621
0133	578332J	8296927!	NXQLLS1	QBIOZ2G!	48603	47759
0134	199804C	72449 P	AIRQ4HL	7BMDI-X	78332	63199
0135	4/8 30M	07299H	A#Y&COM	!X2IRH	99804	83778
0136	977940G	02711	ZPGRD4G	!KP/9	78136	46977
0137	756077J	070M	7564PGJ	!X!M	77940	30756
0138	734017H	98C	7LUD17Y	!QL	56077	77734
0139	490094R	4?	UR00RU9	U?	34017	35671
0140	220112G	P	2K0/J27	P	90094	86224
0141	102070	07830267?	A02!XW	6!78L42074	51243	94714
0142	38320C	50527389G	TH3003	N4VK73QZ7	41330	80938
0143	:252/R	02820>9H	:SFS#9	02QS4;94	38320	75652
0144	90840R	415053L	Z0Q4Y0	4A50NTT	52527	56590
0145	433/6J	24512D	D3T-6J	SD5A24	90848	32243
0146	34224C	1528N	LDBB4T	/ES45	43376	88834
0147	4/600M	037K	A4G!?D	!3X3	34224	48258
0148	11824G	78A	/JQKD7	X0J	77600	37092
0149	89425J	0F	QZHSNJ	0N	38955	30989
0150	01249H	R	!ABMZQ	I	89425	40901

PGLIN	LABEL	OPCODE	OPERAND	LINES	37	MAXIMUM LIVES PER PAGE
AA01						
AA02						
AA03						
AA04						
AA05	*STANDARD TADS.					
AA06	TAD0	OC	@ @		PRINTED OUTPUT	BYPASS ALL PRINTING 1 01000
AA07	TAD1	OC	@ @		NO LOOPS	LOOP ROUTINE 1 01001
AA08	TAD2	OC	@ @		NO ERRJR HALTS	HALT ON ERROR 1 01002
AA09	TAD3	OC	@ @		1000 PASSES ONLY	CONTINUE CYCLING 1 01003
AA10	*SPECIAL TADS.					
AA11	TAD4	DC	@ @		NO LOOP ON ERROR	LOOP ON ERROR 1 01004
AA12	*					
AA13	TAD5	OC	@ @		NO PRINT EXTRA	PROGRAM SETS TAO1 1 01005
AA14	*				ERROR DATA	PRINT EXTRA ERROR DATA
AA15	TAD6	OC	@ @		USE PROGRAMMO	ENTER YOUR CONSTANTS 1 01006
AA16	*				CONSTANTS	PROG. CLEARS TAD6
AA17	*					PRGG. SETS TAO7
AA18	TAD7	DC	@ @		USE PROGRAMMED	MAINTAIN PRESENT 1 01007
AA19	*				CONSTANTS	CONSTANTS & BYPASS
AA20	*					ROUTINES 2-45
AA21	TAD8	OC	@ @ G ava		CHECK INTERRUPT	BYPASS INTERRUPT CHK 1 01008
AA22		OCW	ava			1 01009
AA23	*					
AA24	*					
AA25	*					
AA26		ORS	1010			01010
AA27	*	*** THIS AREA WILL BE RELOCATED TO ADDRESSES 00101-00157 ***				
AA28	R00101 *	SBR	X1		STORE INTERRUPT ADDRESS	* 7 01010 S 00029 B
AA29	R00108 *	MLZWA	@ @,X1		CLEAR ZONES	* 12 01017 D 29155 00029 M
AA30	R00120 *	BCE	LC14,LC12&1,#		BRANCH-ERRJR-SHOULONT INTRJPT*	12 01029 3 25201 25109 #
AA31	R00132 *	C	X1,X2		IS INTERRUPT ADDRESS CORRECT	11 01041 C 00029 00034
AA32	R00143 *	BE	RUP TOK		BRANCH-OK	* 7 01052 J 25259 S
AA33	R00150 *	B	RUPBAD G		INCORRECT INTERRUPT ADDRESS	* 7 01059 J 25230
AA34	R00157 *	OCW	ava			1 01065
AA35	*	*****				

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD DPERAND

*DEFINE CONTROL CONSTANTS.

AA37	NEX1	EQJ	400				
AA38							
AA39		DRG	01239		EQUATE LOAD PROGRAM TO ADDR 400		01239
AA40		DCW	20+11+12		NOT APPLICABLE TO 10K OR 20K		6 01244
AA41		DC	205J2		SEQ-NO.051,RELIABILITY MODE PRGS.		3 01247
AA42		DC	23+2		30 IS LAST 1000S,USE SYS1 ONLY		2 01249

*TEST NUMBER AND SUFFIX

AA43							
AA44	NUMBR	DCW	2CU012				4 01250
AA45	SUFFIX C	DC	2C2,G				1 01254

*STANDARD SYSTEM CONTROL CARD.

AA46							
AA47		ORG	1256				01255
AA48	SYS1	DC	2		MACHINE TYPE D-1410, I-1410I, X-7010	13	1 01255
AA49		DC	2		0-10K,1-20K,3-40K,5-50K,7-80K,9-100K.	14	1 01257
AA50		DC	2		SPARE	15	1 01258
AA51		DC	2		CHANNEL ONE PRINTER--1-100,2-132 CHAR	16	1 01259
AA52		DC	2		CHANNEL TWO PRINTER--1-100, 2-132 CHAR	17	1 01260
AA53		DC	2		1 BIT--EUROPEAN EDIT	18	1 01261
AA54					2 BIT--50 CYCLE POWER		

AA55		DC	2		SPARE	19	1 01262
AA56		DC	2		OVERLAP IF 1	20	1 01263
AA57		DC	2		PRIORITY ALERT IF 1	21	1 01264
AA58					SPARES	22-24	3 01267
AA59					CHANNEL ONE PRESENT IF 1	25	1 01268
AA60					CHANNEL TWO PRESENT IF 1	25	1 01269
AA61		DC	2		SPARES	27-28	2 01271
AA62					SPARES	29-31	3 01274
AA63		DC	2		SPARE	32	1 01275
AA64					REAL TIME CLOCK IF 1	33	1 01275
AA65		DC	2		SPARES	34-35	2 01278
AA66					2 SPARES	36-45	10 01288

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 3

PGLIN	LABEL	OPCODE	OPERAND	CT	ADORS	INSTRUCTION
AA6B			*STANDARD TYPE ROUTINE 2.			
AA69		ORG	12B9		01289	
AA70	TYP1	SBR	TYP2EB	7	01289	G 01304 B
AA71	TYP2	WCP	0	10	01296	M XT0 00000 W
AA72		SBR	TYP3E5	7	01306	G 01332 B
AA73		BCB1	*-23	7	01313	R 01296 2
AA74		BAL	*E1	7	01320	R 01327 M
AA75	TYP3	B	0	7	01327	J 00000
AA76			*PROGRAM ALTER ROUTINE.			
AA77	ITR	SBR	ITREXT5	7	01334	G 01394 B
AA78	ITR1	RCP	ITR2E4	10	01341	M XT0 01369 R
AA79		BEX1	ITR1,M	7	01351	R 01341 M
AA80		BAL	ITR2	7	01358	R 01365 M
AA81	ITR2	RCPW	0	10	01365	L XT0 00000 R
AA82		BEX1	ITR2,M	7	01375	R 01365 M
AA83		BAL	*E1	7	01382	R 01389 M
AA84	ITREXT	B	0	7	01389	J 00000
AA85		H		1	01396	.
AA86			*CONSTANTS AND STORAGE.			
AA87	CN3	DCW	a 7a	5	01401	
AA88	CN4		2a2	1	01402	
AA89	CN6		00000	5	01407	
AA90	CN8		000000000001	11	01418	
AA91	CN9		000000000002	11	01429	
AA92	CN0		000000000003	11	01440	
AA93	CA1		000000000000	11	01451	
AA94	CA2	DCW	000000000000	11	01462	
AA95	CO2	DCW	00010	5	01467	
AA96	CO25	DCW	00010	5	01472	
AA97	CO26	DCW	0	1	01473	

ENTER ROUTINE HERE
 TYPE MESSAGE
 SET RETURN ADDRESS
 BRANCH BUSY
 BRANCH ANY
 RETURN TO PROGRAM
 STORE BAR FOR RETURN
 ENTER LOCATION TO BE ALTERED
 RETURN TO REQUEST ON ANY BUT WLR
 RESET I/O INTERLOCK
 ENTER DATA
 RETURN TO REQUEST ON ANY BUT WLR
 BRANCH ANY
 RETURN TO PROGRAM
 DEFINE BRANCH INSTRUCTION
 ROUTINE COUNTER
 ERROR INDICATOR
 BASIC ADD CHECK STORAGE
 INITIAL CONSTANTS
 TEMPORARY CONSTANT STORAGE
 OITTO
 LENGTH OF CONSTANTS AA AND CC
 LENGTH OF CONSTANTS BB AND DD
 CONSTANT LENGTH INDICATOR

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 INSTRUCTION

CT AOORS

UPCOD OPERAND

LABEL

PGLIN

AB36	X	DCW	a	CONSTANT AA	11	01845
AB37	Y	DCW	a	CONSTANT BB	11	01856
AB38	Z	DCW	a	CONSTANT CC	11	01867
AB39		DCW	a	CONSTANT DD	1	01868
AB40	AA	DCW	20000000000Ja	CONSTANT EE	10	01878
AB41		DCW	a	CONSTANT FF	1	01879
AB42	BB	DCW	20000000000Ma		10	01889
AB43		DCW	a		1	01890
AB44	CC	DCW	a'0+M+M.MOJa		10	01900
AB45		DCW	a		1	01901
AB46	DD	DCW	a'.'+0.'0M'+uJa		10	01911
AB47	EE	DCW	39000		5	01916
AB48	FF	DCW	39500		5	01921
AB49	C19	DCW	00001		5	01922
AB50		DCW	00002		5	01931
AB51		DCW	00003		5	01936
AB52		DCW	00004		5	01941
AB53		DCW	00005		5	01946
AB54		DCW	00006		5	01951
AB55		DCW	00007		5	01956
AB56		DCW	00008		5	01961
AB57		DCW	00009		5	01966
AB58		DCW	00010		5	01971
AB59		DCW	00011		5	01976
AB60		DCW	00012		5	01981
AB61		DCW	00013		5	01986
AB62		DCW	00014		5	01991
AB63	C20	DCW	00015		5	01996
AB64		DCW	aMa		1	01997
AB65		DRG	2000		02000	
AB66	START	WCP	1250	TYPE PROGRAM IDENTITY	10	02000 M 310 01250 W
AB67		BCB1	*-16		7	02010 R 02000 2
AB68		BAL	*E1		7	02017 R 02024 M

PGLIN	LABEL	OPCODE	OPERAND	ROUTINE	CT	ADDRS	INSTRUCTION
AB70	*ROUTINE	O-PRELIMINARY BASIC TESTS.					
AB71	AA1	B	AA2		7	02024	J 02032
AB72	H			ROUTINE 0 ERROR	1	02031	.
AB73	*		UNCONDITIONAL BRANCH AT AA1 DID NOT BRANCH				
AB74	AA2	BCE	AA4,000,1	SHOULD NOT BRANCH	12	02032	B 02056 29166 1
AB75	AA3	BCE	AA5,010,1	SHOULD BRANCH	12	02044	B 02057 29167 1
AB76	AA4	H		ROUTINE 0 ERROR	1	02056	.
AB77	*		BRANCH CHARACTER EQUAL AT AA2 BRANCHED OR BCE AT AA3				
AB78	*		DID NOT BRANCH				
AB79	AA5	SBR	AA6010		7	02057	G 02074 B
AB80	AA6	BCE	AA7,0,.		12	02064	B 02077 00000 .
AB81	H			ROUTINE 0 ERROR	1	02076	.
AB82	*		STORE B REG AT AA5 FAILED				
AB83	AA7	MLNWA	0 00,CN3	CLEAR ROUTINE COUNTER	12	02077	D 29172 01401 V
AB84	BCE	AA8,CN3,0			12	02089	B 02102 01401 0
AB85	H			ROUTINE 0 ERROR	1	02101	.
AB86	*		MOVE INSTRUCTION AT AA7 FAILED				
AB87	AA8	MLCWA	099993,CN6		12	02102	D 29177 01407 X
AB88	AA9	A	000006,CN6	CHECK BASIC ADD	11	02114	A 29182 01407
AB89	BCE	AA0,CN6,1			12	02125	B 02138 01407 1
AB90	H			ROUTINE 0 ERROR	1	02137	.
AB91	*		ADD INSTRUCTION AT AA9 FAILED				
AB92	AA0	MLCS	000,CN4	CLEAR ERROR INDICATOR	12	02138	D 29166 01402 3
AB93	MLCWA	000000,C01		CLEAR PASS COUNTER	12	02150	D 29186 28538 X
AB94	MLCWA	000000,C04		CLEAR SUCCESS PASS COUNTER	12	02162	D 29186 01477 X
AB95	MLCWA	CQ8,CRI013		STORE INITIAL SPEC. CHAR. CONSTS.	12	02174	D 01671 01698 X
AB96	MLCWA	CQ9,CRI013		STORE INITIAL SPEC. CHARACTERS	12	02186	D 01684 01713 X
AB97	MLCWA	CR4046,CPI046		RESTORE ZONES	12	02198	D 01763 01554 X
AB98	MLCA	0RUPBOT,CT4		RESET INTERRUPT OP SELECTION ADDR	12	02210	D 29191 28726 1
AB99	MLCWA	0000000,X10		CLEAR INDEX REGISTERS	12	02222	D 29196 00074 X
AC00	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02234	D 29196
AC01	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02240	D 29196
AC02	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02246	D 29196
AC03	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02252	D 29196
AC04	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02258	D 29196
AC05	MLCWA	00000000		CLEAR INDEX REGISTERS	6	02264	D 29196

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCOD	OPERANO	CT	ADORS	INSTRUCTION
AC07		MLCWA	2000002	6	02270	O 29196
AC08		MLCWA	2000002	6	02276	D 29196
AC09		MLCWA	2000002	6	02282	O 29196
AC10		MLCWA	LOC21,21	12	02288	O 28713 00021 X
AC11		MLCWA		1	02300	O
AC12		MLCWA		1	02301	O
AC13		MLCWA	CQ5,7	12	02302	O 01629 00007 X
AC14		B	SC1	7	02314	J 27380
AC15		*ROUTINE I-SET INITIAL CONSTANTS, FIRST PASS ONLY.				
AC16	A81	MLCWA	CN8,A	12	02321	O 01418 01790 X
AC17		MLCWA	CN8,E	12	02333	O 01418 01812 X
AC18		MLCWA	CN9,8	12	02345	O 01429 01801 X
AC19		MLCWA	CN9,F	12	02357	O 01429 01823 X
AC20		MLCWA	CN0,G	12	02369	O 01440 01834 X
AC21		BNQ	ITR	7	02381	J 01334 Q
AC22	A82	C	CN8,A	11	02388	C 01418 01790
AC23		BU	A83	7	02399	J 02485 /
AC24		C	E,CN8	11	02406	C 01812 01418
AC25		BU	A83	7	02417	J 02485 /
AC26		C	B,CN9	11	02424	C 01801 01429
AC27		BU	A83	7	02435	J 02485 /
AC28		C	CN9,F	11	02442	C 01429 01823
AC29		BU	A83	7	02453	J 02485 /
AC30		C	G,CN0	11	02460	C 01834 01440
AC31		BU	A83	7	02471	J 02485 /
AC32		B	A84	7	02478	J 02493
AC33	A83	B	SE1	7	02485	J 27220
AC34		H		1	02492	.
AC35	*	THE PROPER DATA WAS NOT MOVED TO A,E,B,F OR G, OR				
AC36	*	ONE OF THE COMPARE OR BRANCH UNEQUAL INSTRUCTIONS				
AC37	*	OIO NOT OPERATE PROPERLY				
AC38	A84	8CE	A81,TA01,1	12	02493	B 02321 01001 1
AC39		B	SC1	7	02505	J 27380
						ROUTINE 1 ERROR
						LOOP ROUTINE 1
						STEP ROUTINE COUNTER TO 2

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AC41	*ROUTINE	2-SET HIGH ORDER DIGITS OF CONSTANTS A,E,B,F, AND G TO				
AC42	*	ZERO-THIS IS START OF PROGRAM ON REPETITIVE PASSES				
AC43	AC1	BCE	SD1,TAD6,1	12	02512	B 27405 01006 1
AC44		BCE	SD8,TAD7,1	12	02524	B 28032 01007 1
AC45	AC9	MLCWS	202,A-10	12	02536	D 29166 01780 7
AC46		MLCWS	202,E-10	12	02548	D 29166 01802 7
AC47		MLCWS	202,8-10	12	02560	D 29166 01791 7
AC48		MLCWS	202,F-10	12	02572	D 29166 01813 7
AC49		MLCWS	202,G-10	12	02584	D 29166 01824 7
AC50	AC2	BCE	AC3,A-10,0	12	02596	B 02615 01780 0
AC51		B	AC7	7	02608	J 02684
AC52	AC3	BCE	AC4,E-10,0	12	02615	B 02634 01802 0
AC53		B	AC7	7	02627	J 02684
AC54	AC4	BCE	AC5,B-10,0	12	02634	B 02653 01791 0
AC55		B	AC7	7	02646	J 02684
AC56	AC5	BCE	AC6,F-10,0	12	02653	B 02672 01813 0
AC57		B	AC7	7	02665	J 02684
AC58	AC6	BCE	AC8,G-10,0	12	02672	B 02711 01824 0
AC59	AC7	8NQ	ITR	7	02684	J 01334 Q
AC60		D	SEL	7	02691	J 27220
AC61	H			1	02698	.
AC62	*		THIS ERROR HALT INDICATES THAT THE HIGH ORDER DIGIT			
AC63	*		OF ONE OF THE FIVE CONSTANTS IS NOT NOW SET TO ZERO			
AC64	*		-ONE OF THE MLCWS, BCE, OR B INSTRUCTIONS FAILED			
AC65		8CE	AC9,TAD1,1	12	02699	B 02536 01001 1
AC66	AC8	B	SCI	7	02711	J 27380

ROUTINE 2 ERROR

THIS ERROR HALT INDICATES THAT THE HIGH ORDER DIGIT

OF ONE OF THE FIVE CONSTANTS IS NOT NOW SET TO ZERO

-ONE OF THE MLCWS, BCE, OR B INSTRUCTIONS FAILED

LOOP ROUTINE 2

STEP ROUTINE COUNTER TO 3

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 9

PGLIN	LABEL	UPCODE	OPERAND	CT	ADDRES	INSTRUCTION
AC68	*ROUTINE	3-SAVE	CONSTANTS A AND B.			
AC69	AD1	BNQ	ITR	7	02718	J 01334 Q
AC70		MLCWA	A,X	12	02725	D 01790 01845 X
AC71		MLCWA	B,Y	12	02737	D 01801 01856 X
AC72		C	A,X	11	02749	C 01790 01845
AC73		BU	AD3	7	02760	J 02792 /
AC74		C	Y,B	11	02767	C 01856 01801
AC75		BU	AD3	7	02778	J 02792 /
AC76		B	AD4	7	02785	J 02812
AC77	AD3	B	SE1	7	02792	J 27220
AC78	H			1	02799	.
AC79	*					ROUTINE 3 ERROR
AC80	*					THIS ERROR HALT INDICATES THAT A DOES NOT EQUAL X OR
AC81						B DOES NOT EQUAL Y-MLCWA,C, OR BU INSTRUCTION FAILED
AC82	AD4	B	SCI	12	02800	B 02718 01001 I
AC83	*ROUTINE	4-SUBTRACT	CONSTANT B FROM CONSTANT A.	7	02812	J 27380
AC84	AE1	BNQ	ITR	7	02819	J 01334 Q
AC85		MLCWA	X,A	12	02826	D 01845 01790 X
AC86		S	B,A	11	02838	S 01801 01790
AC87		MLCWA	A,CAL	12	02849	D 01790 01451 X
AC88		A	B,CAL	11	02861	A 01801 01451
AC89		S	X,CAL	11	02872	S 01845 01451
AC90		BZ	AE2	7	02883	J 02898 V
AC91		B	SE1	7	02890	J 27220
AC92	H			1	02897	.
AC93	*					ROUTINE 4 ERROR
AC94	*					THE DIFFERENCE OF A MINUS B WHEN ADDED TO B, DID NOT
AC95	AE2	BCE	AE1,IA01,1	12	02898	B 02819 01001 I
AC96	B		SCI	7	02910	J 27380
						STEP ROUTINE COUNTER TO 5

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AC98	*ROUTINE	5-SET CONSTANT B TO NEW VALUE.				
AC99	AF1	BNQ	ITR	7	02917	J 01334 Q
AD00		MLCWA	A,B	12	02924	D 01790 01801 X
AD01		C	B,A	11	02936	C 01801 01790
AD02		BE	AF2	7	02947	J 02962 S
AD03		B	SE1	7	02954	J 27220
AD04		H		1	02961	.
AD05	*		AFTER MOVING A TO B, A COMPARISON OF A AND B DID NOT			
AD06	*		RESULT IN A BRANCH ON EQUAL			
AD07	AF2	BCE	AF1,TAD1,1	12	02962	B 02917 01001 1
AD08		B	SC1	7	02974	J 27380
AD09	*ROUTINE	6-SET CONSTANT A TO FORMER VALUE OF CONSTANT B.				
AD10	AG1	BNQ	ITR	7	02981	J 01334 Q
AD11		MLCWA	Y,A	12	02988	D 01856 01790 X
AD12		C	Y,A	11	03000	C 01856 01790
AD13		BE	AG2	7	03011	J 03026 S
AD14		B	SE1	7	03018	J 27220
AD15		H		1	03025	.
AD16	*		AFTER MOVING Y TO A, A COMPARE OF Y AND A DID NOT			
AD17	*		RESULT IN A BRANCH EQUAL			
AD18	AG2	BCE	AG1,TAD1,1	12	03026	B 02981 01001 1
AD19		B	SC1	7	03038	J 27380
AD20	*ROUTINE	7-MOVE CONSTANT B TO CONSTANT AA STORAGE.				
AD21	AH1	BNQ	ITR	7	03045	J 01334 Q
AD22		MLCWA	B,AA	12	03052	D 01801 01878 X
AD23		C	B,AA	11	03064	C 01801 01878
AD24		BE	AH2	7	03075	J 03090 S
AD25		B	SE1	7	03082	J 27220
AD26		H		1	03089	.
AD27	AH2	BCE	AH1,TAD1,1	12	03090	B 03045 01001 1
AD28		B	SC1	7	03102	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 11

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AD30	*ROUTINE	B-LOAD INDEX REGISTER ONE TO 11.				
AD31	AI1	BNQ	ITR	7	03109	J 01334 Q
AD32		MLCWA	0000110,X1	12	03116	D 29201 00029 X
AD33		C	0000110,X1	11	03128	C 29201 00029
AD34		BE	AI2	7	03139	J 03154 S
AD35		B	SE1	7	03146	J 27220
AD36		H		1	03153	.
AD37	*		AFTER LOADING INDEX REGISTER ONE WITH THE CONSTANT			
AD38	*		11, INDEX REGISTER ONE DID NOT COMPARE WITH THE			
AD39	*		CONSTANT 11.			
AD40	AI2	BCE	AI1,TAD1,1	12	03154	B 03109 01001 1
AD41		B	SC1	7	03166	J 27380
AD42	*ROUTINE	9-CYCLE SPECIAL CHARACTERS AND CONSTANT ONE POSITION.				
AD43	AI3	BNQ	ITR	7	03173	J 01334 Q
AD44		SW	CR101	6	03180	, 01686
AD45		SW	CR201	6	03186	, 01701
AD46		MRCWG	CR101,CR1	12	03192	D 01686 01685 L
AD47		MRCWG	CR201,CR2	12	03204	D 01701 01700 L
AD48		BCE	AI4,CR1013,M	12	03216	B 03235 01698 M
AD49		B	AI5	7	03228	J 03247
AD50	AI4	BCE	AI6,CR2013,M	12	03235	B 03279 01713 M
AD51	AI5	B	SE1	7	03247	J 27220
AD52		H		1	03254	.
AD53	*		IF THE TWO MRCWG MOVES OPERATED PROPERLY, CR1013			
AD54	*		AND CR2013 SHOULD CONTAIN GROUP MARKS. THEY DO NOT.			
AD55		MLCS	CR1,CR1013	12	03255	D 01685 01698 3
AD56		MLCS	CR2,CR2013	12	03267	D 01700 01713 3
AD57	AI6	MLCS	CR1,CR1013	12	03279	D 01685 01698 3
AD58		MLCS	CR2,CR2013	12	03291	D 01700 01713 3
AD59		C	CR1,CR1013	11	03303	C 01685 01698
AD60		BE	AI7	7	03314	J 03328 S
AD61		B	AI8	7	03321	J 03346
AD62	AI7	C	CR2,CR2013	11	03328	C 01700 01713
AD63		BE	AI9	7	03339	J 03354 S

PGLIN	LABEL	DPCDD	OPERAND	CT	ADDRS	INSTRUCTION
AD65	AI8	B	SEI	7	03346	J 27220
AD66		H		1	03353	.
AD67			ROUTINE 9 ERROR			
AD68			AFTER THE OPERATION OF THE ABOVE TWO MLCS			
AD69			INSTRUCTIONS, THE LOCATION MOVED TO DID NOT COMPARE			
AD70	AI9		WITH THE DATA MOVED.			
AD71		8CE	AI3,TAD1,1	12	03354	B 03173 01001 1
AD72			LOOP ROUTINE 9			
AD73		8	SCI	7	03366	J 27380
AD74	AI11		ROUTINE 10-DEPOSIT OCCASIONAL SPECIAL CHARACTERS IN CONSTANT AA.			
AD75		8NQ	ITR	7	03373	J 01334 Q
AD76		C	AA-10&X1,CR1&2&X1	11	03380	C 018W8 016Y7
AD77		8U	AI12	7	03391	J 03436 /
AD78		MLCS	CR2&2&X1,AA-10&X1	12	03398	D 017+2 018W8 3
AD79		C	AA-10&X1,CR2&2&X1	11	03410	C 018W8 017+2
AD80		8E	AI12	7	03421	J 03436 S
AD81		8	SEI	7	03428	J 27220
AD82		H		1	03435	.
AD83			AFTER OPERATION OF THE MLCS INSTRUCTION, THE			
AD84			LOCATION MOVED TO DID NOT COMPARE WITH THE DATA			
AD85	AI12		MOVED.			
AD86		MLCWA	X1,C08	12	03436	D 00029 01482 X
AD87		S	&1,X1	11	03448	S 29202 00029
AD88		BZ	AI13	7	03459	J 03503 V
AD89		A	-1,C08	11	03466	A 29203 01482
AD90		C	X1,C08	11	03477	C 00029 01482
AD91		8E	AI11	7	03488	J 03373 S
AD92		8	SEI	7	03495	J 27220
AD93		H		1	03502	.
AD94			AFTER SUBTRACTING A &1 FROM INDEX REG ONE, AND			
AD95			ADDING A -1 TO THE SAME NUMBER IN C08, INDEX REG ONE			
AD96	AI13		AND C08 DID NOT COMPARE.			
AD97		MLCWA	000011&X1	12	03503	D 29201 00029 X
AD98		BCE	AI11,TAD1,1	12	03515	B 03373 01001 1
		B	SCI	7	03527	J 27380
			LOAD INDEX REG 1 FOR LOOPING			
			LODP ROUTINE 10			
			STEP ROUTINE COUNTER TO 11			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 13

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AE00			*ROUTINE 11-CLEAR INDEX REGISTER ONE.			
AE01	AJ1	BNQ	ITR	7	03534	J 01334 Q
AE02		MLCWA	20000002,X1	12	03541	D 29196 00029 X
AE03		C	20000002,X1	11	03553	C 29196 00029
AE04		BE	AJ2	7	03564	J 03579 S
AE05		B	SE1	7	03571	J 27220
AE06		H		1	03578	.
AE07	*		COULD NOT CLEAR INDEX REG ONE.			
AE08	AJ2	BCE	AJ1,TAD1,1	12	03579	B 03534 01001 I
AE09		B	SC1	7	03591	J 27380
AE10			*ROUTINE 12-SET INDEX REG ONE FROM THE PROGRAM PASS COUNTER.			
AE11	AK1	BNQ	ITR	7	03598	J 01334 Q
AE12		MLCS	CO1-1,X1	12	03605	D 28537 00029 3
AE13		MLNS	CO1-1,AK2&11	12	03617	D 28537 03640 1
AE14	AK2	BCE	AK3,X1,0	12	03629	B 03649 00029 0
AE15		B	SE1	7	03641	J 27220
AE16		H		1	03648	.
AE17	*		COULD NOT SET INDEX REG ONE WITH AN MLCS INSTRUCTION			
AE18	AK3	BCE	AK1,TAD1,1	12	03649	B 03598 01001 I
AE19		B	SC1	7	03661	J 27380

CT ADORS INSTRUCTION

PGLIN LABEL

OPCDD OPERAND

*ROUTINE 13-REDUCE FIELD LENGTH OF CONSTANT AA BY ONE CHARACTER

* EVERY TENTH PROGRAM PASS BY PLACEMENT OF WORD MARK

AL1

BNQ ITR

BRANCH INQUIRY

SW AA-9&X1

SCNLA AA,1011

SBR CO2

A -1011,CO2

MLZS @ @,CO2

C CO2,@00010@

BL AL2

C CO2,@00001@

BH AL2

B AL3

B SE1

H

ROUTINE 13 ERROR

WORD MARK WAS NOT SET WITHIN THE FIELD OF CONSTANT

AA OR THE SCNLA INSTRUCTION DID NOT OPERATE

PROPERLY-IF WORD-MARK DID NOT SET, ERRONEOUS ERROR

INDICATIONS OR LOSS OF CONTROL MAY RESULT IN LATER

ROUTINES

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

STEP ROUTINE COUNTER TO 14

AL3

BCE AL1,TA01,1

8

SC1

LDOP ROUTINE 13

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 15

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AE44			*ROUTINE 14-SET HIGH ORDER DIGITS OF E,F AND G TO ZERO			
AE45	AN1	BNQ	ITR	7	03793	J 01334 Q
AE46		MLCS	303,E-10	12	03800	D 29166 01802 3
AE47		MLCS	303,F-10	12	03812	D 29166 01813 3
AE48		MLCS	303,G-10	12	03824	D 29166 01824 3
AE49		BCE	AN3,E-10,0	12	03836	B 03855 01802 0
AE50		B	AN5	7	03848	J 03886
AE51	AN3	BCE	AN4,F-10,0	12	03855	B 03874 01813 0
AE52		B	AN5	7	03867	J 03886
AE53	AN4	BCE	AN6,G-10,0	12	03874	B 03894 01824 0
AE54	AN5	B	SE1	7	03886	J 27220
AE55	H			1	03893	.
AE56	*		THE HIGH ORDER DIGIT OF E,F,OR G DID NOT SET TO ZERO			
AE57	*		OR A BCE INSTRUCTION FAILED.			
AE58	AN6	BCE	AN1,TAD1,1	12	03894	B 03793 01001 1
AE59		B	SC1	7	03906	J 27380
AE60			*ROUTINE 15-SAVE E,F AND G INX,Y AND Z			
AE61	AD1	BNQ	ITR	7	03913	J 01334 Q
AE62		MLCA	E,X	12	03920	D 01812 01845 1
AE63		MLCA	F,Y	12	03932	D 01823 01856 1
AE64		MLCA	G,Z	12	03944	D 01834 01867 1
AE65		C	E,X	11	03956	C 01812 01845
AE66		BU	AD3	7	03967	J 04017 /
AE67		C	F,Y	11	03974	C 01823 01856
AE68		BU	AD3	7	03985	J 04017 /
AE69		C	Z,G	11	03992	C 01867 01834
AE70		BU	AD3	7	04003	J 04017 /
AE71		B	AD4	7	04010	J 04025
AE72		B	SE1	7	04017	J 27220
AE73	H			1	04024	.
AE74	*		E AND X, F AND Y, OR G AND Z DID NOT COMPARE AFTER			
AE75	*		MLCA INSTRUCTIONS.			
AE76	AD4	BCE	A01,TAD1,1	12	04025	B 03913 01001 1
AE77		B	SC1	7	04037	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 16
INSTRUCTION

PGLIN

LABEL

OPC00 OPERAND

CY ADORS

INSTRUCTION

PGLIN	LABEL	OPC00	OPERAND	CY	ADORS	INSTRUCTION
AE79	•ROUTINE 16-ADD F TO G					
AE80	AP1	BNQ	ITR	7	04044	J 01334 Q
AE81		MLCA	Z,G	12	04051	D 01867 01834 T
AE82		A	F,G	11	04063	A 01823 01834
AE83	AP2	MLCA	G,CA1	12	04074	D 01834 01451 T
AE84		S	F,CA1	11	04086	S 01823 01451
AE85		S	Z,CA1	11	04097	S 01867 01451
AE86		BZ	AP3	7	04108	J 04123 V
AE87		B	SE1	7	04115	J 27220
AE88		H		1	04122	.
AE89	•		SUBTRACTING CONSTANT F FROM THE SUM OF F PLUS G OLD			
AE90	•		NOT RESULT IN A DIFFERENCE THAT COMPARED WITH G.			
AE91	AP3	BCE	AP1,TAD1,1	12	04123	B 04044 01001 1
AE92		B	SC1	7	04135	J 27380
AE93	•ROUTINE 17-SET HIGH ORDER DIGIT OF G TO ZERO					
AE94	AQ1	BNQ	ITR	7	04142	J 01334 Q
AE95		MLCS	202,G-10	12	04149	D 29166 01824 3
AE96		BCE	AQ2,G-10,0	12	04161	B 04181 01824 0
AE97		B	SE1	7	04173	J 27220
AE98		H		1	04180	.
AE99	•		MLCS INSTRUCTION DID NOT OPERATE PROPERLY OR BCE			
AF00	•		INSTRUCTION FAILED.			
AF01	AQ2	BCE	AQ1,TAD1,1	12	04181	B 04142 01001 1
AF02		B	SC1	7	04193	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AF04	*ROUTINE 18-SUBTRACT CONSTANT G FROM CONSTANT E					
AF05	AR1	BNQ	ITR	7	04200	J 01334 Q
AF06		MLCA	X,E	12	04207	D 01845 01812 I
AF07		S	G,E	11	04219	S 01834 01812
AF08		MLCA	E,CAL	12	04230	D 01812 01451 I
AF09		A	G,CAL	11	04242	A 01834 01451
AF10		S	X,CAL	11	04253	S 01845 01451
AF11		BZ	AR2	7	04264	J 04279 V
AF12		B	SE1	7	04271	J 27220
AF13		H		1	04278	.
AF14	*					ROUTINE 18 ERROR
AF15	*					THE DIFFERENCE OF E MINUS G WHEN ADDED TO G DID NOT
AF16	AR2	BCE	AR1,IAD1,1	12	04279	B 04200 01001 I
AF17		B	SC1	7	04291	J 27380
AF18	*ROUTINE 19-SAVE CONSTANT E					
AF19	AS1	BNQ	ITR	7	04298	J 01334 Q
AF20		MLCA	E,CAL	12	04305	D 01812 01451 I
AF21		C	E,CAL	11	04317	C 01812 01451
AF22		BE	AS2	7	04328	J 04343 S
AF23		B	SC1	7	04335	J 27220
AF24		H		1	04342	.
AF25	*					ROUTINE 19 ERROR
AF26	AS2	BCE	AS1,IAD1,1	12	04343	B 04298 01001 I
AF27		B	SC1	7	04355	J 27380
AF28	*ROUTINE 20-SET E TO FORMER F, F TO FORMER G, AND G TO RESULT OF					
AF29	*					THE SUBTRACTION IN THE ROUTINE BEFORE THE LAST.
AF30	AT1	BNQ	ITR	7	04362	J 01334 Q
AF31		MLCA	Y,E	12	04369	D 01856 01812 I
AF32		MLCA	Z,F	12	04381	D 01867 01823 I
AF33		MLCA	CAL,G	12	04393	D 01451 01834 I

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 18

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRES	INSTRUCTION
AF35		C	E,Y	11	04405	C 01812 01856
AF36		BU	AT3	7	04416	J 04466 /
AF37		C	F,Z	11	04423	C 01823 01867
AF38		BU	AT3	7	04434	J 04466 /
AF39		C	CAL,G	11	04441	C 01451 01834
AF40		BU	AT3	7	04452	J 04466 /
AF41		B	AT4	7	04459	J 04474
AF42	AT3	B	SE1	7	04466	J 27220
AF43		H		1	04473	.
AF44	*		AN MLC A OR COMPARE INSTRUCTION FAILED.			
AF45	AT4	BCE	AT1,TAD1,1	12	04474	B 04362 01001 1
AF46		B	SC1	7	04486	J 27380
AF47	*ROUTINE 21-MOVE CONSTANT G TO LOCATION 88					
AF48	AU1	BNQ	ITR	7	04493	J 01334 Q
AF49		MLCWA	G,88	12	04500	D 01834 01889 X
AF50		C	G,88	11	04512	C 01834 01889
AF51		BE	AU2	7	04523	J 04538 S
AF52		B	SE1	7	04530	J 27220
AF53		H		1	04537	.
AF54	*		AFTER MOVING G TO 88, G AND 88 DID NOT COMPARE.			
AF55	AU2	BCE	AU1,TAD1,1	12	04538	B 04493 01001 1
AF56		B	SC1	7	04550	J 27380
AF57	*ROUTINE 22-LOAD INDEX REGISTER ONE TO 11.					
AF58	AU1	BNQ	ITR	7	04557	J 01334 Q
AF59		MLCWA	0000112,X1	12	04564	D 29201 00029 X
AF60		C	0000112,X1	11	04576	C 29201 00029
AF61		BE	AU2	7	04587	J 04602 S
AF62		B	SE1	7	04594	J 27220
AF63		H		1	04601	.
AF64	*		AFTER LOADING INDEX REGISTER ONE WITH THE CONSTANT			
AF65	*		11, INDEX REGISTER ONE DID NOT COMPARE WITH THE			
AF66	*		CONSTANT 11.			
AF67	AU2	BCE	AU1,TAD1,1	12	04602	B 04557 01001 1
AF68		B	SC1	7	04614	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AF70			*ROUTINE 23-DEPOSIT OCCASIONAL SPECIAL CHARACTERS IN CONSTANT BB.			
AF71	AUU3	BNQ	ITR	7	04621	J 01334 Q
AF72	C	BB-10EX1,CR1&2&X1	COMPARE SPEC CONST WITH BB CHAR	11	0462B	C 01BX9 016Y7
AF73	BU	AUU4	BRANCH-NO DEPOSIT	7	04639	J 046B4 /
AF74	AUU6	MLCS	CR2&2&X1,B8-10&X1	12	04646	D 017+2 018X9 3
AF75	C	8B-10&X1,CR2&2&X1	CHECK MOVE	11	04658	C 01BX9 017+2
AF76	BE	AUU4	BRANCH-MOVE OK	7	04669	J 046B4 S
AF77	B	SEI	BRANCH TO ERROR ROUTINE	7	04676	J 27220
AF78	H		ROUTINE 23 ERROR	1	046B3	.
AF79			AFTER OPERATION OF THE MLCS INSTRUCTION, THE			
AF80			LOCATION MOVED TO DID NOT COMPARE WITH THE DATA			
AF81			MOVEO.			
AF82	AUU4	MLCWA	X1,C0B	12	04684	D 00029 01482 X
AF83	S	&1,X1	REDUCE INDEX REG 1	11	04696	S 29202 00029
AF84	BZ	AUU5	BRANCH-ROUTINE COMPLETE	7	04707	J 04751 V
AF85	A	-1,C0B	CHECK SUBTRACTION	11	04714	A 29203 01482
AF86	C	X1,C0B		11	04725	C 00029 01482
AF87	BE	AUU3	BRANCH-ADD,SUBTRACT OK	7	04736	J 04621 S
AF88	B	SEI	BRANCH TO ERROR ROUTINE	7	04743	J 27220
AF89	H		ROUTINE 23 ERROR	1	04750	.
AF90			AFTER SUBTRACTING A &1 FROM INDEX REG ONE, AND			
AF91			ADDING A -1 TO THE SAME NUMBER IN C0B, INDEX REG ONE			
AF92			AND C0B DID NOT COMPARE.			
AF93	AUU5	MLCWA	2000112,X1	12	04751	D 29201 00029 X
AF94	BCE	AUU3,TAD1,1	LOAD INDEX REG 1 FOR LOOPING	12	04763	B 04621 01001 1
AF95	B	SC1	LOOP ROUTINE 23	7	04775	J 273B0
AF96			STEP ROUTINE COUNTER TO 24			
AF97	AV1	BNQ	ITR	7	04782	J 01334 Q
AF98			BRANCH INQUIRY			
AF99	C	2000002,X2	MOVE ZEROS TO INDEX REG TWO	12	04789	D 29196 00034 X
AG00	BE	AV2	CHECK MOVE	11	04B01	C 00034 29196
AG01	B	SEI	BRANCH-INDEX 2 CLEARED OK	7	04B12	J 04827 S
AG02	H		BRANCH TO ERROR ROUTINE	7	04B19	J 27220
AG03			ROUTINE 24 ERROR	1	04B26	.
AG04			AFTER MOVING ZEROS INTO INDEX REG. ONE, INDEX REG.			
			ONE DID NOT COMPARE WITH AN ALL ZERO CONSTANT.			

PGLIN	LABEL	OPCOD	OPERAND	CT	ADORS	INSTRUCTION
AG06	AV2	8CE	AV1,IA01,1	12	04827	B 04782 01001 1
AG07		8	SC1	7	04839	J 27380
AG08	*ROUTINE 25-SET INDEX REG. 2 EQUAL TO LOW ORDER DIGIT OF PASS COUNT					
AG09	AW1	BNQ	ITR	7	04846	J 01334 Q
AG10		MLNS	CO1,X2	12	04853	D 28538 00034 1
AG11		MLCS	CO1,AW2&11	12	04865	D 28538 04888 3
AG12	AW2	8CE	AW3,X2,0	12	04877	B 04897 00034 0
AG13		8	SE1	7	04889	J 27220
AG14		H		1	04896	.
AG15	* INDEX REG. 2 FAILED TO SET TO PROPER NUMBER.					
AG16	AW3	8CE	AW1,IA01,1	12	04897	B 04846 01001 1
AG17		8	SC1	7	04909	J 27380
AG18	*ROUTINE 26-SET LENGTH OF BB TO 1 TO 10 DIGITS WITH A WORD MARK.					
AG19	* THE LENGTH OF BB WILL DECREASE ONE DIGIT EACH PASS.					
AG20	AX1	8NQ	ITR	7	04916	J 01334 Q
AG21		SW	8B-9&X2	6	04923	. 01800
AG22		SCNLA	88,1011	12	04929	D 01889 01011 B
AG23		S8R	CO25	7	04941	G 01472 8
AG24		A	-1011,CO25	11	04948	A 29207 01472
AG25		MLZS	@ @,CO25	12	04959	D 29208 01472 2
AG26		C	CO25,@00010@	11	04971	C 01472 29213
AG27		8L	AX2	7	04982	J 05014 T
AG28		C	CO25,@00001@	11	04989	C 01472 29218
AG29		8H	AX2	7	05000	J 05014 U
AG30		B	AX3	7	05007	J 05022
AG31	AX2	8	SE1	7	05014	J 27220
AG32		H		1	05021	.
AG33	* WORD MARK WAS NOT SET PROPERLY OR SCNLA INSTRUCTION					
AG34	* FAILED. IF THIS IS A WORD MARK FAILURE, FOLLOWING					
AG35	* ROUTINES MAY GIVE ERRONEOUS ERROR INDICATIONS OR					
AG36	* LOSE CONTROL.					
AG37	AX3	8CE	AX1,IA01,1	12	05022	B 04916 01001 1
AG38		8	SC1	7	05034	J 27380

1410/7010 CPU RELIABILITY TEST-4DK & UP

CU01 PAGE 21

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRES	INSTRUCTION
AG40			*ROUTINE 27-MOVE CONSTANT AA TO LOCATION CC.			
AG41	BA1	BNQ	ITR	7	05041	J 01334 Q
AG42		MLCWA	AA,CC	12	05048	D 01878 01900 X
AG43		C	AA,CC	11	05060	C 01878 01900
AG44		BE	BA2	7	05071	J 05086 S
AG45		B	SE1	7	05078	J 27220
AG46		H		1	05085	.
AG47			ROUTINE 27 ERROR			
AG48			AFTER MOVING CONSTANT AA TO LOCATION CC, AA AND CC DID NOT COMPARE.			
AG49	BA2	8CE	8A1,IAD1,1	12	05086	B 05041 01001 1
AG50		B	SC1	7	05098	J 27380
AG51			*ROUTINE 28-MOVE CONSTANT BB TO LOCATION DD.			
AG52	BB1	BNQ	ITR	7	05105	J 01334 Q
AG53		MLCWA	BB,DD	12	05112	D 01889 01911 X
AG54		C	BB,DD	11	05124	C 01889 01911
AG55		8E	BB2	7	05135	J 05150 S
AG56		B	SE1	7	05142	J 27220
AG57		H		1	05149	.
AG58			ROUTINE 28 ERROR			
AG59			AFTER MOVING CONSTANT BB TO LOCATION DD, BB AND DD DID NOT COMPARE.			
AG60	BB2	8CE	BB1,IAD1,1	12	05150	B 05105 01001 1
AG61		B	SC1	7	05162	J 27380
AG62			*ROUTINE 29-STORE THREE CHARACTERS OF ZONE CONSTANT.			
AG63	BC1	BNQ	ITR	7	05169	J 01334 Q
AG64		MRZG	CP1644,CP5	12	05176	D 01552 01580 .
AG65		MLZB	CP1646,CP6	12	05188	D 01554 01586 K
AG66		C	CP582,CP6	11	05200	C 01582 01586
AG67		BE	BC2	7	05211	J 05226 S
AG68		B	SE1	7	05218	J 27220
AG69		H		1	05225	.
AG70			ROUTINE 29 ERROR			
AG71			AFTER USING TWO DIFFERENT MOVE INSTRUCTIONS TO MOVE THE SAME THREE CHARACTER FIELD TO TWO DIFFERENT LOCATIONS, THE TWO LOCATIONS DID NOT COMPARE.			
AG72						
AG73	BC2	8CE	BC1,IAD1,1	12	05226	B 05169 01001 1
AG74		B	SC1	7	05238	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AG76			*ROUTINE 30-CYCLE REMAINDER OF ZONE CONSTANT THREE POSITIONS.			
AG77	BD1	BNQ	ITR	7	05245	J 01334 Q
AG78		MLZA	CP1&43,CP1&46	12	05252	D 01551 01554 S
AG79		MLZS	CP1,802&11	12	05264	O 01508 05287 2
AG80	BD2	BCE	803,CP1&3,	12	05276	B 05296 01511
AG81		B	SE1	7	05288	J 27220
AG82		H		1	05295	.
AG83			ROUTINE 30 ERROR			
AG84			AFTER MAKING THE RIGHT TO LEFT MOVE, THE FAILURE OF			
AG85			THE BCE INSTRUCTION TO BRANCH INDICATES THAT THE			
AG86			HIGH ORDER CHARACTER WAS NOT PROPERLY MOVED.			
AG87			NOTE-IF THIS ROUTINE IS LOOPEO, THE DATA WILL VARY			
			EVERY PASS OF THE ROUTINE.			
AG88	BD3	BCE	BD1,TAD1,1	12	05296	B 05245 01001 1
AG89		B	SC1	7	05308	J 27380
AG90			*ROUTINE 31-RELOCATE THREE CHARACTERS OF ZONE CONSTANT.			
AG91	BE1	BNQ	ITR	7	05315	J 01334 Q
AG92		MLZS	CP5&2,CP1	12	05322	D 01582 01508 2
AG93		MLZS	CP5&2,8E2&11	12	05334	O 01582 05405 2
AG94		MLZS	CP5&1,CP1&1	12	05346	D 01581 01509 2
AG95		MLZS	CP5&1,8E3&11	12	05358	O 01581 05424 2
AG96		MLZS	CP5,CP1&2	12	05370	O 01580 01510 2
AG97		MLZS	CP5,8E4&11	12	05382	O 01580 05443 2
AG98	BE2	BCE	BE3,CP1,	12	05394	B 05413 01508
AG99		B	BE5	7	05406	J 05444
AH00	BE3	BCE	BE4,CP1&1,	12	05413	B 05432 01509
AH01		B	BE5	7	05425	J 05444
AH02	BE4	BCE	BE6,CP1&2,	12	05432	B 05452 01510
AH03	BE5	B	SE1	7	05444	J 27220
AH04		H		1	05451	.
AH05			ROUTINE 31 ERROR			
AH06			THE FAILURE OF ONE OF THE BCE INSTRUCTIONS TO BRANCH			
AH07			INDICATES THAT AT LEAST ONE OF THE MOVE INSTRUCTIONS			
			FAILED.			
AH08	BE6	BCE	BE1,TAD1,1	12	05452	B 05315 01001 1
AH09		B	SC1	7	05464	J 27380
			LOOP ROUTINE 31			
			STEP ROUTINE COUNTER TO 32			

PGLIN

OPCOO OPERANO

CT

ADORS

PAGE 23

*ROUTINE 32--MOVE ZONE CONSTANT TO LOCATION CC TO FORM CONSTANT CC.

AH11	BF1	BNQ	ITR	BRANCH INQUIRY	7	05471	J	01334	Q
AH12		MLNWA	CC,CA1	STORE CC FOR CHECK	12	05478	O	01900	01451 V
AH13		MLZB	CPI11,CC	MAKE MOVE	12	05490	D	01519	01900 K
AH14		MLZB	CPI11,CA1	MOVE SAME ZONE FOR CHECKING	12	05502	O	01519	01451 K
AH15		C	CC,CA1	CHECK MOVES	11	05514	C	01900	01451
AH16		BE	HF2	BRANCH--MOVES OK	7	05525	J	05540	S
AH17		B	SE1	BRANCH TO ERROR ROUTINE	7	05532	J	27220	
AH18		H		ROUTINE 32 ERROR	1	05539	.		

* AFTER MOVING THE SAME DATA TO LOCATION CA1 THAT WAS
MOVED TO LOCATION CC, CA1 AND CC DID NOT COMPARE.

AH20	BF2	BCE	BF1,TAD1,1	LOOP ROUTINE 32	12	05540	B	05471	01001 I
AH21		B	SC1	STEP ROUTINE COUNTER TO 33	7	05552	J	27380	

*ROUTINE 33--MOVE ZONE CONSTANT TO LOCATION OD TO FORM CONSTANT OD.

AH22	BG1	BNQ	ITR	BRANCH INQUIRY	7	05559	J	01334	Q
AH23		MLNWA	OD,CA1	STORE OD FOR CHECK	12	05566	O	01911	01451 V
AH24		MLZB	CPI120,OD	MAKE MOVE	12	05578	O	01528	01911 K
AH25		MLZB	CPI120,CA1	MOVE SAME ZONE FOR CHECKING	12	05590	D	01528	01451 K
AH26		C	OD,CA1	CHECK MOVES	11	05602	C	01911	01451
AH27		BE	HG2	BRANCH--MOVES OK	7	05613	J	05628	S
AH28		B	SE1	BRANCH TO ERROR ROUTINE	7	05620	J	27220	
AH29		H		ROUTINE 33 ERROR	1	05627	.		

* AFTER MOVING THE SAME DATA TO LOCATION CA1 THAT WAS
MOVED TO LOCATION OD, CA1 AND OD DID NOT COMPARE.

AH30	BG2	BCE	BG1,TA01,1	LOOP ROUTINE 33	12	05628	B	05559	01001 I
AH31		B	SC1	STEP ROUTINE COUNTER TO 34	7	05640	J	27380	

STEP ROUTINE COUNTER TO 34

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
AH38			*ROUTINE 34-EXTRACT ADDRESS FROM CONSTANT A FOR FORMATION OF			
AH39	*		CONSTANT EE.			
AH40	BH1	BNQ	ITR	7	05647	J 01334 Q
AH41		MLCB	A-1,C08	12	05654	D 01789 01482 L
AH42		MLCB	A-1,C09	12	05666	O 01789 01487 L
AH43		C	C08,C09	11	05678	C 01482 01487
AH44		BE	BH2	7	05689	J 05704 S
AH45		B	SE1	7	05696	J 27220
AH46		H		1	05703	.
AH47	*		ROUTINE 34 ERROR			
AH48	*		AFTER MOVING THE SAME DATA TO LOCATIONS C08 AND C09,			
AH49		BCE	BH1,TAD1,1	12	05704	B 05647 01001 I
AH50		B	SC1	7	05716	J 27380
AH51			LOOP ROUTINE 34			
AH52			STEP ROUTINE COUNTER TO 35			
AH53			*ROUTINE 35-ADD THE LAST ADDRESS OF THIS PROGRAM TO THE CONSTANT IF			
AH54			THE CONSTANT IS LOWER THAN THE LAST ADDRESS.			
AH55	BH1	BNQ	ITR	7	05723	J 01334 Q
AH56	BH2	MLCA	C08,C09	12	05730	D 01482 01487 I
AH57		MLCA	C09,C095	12	05742	D 01487 01492 I
AH58		MLCB	ALASTE5,XLAST	12	05754	D 29160 01622 L
AH59		C	C09,XLAST	11	05766	C 01487 01622
AH60		BL	B15	7	05777	J 05818 I
AH61		A	XLAST,C09	11	05784	A 01622 01487
AH62		MLCA	C09,C095	12	05795	D 01487 01492 I
AH63		S	XLAST,C095	11	05807	S 01622 01492
AH64		C	C095,C08	11	05818	C 01492 01482
AH65		BE	B16	7	05829	J 05844 S
AH66		B	SE1	7	05836	J 27220
AH67		H		1	05843	.
AH68	*		ROUTINE 35 ERROR			
AH69			C095 AND C08 DID NOT COMPARE AFTER MOVING C08 TO			
AH70			C095 AT B12 AND/OR AFTER ADDING AND SUBTRACTING			
			THE SAME NUMBER FROM C095 AT B13 AND B14.			
BH6		BCE	BH1,TAD1,1	12	05844	B 05723 01001 I
		B	SC1	7	05856	J 27380
			LOOP ROUTINE 35			
			STEP ROUTINE COUNTER TO 36			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 25

PGLIN	LABEL	OPCOD	OPERANO	CT	ADDRS	INSTRUCTION
AH72			*ROUTINE 36-REDUCE CONSTANT 5000 AT A TIME UNTIL CONSTANT IS LOWER			
AH73			THAN THE LAST ADDRESS OF THIS MACHINES MEMORY.			
AH74			MLCS SYS161,CP9-4			
AH75	BJ1	BNQ	ITR	12	05863	D 01257 01592 3
AH76			MLCA C09,C08	7	05875	J 01334 Q
AH77	BJ2	C	C08,CP9	12	05882	D 01487 01482 1
AH78	BJ3	BH	BJ5	11	05894	C 01482 01596
AH79			MLCA C08,C095	7	05905	J 06011 U
AH80		S	£5000,C08	12	05912	D 01482 01492 1
AH81		MLCA	C08,C096	11	05924	S 29222 01482
AH82		A	£5000,C096	12	05935	D 01482 01497 1
AH83		C	C096,C095	11	05947	A 29222 01497
AH84		BE	BJ2	11	05958	C 01497 01492
AH85		BZN	BJ4,C096,	7	05969	J 05894 S
AH86		B	SE1	12	05976	V 06003 01497 2
AH87		H		7	05988	J 27220
AH88			ROUTINE 36 ERROR	1	05995	.
AH89			THE ZONE IN THE SIGN POSITION OF C096 SHOULD REMAIN			
AH90			BLANK. THE BZN INSTRUCTION FAILED, OR C096 BECAME			
AH91			SIGNED. C096 COULD BECOME NEGATIVELY SIGNED IF THE			
AH92			BRANCH AT BJ3 DID NOT OCCUR AFTER THE CONSTANT WAS			
AH93			REDUCED TO A NUMBER SMALLER THAN MEMORY. NOTE-THIS			
AH94			ERROR MAY CAUSE LOSS OF CONTROL OR ERRONEOUS ERROR			
AH95			INDICATIONS IN LATER ROUTINES.			
AH96	BJ4	B	BJ5	7	05996	J 06011
AH97		B	SE1	7	06003	J 27220
AH98		H		1	06010	.
AH99			ROUTINE 36 ERROR			
A100			THE RESULT OF ADDING 5000 TO THE CONSTANT AND			
A101			SUBTRACTING 5000 FROM THE SUM, O10 NOT COMPARE WITH			
A102			THE ORIGINAL CONSTANT. NOTE-THIS ERROR MAY CAUSE			
A103			LOSS OF CONTROL OR ERRONEOUS ERROR INDICATIONS IN			
A104			LAYER ROUTINES.			
	BCE		BJ1,TAD1,1	12	06011	B 05875 01001 1
	B		SCI	7	06023	J 27380
			LOOP ROUTINE 36			
			STEP ROUTINE COUNTER TO 37			

*ROUTINE 37-ENSURE THAT CONSTANT IS AT LEAST 150 HIGHER THAN LAST

* ADDRESS OF PROGRAM.

AI06	BK1	BNQ	ITR	BRANCH INQUIRY	7	06030	J	01334	Q
AI07		MLCA	0001503,C09	SET UP CHECK	12	06037	D	29227	01487
AI08		A	XLAST,C09		11	06049	A	01622	01487
AI09		MLCA	C08,C095	SAVE CONSTANT IN C08	12	06060	D	01482	01492
AI10		C	C095,C09	IS CONSTANT 150 HIGHER	11	06072	C	01492	01487
AI11		BL	BK2	BRANCH-YES IT IS	7	06083	J	06150	T
AI12		A	0150,C095	INCREASE CONSTANT	11	06090	A	29230	01492
AI13		MLCA	C095,C096	SAVE SUM IN C095	12	06101	D	01492	01497
AI14		S	0150,C096	CHECK ADDITION	11	06113	S	29230	01497
AI15		C	C096,C08		11	06124	C	01497	01482
AI16		RE	BK2	BRANCH-ADD, SUB. OK	7	06135	J	06150	S
AI17		R	SE1	BRANCH TO ERROR ROUTINE	7	06142	J	27220	
AI18		H		ROUTINE 37 ERROR	1	06149	.		

* THE RESULT OF ADDING 150 TO THE CONSTANT AND
 * SUBTRACTING 150 FROM THE SUM DID NOT COMPARE WITH
 * THE ORIGINAL CONSTANT.

AI24	BK2	HCE	BK1,TAD1,I	LOOP ROUTINE 37	12	06150	B	06030	01001
AI25		B	SC1	STEP ROUTINE COUNTER TO 38	7	06162	J	27380	

*ROUTINE 38-ENSURE THAT CONSTANT IS AT LEAST 23 LOWER THAN LAST

* ADDRESS OF MEMORY.

AI26		BNQ	ITR	BRANCH INQUIRY	7	06169	J	01334	Q
AI27		MLCA	-00023,C09	SET UP CHECK	12	06176	D	29235	01487
AI28		A	CP9,C09	ADD LAST ADDRESS OF MEMORY	11	06188	A	01596	01487
AI29		MLCA	C095,C08	SAVE CONSTANT IN C095	12	06199	D	01492	01482
AI30		C	C09,C08	IS CONSTANT 23 LOWER	11	06211	C	01487	01482
AI31		BL	BL2	BRANCH-YES IT IS	7	06222	J	06289	T
AI32		S	0000232,C08	REDUCE	11	06229	S	29240	01482
AI33		MLCA	C08,C096	SAVE RESULT IN C08	12	06240	D	01482	01497

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 27

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AI37	*	A	2000230,C096	11	06252	A 29240 01497
AI38	*	C	C096,C095	11	06263	C 01497 01492
AI39	*	BE	BL2	7	06274	J 06289 S
AI40	*	B	SE1	7	06281	J 27220
AI41	*	H		1	06288	.
AI42	*		THE RESULT OF SUBTRACTING 23 FROM THE CONSTANT AND			
AI43	*		ADDING 23 TO THE DIFFERENCE DID NOT COMPARE WITH THE			
AI44	*		ORIGINAL CONSTANT.			
AI45	BL2	8CE	BL1,TAD1,1	12	06289	B 06169 01001 1
AI46	*	B	SC1	7	06301	J 27380
AI47	*		ROUTINE 39-STORE CONSTANT EE.			
AI48	BM1	BNQ	ITR	7	06308	J 01334 Q
AI49		MLCA	C08,EE	12	06315	D 01482 01916 Y
AI50		C	C08,EE	11	06327	C 01482 01916
AI51		BE	BM2	7	06338	J 06353 S
AI52		B	SE1	7	06345	J 27220
AI53	*	H		1	06352	.
AI54	*		AFTER MOVING C08 TO EE, C08 AND EE DID NOT COMPARE.			
AI55	BM2	8CE	BM1,TAD1,1	12	06353	B 06308 01001 1
AI56	*	B	SC1	7	06365	J 27380
AI57	*		ROUTINE 40-EXTRACT 5 DIGIT CONSTANT FROM CONSTANT B FOR FORMING			
AI58	*		CONSTANT FF.			
AI59	BN1	BNQ	ITR	7	06372	J 01334 Q
AI60		MLCB	8-4,C08	12	06379	D 01797 01482 L
AI61		MLCB	H-4,C09	12	06391	D 01797 01487 L
AI62		C	C08,C09	11	06403	C 01482 01487
AI63		BE	BN2	7	06414	J 06429 S
AI64		B	SE1	7	06421	J 27220
AI65	*	H		1	06428	.
AI66	*		AFTER USING TWO MOVE INSTRUCTIONS TO MOVE THE SAME			
AI67	*		DATA TO LOCATIONS C08 AND C09, C08 AND C09 DID NOT			
AI68	*		COMPARE.			
AI69	BN2	BCE	BN1,TAD1,1	12	06429	B 06372 01001 1
AI70		B	SC1	7	06441	J 27380

PGLIN	LABEL	UPCOD	OPERAND	CT	ADDRS	INSTRUCTION
A172	*ROUTINE 41-IF THE CONSTANT IS EQUAL TO OR LOWER THAN THE LAST					
A173	* ADDRESS OF THIS PROGRAM PLUS 50, ADD THE LAST ADDRESS					
A174	* PLUS 50 TO THE CONSTANT.					
A175	B01	BNQ	ITR	7	06448	J 01334 Q
A176		MLCA	XLAST,C096	12	06455	O 01622 01497 I
A177		A	E50,C096	11	06467	A 29242 01497
A178		MLCA	C096,C097	12	06478	O 01497 01502 I
A179		S	E50,C097	11	06490	S 29242 01502
A180		C	C097,XLAST	11	06501	C 01502 01622
A181		DE	B02	7	06512	J 06527 S
A182		B	SE1	7	06519	J 27220
A183		H		1	06526	.
A184			ROUTINE 41 ERROR			
A185	* THE RESULT OF ADDING 50 TO A CONSTANT AND					
A186	* SUBTRACTING 50 FROM THE SUM DID NOT COMPARE WITH THE					
A187	* ORIGINAL CONSTANT. NOTE-THIS ERROR MAY CAUSE LOSS OF					
A188	* CONTROL OR ERRONEOUS ERROR INDICATIONS IN LATER					
A189	* ROUTINES.					
A190	B02	MLCA	CU8,C09	12	06527	O 01482 01487 I
A191		C	C096,C09	11	06539	C 01497 01487
A192		BH	B03	7	06550	J 06617 U
A193		A	C096,C09	11	06557	A 01497 01487
A194		MLCA	CU9,C095	12	06568	D 01487 01492 I
A195		S	C096,C095	11	06580	S 01497 01492
A196		C	C095,C08	11	06591	C 01492 01482
A197		BE	B03	7	06602	J 06617 S
A198		B	SE1	7	06609	J 27220
A199		H		1	06616	.
AJ00	* THE RESULT OF ADDING CONSTANT 1 TO CONSTANT 2 AND					
AJ01	* SUBTRACTING CONSTANT 1 FROM THE SUM DID NOT COMPARE					
AJ02	* WITH THE ORIGINAL CONSTANT 2. NOTE-THIS ERROR MAY					
AJ03	* CAUSE LOSS OF CONTROL IN LATER ROUTINES.					
AJ04	BCE	801,TAD1,1	LOOP ROUTINE 41	12	06617	8 06448 01001 1
	B	SC1	STEP ROUTINE COUNTER TO 42	7	06629	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 29

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AJ06			*ROUTINE 42-CALCULATE HIGHEST ADDRESS OF MEMORY MINUS 350.			
AJ07	BP1	BNQ	ITR	7	06636	J 01334 Q
AJ08		MLCA	CP9,C08	12	06643	D 01596 01482 T
AJ09		S	E350,C08	11	06655	S 29245 01482
AJ10		MLCA	C08,C095	12	06666	D 01482 01492 T
AJ11		A	E350,C095	11	06678	A 29245 01492
AJ12		C	C095,CP9	11	06689	C 01492 01596
AJ13		BE	BP2	7	06700	J 06715 S
AJ14		B	SE1	7	06707	J 27220
AJ15		H		1	06714	.
AJ16	*		AFTER SUBTRACTING 350 FROM A CONSTANT AND ADDING 350			
AJ17	*		TO THE DIFFERENCE, THE RESULT DID NOT COMPARE WITH			
AJ18	*		THE ORIGINAL CONSTANT.			
AJ19	BP2	BCE	BP1,IAD1,1	12	06715	B 06636 01001 I
AJ20		B	SC1	7	06727	J 27380

ROUTINE 42 ERROR

LOOP ROUTINE 42

STEP ROUTINE COUNTER TO 43

CT ADDR INSTRUCTION

LABEL

PGLIN

OPCODE OPERAND

*ROUTINE 43-REPEATEDLY SUBTRACT 5000 FROM CONSTANT UNTIL IT IS

* LOWER THAN THE LAST ADDRESS OF MEMORY MINUS 350

BQ1 BQ5 ITR BRANCH INQUIRY

MLCA C09,C095 SAVE CONSTANT IN C09

BQ2 MLCA C095,C096 SAVE CONSTANT IN C095

C C08,C095 IS CONSTANT NOW LOWER

BQ3 BL BQ5 BRANCH-YES IT IS LOWER

S £5000,C095

AJ29 MLCA C095,C097 SAVE RESULT IN C095

AJ30 A £5000,C097 CHECK SUBTRACTION

C C097,C096

AJ31 BE BQ2 BRANCH-ADD,SUBTRACTION OK

AJ32 BZN BQ4,C095, BRANCH-ROUTINE NOT HUNG

AJ33 R SE1 BRANCH TO ERROR ROUTINE

AJ34 H ROUTINE 43 ERROR

* CONSTANT C095 SHOULD REMAIN UNSIGNED. THE FAILURE OF

* THE BZN INSTRUCTION TO BRANCH INDICATES THAT THE BZN

* INSTRUCTION FAILED, OR C095 IS NOW SIGNED. C095

* COULD BECOME NEGATIVELY SIGNED IF THIS ROUTINE HUNG

* IN A LOOP DUE TO THE BL INSTRUCTION AT BQ3 NOT

* BRANCHING WHEN IT SHOULD. NOTE-THIS ERROR COULD

* CAUSE ERRONEOUS ERROR INDICATIONS OR LOSS OF CONTROL

* IN LATER ROUTINES.

B BQ5

BQ4 B SE1 BRANCH TO ERROR ROUTINE

H ROUTINE 43 ERROR

* THE RESULT OF SUBTRACTING 5000 FROM A CONSTANT AND

* ADDING 5000 TO THE DIFFERENCE DID NOT COMPARE WITH-

* THE ORIGINAL CONSTANT.

BCE BQ1,TAD1,1 LOOP ROUTINE 43

B SC1 STEP ROUTINE COUNTER TO 44

12 06870 B 06734 01001 I

7 06882 J 27380

7 06734 J 01334 Q

12 06741 D 01487 01492 I

12 06753 D 01492 01497 I

11 06765 C 01482 01492

7 06776 J 06870 I

11 06783 S 29222 01492

12 06794 D 01492 01502 I

11 06806 A 29222 01502

11 06817 C 01502 01497

7 06828 J 06753 S

12 06835 V 06862 01492 2

7 06847 J 27220

1 06854 .

7 06855 J 06870

7 06862 J 27220

1 06869 .

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AJ54	•	ROUTINE 44-IF CONSTANT IS WITHIN 100 OF CONSTANT EE, ADD 200 TO				
AJ55	•	THE NEW CONSTANT.				
AJ56	BR1	BNQ	1TR	7	06889	J 01334 Q
AJ57		MLCA	C095,C08	12	06896	D 01492 01482 I
AJ58		S	EE,C08	11	06908	S 01916 01482
AJ59		MLCA	C08,C09	12	06919	D 01482 01487 I
AJ60		A	EE,C09	11	06931	A 01916 01487
AJ61		MLZS	2 2,C09	12	06942	D 29208 01487 2
AJ62		C	C09,C095	11	06954	C 01487 01492
AJ63		BE	BR2	7	06965	J 06980 S
AJ64		B	SE1	7	06972	J 27220
AJ65		H		1	06979	•
AJ66	•		THE RESULT OF SUBTRACTING EE FROM THE CONSTANT AND			
AJ67	•		ADDING EE TO THE DIFFERENCE DID NOT COMPARE WITH THE			
AJ68	•		ORIGINAL CONSTANT.			
AJ69	BR2	MLZS	2 2,C08	12	06980	D 29208 01482 2
AJ70	BR3	B8E	BR6,C08,C	12	06992	W 07101 01482 E
AJ71		C	2001002,C08	11	07004	C 29250 01482
AJ72		BH	BR7	7	07015	J 07109 U
AJ73		MLCA	C095,C09	12	07022	D 01492 01487 I
AJ74	BR4	A	2200,C09	11	07034	A 29253 01487
AJ75		MLCA	C09,C08	12	07045	D 01487 01482 I
AJ76	BR5	S	2200,C08	11	07057	S 29253 01482
AJ77		C	C08,C095	11	07068	C 01482 01492
AJ78		BE	BR7	7	07079	J 07109 S
AJ79		B	SE1	7	07086	J 27220
AJ80		H		1	07093	•
AJ81	•		THE RESULT OF ADDING 200 TO THE CONSTANT AT BR4 AND			
AJ82	•		SUBTRACTING 200 FROM THE SUM AT BR5 DID NOT COMPARE			
AJ83	•		WITH THE ORIGINAL CONSTANT.			
AJ84	B	BR7		7	07094	J 07109

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCCD	OPERAND	CT	ADDRS	INSTRUCTION
AJ86	BR6	B	SE1	7	07101	J 27220
AJ87	H			1	07108	.
AJ88	*					ROUTINE 44 ERROR
AJ89	*					BRANCH TO ERROR ROUTINE
AJ90	*					THE BRANCH BIT EQUAL INSTRUCTION AT BR3 BRANCHED TO
AJ91	*					THIS ERROR HALT. THIS INDICATES THAT THE MOVE
AJ92	*					INSTRUCTION AT BR2 DID NOT CLEAR THE ZONE OF
AJ93	*					CONSTANT C08. NOTE-THIS ERROR MAY CAUSE ERRONEOUS
AJ94	*					ERROR INDICATIONS IN LATER ROUTINES.
AJ95	BR7	BCE	BR1,IAD1,1	12	07109	B 06889 01001 1
AJ96	B	SC1		7	07121	J 27380
AJ97	*ROUTINE 45-STORE CONSTANT FF..					
AJ98	BS1	BNQ	ITR	7	07128	J 01334 Q
AJ99	MLCA	C09,FF		12	07135	D 01487 01921 T
AK00	C	C09,FF		11	07147	C 01487 01921
AK01	BE	BS2		7	07158	J 07173 S
AK02	B	SE1		7	07165	J 27220
AK03	H			1	07172	.
AK04	*					ROUTINE 45 ERROR
AK05	*					AFTER MOVING CONSTANT C09 TO LOCATION FF, C09 AND
AK06	BS2	BCE	BS1,IAD1,1	12	07173	B 07128 01001 1
AK07	B	SC1		7	07185	J 27380
AK08						LOOP ROUTINE 45
AK09						STEP ROUTINE COUNTER TO 46

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AK07	*ROUTINE 46-CHECK SCNLS, SAR, SBR INSTRUCTIONS.					
AK08	BT1	BNQ	ITR	7	07192	J 01334 Q
AK09		MLCA	EE,BT2&10	12	07199	D 01916 07221 T
AK10	BT2	SCNLS	AA,0	12	07211	D 01878 00000
AK11		SAR	BT3-1	7	07223	G 07270 A
AK12		SBR	CO9	7	07230	G 01487 B
AK13		MLZS	@ @,CO9	12	07237	D 29208 01487 2
AK14		A	&1,CO9	11	07249	A 29202 01487
AK15		C	AA-1,0	11	07260	C 01877 00000
AK16	BT3	BE	BT4	7	07271	J 07286 S
AK17		B	SE1	7	07278	J 27220
AK18		H		1	07285	.
AK19	*		IF THE SCNLS INSTRUCTION AT BT2 REDUCED THE AAR BY			
AK20	*		ONE AS IT SHOULD, THE BE INSTRUCTION AT BT3 SHOULD			
AK21	*		HAVE BRANCHED.			
AK22	BT4	C	CO9,EE	11	07286	C 01487 01916
AK23	BT5	BE	BT6	7	07297	J 07312 S
AK24		R	SE1	7	07304	J 27220
AK25		H		1	07311	.
AK26	*		IF THE SCNLS INSTRUCTION AT BT2 REDUCED THE BAR BY			
AK27	*		ONE AS IT SHOULD, THE BE INSTRUCTION AT BT5 SHOULD			
AK28	*		HAVE BRANCHED.			
AK29	BT6	RCE	BT1,TAD1,1	12	07312	R 07192 01001 1
AK30		B	SC1	7	07324	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 34

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AK32			*ROUTINE 47-LOAD INDEX REG 5 WITH CONSTANT EE AND INDEX REG 6 WITH			
AK33			CONSTANT FF.			
AK34	BU1	BNQ	ITR	7	07331	J 01334 Q
AK35		MLCWA	EE,X5	12	07338	D 01916 00049 X
AK36		MLCWA	FF,X6	12	07350	D 01921 00054 X
AK37		C	EE,X5	11	07362	C 01916 00049
AK38		BE	BU2	7	07373	J 07388 S
AK39		B	SE1	7	07380	J 27220
AK40		H		1	07387	.
AK41			ROUTINE 47 ERROR			
AK42			AFTER USING AN MLCWA INSTRUCTION TO MOVE CONSTANT EE			
AK43			TO INDEX REG. 5, EE AND INDEX REG. 5 DID NOT COMPARE			
AK44	BU2	C	FF,X6	11	07388	C 01921 00054
AK45		BE	BU3	7	07399	J 07414 S
AK46		B	SE1	7	07406	J 27220
AK47		H		1	07413	.
AK48			ROUTINE 47 ERROR			
AK49			AFTER USING AN MLCWA INSTRUCTION TO MOVE CONSTANT FF			
AK50			TO INDEX REG. 6, FF AND INDEX REG. 6 DID NOT COMPARE			
AK51	BU3	BCE	BU1,IAD1,1	12	07414	H 07331 01001 I
AK52		B	SC1	7	07426	J 27380
AK53			LOOP ROUTINE 47			
AK54			STEP ROUTINE COUNTER TO 48			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 35

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AK52	*ROUTINE 48-CHECK MLWA, MLZA, MLNA, MLCWA INSTRUCTIONS USING					
AK53	*		INDEXING FOR B ADDRESSES.			
AK54	BV1	HNQ	ITR	7	07433	J 01334 Q
AK55		MLWA	CC,0EX5	12	07440	D 01900 00#0 U
AK56		MLZA	CC,0EX5	12	07452	D 01900 00#0 S
AK57		MLNA	CC,0EX5	12	07464	D 01900 00#0 /
AK58		MLCWA	CC,0EX6	12	07476	D 01900 00#0 X
AK59		MLCA	EE,BV2E5	12	07488	D 01916 07517 T
AK60		MLCA	FF,BV2E10	12	07500	D 01921 07522 T
AK61	BV2	C	0,0	11	07512	C 00000 00000
AK62		BE	BV3	7	07523	J 07538 S
AK63		B	SE1	7	07530	J 27220
AK64		H		1	07537	.
AK65	*		USING INDEXING, CONSTANT CC WAS MOVED TO LOCATION EE			
AK66	*		BY THREE DIFFERENT MOVE INSTRUCTIONS.-MLWA,MLZA, AND			
AK67	*		MLNA. USING INDEXING, CONSTANT CC WAS MOVED TO			
AK68	*		LOCATION FF BY AN MLCWA INSTRUCTION. AFTER			
AK69	*		COMPLETION OF THESE MOVES. LOCATION EE AND LOCATION			
AK70	*		FF DID NOT COMPARE.			
AK71	BV3	BCE	BV1,TAD1,1	12	07538	B 07433 01001 I
AK72		B	SC1	7	07550	J 27380
AK73	*ROUTINE 49-CHECK MLNWA, MLZB INSTRUCTIONS USING INDEXING FOR B					
AK74	*		MOVE ADDRESSES AND A COMPARE ADDRESS.			
AK75	BW1	BNQ	ITR	7	07557	J 01334 Q
AK76		MLNWA	DD,0EX5	12	07564	D 01911 00#0 V
AK77		MLZB	DD,0EX5	12	07576	D 01911 00#0 K
AK78		C	0EX5,DD	11	07588	C 00#0 01911
AK79		BE	BW2	7	07599	J 07614 S
AK80		B	SE1	7	07606	J 27220
AK81		H		1	07613	.
AK82	*		CONSTANT DD WAS MOVED TO LOCATION EE BY MLNWA AND			
AK83	*		MLZB INSTRUCTIONS.DD AND EE DO NOT COMPARE.			
AK84	BW2	BCE	BW1,TAD1,1	12	07614	B 07557 01001 I
AK85		B	SC1	7	07626	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADRS	INSTRUCTION
AK87			*ROUTINE 50-CHECK MLZWA INSTRUCTION.			
AK88	BX1	BNQ	ITR	7	07633	J 01334 Q
AK89		MLZWA	0&X5,0&X6	12	07640	D 00*0 00*0 W
AK90		MLNA	00,0&X6	12	07652	D 01911 00*0 /
AK91		C	0&X6,0&X5	11	07664	C 00*0 00*0
AK92		BE	BX2	7	07675	J 07690 S
AK93		R	SE1	7	07682	J 27220
AK94		H		1	07689	.
AK95			THE ZONE AND WORD MARK OF CONSTANT 00 WAS MOVED FROM			
AK96			LOCATION EE TO LOCATION FF. THE NUMERIC OF CONSTANT			
AK97			00 WAS MOVED FROM LOCATION EE TO LOCATION FF.			
AK98			LOCATION FF DID NOT COMPARE WITH LOCATION EE.			
AK99	BX2	BCE	BX1,TA01,1	12	07690	B 07633 01001 1
AL00		B	SC1	7	07702	J 27380
AL01			*ROUTINE 51-CHECK MLNS, MLZS MLCS INSTRUCTIONS. CHECK BCE			
AL02			INSTRUCTION FOR BRANCHING WHEN CHARACTER IS EQUAL.			
AL03	BY1	BNQ	ITR	7	07709	J 01334 Q
AL04		MLNS	CC,0&X6	12	07716	D 01900 00*0 1
AL05		MLZS	CC,0&X6	12	07728	D 01900 00*0 2
AL06		MLCS	CC,BY2&11	12	07740	D 01900 07763 3
AL07	BY2	BCE	BY3,0&X6,0	12	07752	B 07772 00*0 0
AL08		B	SE1	7	07764	J 27220
AL09		H		1	07771	.
AL10			MLNS AND MLZS INSTRUCTIONS WERE USED TO MOVE ONE			
AL11			CHARACTER OF CONSTANT CC TO LOCATION FF. AN MLCS			
AL12			INSTRUCTION WAS USED TO MOVE THE SAME CHARACTER TO			
AL13			THE 0 MODIFIER POSITION OF THE BCE INSTRUCTION. THE			
AL14			BCE INSTRUCTION DID NOT BRANCH.			
AL15	BY3	BCE	BY1,TA01,1	12	07772	B 07709 01001 1
AL16		B	SC1	7	07784	J 27380
			STEP ROUTINE 51			
			STEP ROUTINE COUNTER TO 52			

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADORS	INSTRUCTION
AL18	*ROUTINE 52-CHECK SCNLA, MLWS, 8W INSTRUCTIONS.					
AL19	BZ1	8NQ	ITR	7	07791	J 01334 Q
AL20		SCNLA	0EX5,00	12	07798	O 00#0 01911 B
AL21		SAR	C08	7	07810	G 01482 A
AL22		A	61,C08	11	07817	A 29202 01482
AL23		MLNA	C08,BZ2E5	12	07828	O 01482 07845 /
AL24	BZ2	MLWS	0,0EX6	12	07840	D 00000 00#0 4
AL25	BZ3	8W	BZ4,0EX6	12	07852	V 07872 00#0 1
AL26		B	SEI	7	07864	J 27220
AL27		H		1	07871	.
AL28	*		AN SCNLA INSTRUCTION WAS USED TO FIND THE ADDRESS OF			
AL29	*		THE WORD MARK IN LOCATION EE. THIS ADDRESS WAS			
AL30	*		STORED IN THE A FIELD OF THE MLWS INSTRUCTION. THE			
AL31	*		MLWS INSTRUCTION SHOULD HAVE MOVED THE WORD MARK TO			
AL32	*		LOCATION FF. THE BRANCH ON WORD MARK INSTRUCTION AT			
AL33	*		BZ3 DID NOT BRANCH.			
AL34	BZ4	BCE	BZ1,TA01,1	12	07872	B 07791 01001 1
AL35		B	SC1	7	07884	J 27380
AL36	*ROUTINE 53-CHECK MLCWS, MLNWS INSTRUCTIONS.					
AL37	DA1	BNQ	ITR	7	07891	J 01334 Q
AL38		MLNA	C08,DA2E5	12	07898	O 01482 07939 /
AL39		MLNA	C08,DA3E5	12	07910	O 01482 07951 /
AL40		MLNA	C08,DA4E5	12	07922	O 01482 07963 /
AL41	DA2	MLCWS	0,1EX6	12	07934	D 00000 00#1 7
AL42	DA3	MLNWS	0,2EX6	12	07946	D 00000 00#2 5
AL43	DA4	MLZS	0,2EX6	12	07958	O 00000 00#2 2
AL44		C	1EX6,2EX6	11	07970	C 00#1 00#2
AL45		BE	DA5	7	07981	J 07996 S
AL46		B	SEI	7	07988	J 27220
AL47		H		1	07995	.
AL48	*		AFTER USING AN MLCWS INSTRUCTION TO MOVE A CHARACTER			
AL49	*		AND WORD MARK TO ONE LOCATION AND MLNWS AND MLZS			
AL50	*		INSTRUCTIONS TO MOVE THE SAME CHARACTER TO A			
AL51	*		DIFFERENT LOCATION, THE LOCATIONS DO NOT COMPARE			
AL52	OAS	8CE	DA1,TA01,1	12	07996	B 07891 01001 1
AL53		B	SC1	7	08008	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AL55		*ROUTINE 54-CHECK MLZWS, BZN INSTRUCTIONS.				
AL56	DH1	BNQ	ITR	7	08015	J 01334 Q
AL57		MLNA	C08,DB2E5	12	08022	D 01482 08051 /
AL58		MLNA	C08,DB4E10	12	08034	D 01482 08100 /
AL59	D82	MLZWS	0,3EX6	12	08046	D 00000 00+.3 6
AL60		BW	DB3,3EX6	12	08058	V 08078 00+.3 1
AL61		B	SE1	7	08070	J 27220
AL62		H		1	08077	.
AL63	*		THE MLZWS INSTRUCTION SHOULD HAVE MOVED A WORD MARK			
AL64	*		TO FF PLUS 3. HOWEVER, THE BW INSTRUCTION DID NOT			
AL65	*		BRANCH ON WORD MARK AT FF PLUS 3.			
AL66	D83	MLZS	3EX6,DB4E11	12	08078	D 00+.3 08101 2
AL67	D84	BZN	DB5,0,2	12	08090	V 08110 00000 2
AL68		B	SE1	7	08102	J 27220
AL69		H		1	08109	.
AL70	*		THE MLZWS INSTRUCTION AT DB2 SHOULD HAVE MOVED A			
AL71	*		ZONE TO FF PLUS 3. THE MLZS INSTRUCTION AT DB3			
AL72	*		SHOULD HAVE MOVED THE ZONE FROM FF PLUS 3 TO THE D			
AL73	*		MODIFIER POSITION OF THE BZN INSTRUCTION. HOWEVER,			
AL74	*		THE BZN INSTRUCTION DID NOT BRANCH.			
AL75	D85	BCE	DB1,IAD1,1	12	08110	B 08015 01001 1
AL76		B	SC1	7	08122	J 27380
AL77		*ROUTINE 55-CHECK SW INSTRUCTION.				
AL78	DC1	BNQ	ITR	7	08129	J 01334 Q
AL79		MLNA	EE,DC2E5	12	08136	D 01916 08153 /
AL80	DC2	SW	0	6	08148	, 00000
AL81		MLNA	EE,DC3E10	12	08154	D 01916 08176 /
AL82	DC3	BW	DC4,0	12	08166	V 08186 00000 1
AL83		B	SE1	7	08178	J 27220
AL84		H		1	08185	.
AL85	*		THE BW INSTRUCTION FAILED TO BRANCH ON A WORD MARK			
AL86	*		SET BY THE SW INSTRUCTION.			
AL87	DC4	BCE	DC1,IAD1,1	12	08186	B 08129 01001 1
AL88		B	SC1	7	08198	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 39

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AL90			*ROUTINE 56-CHECK MLNB, MLCB INSTRUCTIONS.			
AL91	DD1	BNQ	ITR	7	08205	J 01334 Q
AL92		MLNA	EE,DD2&10	12	08212	D 01916 08246 /
AL93		MLNA	FF,DD3&10	12	08224	D 01921 08282 /
AL94	DD2	MLWA	CC,0	12	08236	D 01900 00000 U
AL95		MLNB	CC,0&X5	12	08248	D 01900 00+0 J
AL96		ML7B	CC,0&X5	12	08260	D 01900 00+0 K
AL97	DD3	MLWA	CC,0	12	08272	D 01900 00000 U
AL98		MLCB	CC,0&X6	12	08284	D 01900 00+0 L
AL99		C	0&X6,0&X5	11	08296	C 00+0 00+0
AM00		BE	DD4	7	08307	J 08322 S
AM01		B	SE1	7	08314	J 27220
AM02		H		1	08321	.
AM03	*		MLWA, MLNB AND ML7B INSTRUCTIONS WERE USED TO MOVE			
AM04	*		CONSTANT CC TO LOCATION EE. MLWA AND MLCB			
AM05	*		INSTRUCTIONS WERE USED TO MOVE CONSTANT CC TO			
AM06	*		LOCATION FF. EE AND FF FAILED TO COMPARE.			
AM07	DD4	BCE	DD1,1AD1,1	12	08322	B 08205 01001 I
AM08		B	SC1	7	08334	J 27380

ROUTINE 56 ERROR

LOOP ROUTINE 56

STEP ROUTINE COUNTER TO 57

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AM10			*ROUTINE 57-CHECK SW, MLWB, CW, BW INSTRUCTIONS.			
AM11	DE1	BNQ	ITR	7	08341	J 01334 Q
AM12		MLWA	BB,DE5	12	08348	D 01889 DD+D U
AM13		SW	16X5	6	08360	, 00+1
AM14		MLWB	16X5,DE5	12	08366	D DD+1 00+D M
AM15		SBR	X1	7	08378	G 00D29 B
AM16		CW	16X5	6	08385	D 00+1
AM17	DE2	A	61,X1	11	08391	A 29202 00D29
AM18		BW	DE2,DE5X1	12	08402	V 08391 00D+D 1
AM19		S	61,X1	11	08414	S 29202 00D29
AM20		C	X1,EE	11	08425	C 00D29 01916
AM21		BE	DE3	7	08436	J 08451 S
AM22		B	SE1	7	08443	J 2722D
AM23		H		1	08450	.
AM24	*		THE SW AND MLWB INSTRUCTIONS SHOULD HAVE FILLED THE			
AM25	*		FIELD OF ADDRESS EE WITH WORD MARKS. THE CW INSTRUCT			
AM26	*		SHOULD HAVE CLEARED THE WORD MARK IN THE ADDRESS TO			
AM27	*		THE RIGHT OF ADDRESS EE. THE A AND BW INSTRUCTIONS			
AM28	*		ARE USED TO COUNT THE NUMBER OF SEQUENTIAL WORD			
AM29	*		MARKS FROM LEFT TO RIGHT IN THE EE FIELD. THE RESULT			
AM30	*		SHOULD EQUAL THE CONSTANT EE.			
AM31	DE3	BCE	DE1,TAD1,1	12	08451	B 08341 01001 1
AM32		B	SC1	7	08463	J 2738D

ROUTINE 57 ERROR

THE SW AND MLWB INSTRUCTIONS SHOULD HAVE FILLED THE
FIELD OF ADDRESS EE WITH WORD MARKS. THE CW INSTRUCT
SHOULD HAVE CLEARED THE WORD MARK IN THE ADDRESS TO
THE RIGHT OF ADDRESS EE. THE A AND BW INSTRUCTIONS
ARE USED TO COUNT THE NUMBER OF SEQUENTIAL WORD
MARKS FROM LEFT TO RIGHT IN THE EE FIELD. THE RESULT
SHOULD EQUAL THE CONSTANT EE.

BCE DE1,TAD1,1 LOOP ROUTINE 57
B SC1 STEP ROUTINE COUNTER TO 58

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 41

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
AM34						
	ROUTINE 58-CHECK MLN8, MLZWB INSTRUCTIONS.					
AM35	DF1	8NQ	ITR	7	08470	J 01334 Q
AM36		MLCWA	CQ4,0EX5	12	08477	D 01617 00#0 X
AM37		MLCWA	CQ4,0EX6	12	08489	D 01617 00#0 X
AM38		MLCWA	CC,0EX6	12	08501	O 01900 00#0 X
AM39		MLWA	CC,0EX5	12	08513	D 01900 00#0 U
AM40		SW	0EX6	6	08525	, 00#0
AM41		MLNB	0EX6,0EX5	12	08531	D 00#0 00#0 J
AM42		MLZWB	0EX6,0EX5	12	08543	O 00#0 00#0 O
AM43		C	0EX5,0EX6	11	08555	C 00#0 00#0
AM44		SAR	DF2E5	7	08566	G 08607 A
AM45		SBR	OF2E10	7	08573	G 08612 B
AM46		BE	DF2	7	08580	J 08602 S
AM47		B	SEL	7	08587	J 27220
AM48		H		1	08594	.
AM49						
AM50						
AM51						
AM52						
AM53		B	OF3	7	08595	J 08628
AM54		C	O,0	11	08602	C 00000 00000
AM55	OF2	BE	OF3	7	08613	J 08628 S
AM56		B	SEL	7	08620	J 27220
AM57		H		1	08627	.
AM58						
AM59						
AM60						
AM61	DF3	8CE	DF1,TA01,1	12	08628	B 08470 01001 1
AM62		B	SC1	7	08640	J 27380

ROUTINE 58 ERROR

FIELD EE AND FIELD FF SHOULD BE EQUAL WITH A WORD

MARK IN THE RIGHT HAND POSITIONS. THIS HALT

INDICATES ADDRESSES EE AND FF NUMERIC OR ZONE WERE

NOT EQUAL, OR A MICRO MARK IS NOT PRESENT AT EE OR FF

CHECK REMAINDER OF EE FIELD

BRANCH-OK

BRANCH TO ERROR ROUTINE

ROUTINE 58 ERROR

FIELD EE AND FIELD FF SHOULD BE EQUAL WITH A WORD

MARK IN THE RIGHT HAND POSITIONS. THIS HALT

INDICATES EE-1 010 NOT COMPARE WITH FF-1.

LOOP ROUTINE 58

STEP ROUTINE COUNTER TO 59

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AM64			*ROUTINE 59-CHECK ML28, MLNWB INSTRUCTIONS.			
AM65	DG1	BNQ	ITR	7	08647	J 01334 Q
AM66		MLCWA	CQ4,0EX5	12	08654	D 01617 00#0 X
AM67		MLCWA	CQ4,0EX6	12	08666	D 01617 00#0 X
AM68		MLCWA	CC,0EX6	12	08678	D 01900 00#0 X
AM69		MLWA	CC,0EX5	12	08690	D 01900 00#0 U
AM70		SW	0EX6	6	08702	, 00#0
AM71		MLZB	0EX6,0EX5	12	08708	D 00#0 00#0 K
AM72		MLNWB	0EX6,0EX5	12	08720	D 00#0 00#0 N
AM73		C	0EX5,0EX6	11	08732	C 00#0 00#0
AM74		SAR	DG2E5	7	08743	G 08784 A
AM75		SBR	OG2E10	7	08750	G 08789 B
AM76		HE	DG2	7	08757	J 08779 S
AM77		H	SE1	7	08764	J 27220
AM78		H		1	08771	.
AM79	*		FIELD EE AND FIELD FF SHOULD BE EQUAL WITH A WORD			
AM80	*		MARK IN THE RIGHT HAND POSITIONS. THIS HALT			
AM81	*		INDICATES ADDRESSES EE AND FF NUMERIC OR ZONE WERE			
AM82	*		NOT EQUAL, OR A WORD MARK IS NOT PRESENT AT EE OR FF			
AM83		B	DG3	7	08772	J 08805
AM84	OG2	C	0,0	11	08779	C 00000 00000
AM85		BE	DG3	7	08790	J 08805 S
AM86		B	SE1	7	08797	J 27220
AM87		H		1	08804	.
AM88	*		FIELD EE AND FIELD FF SHOULD BE EQUAL WITH A WORD			
AM89	*		MARK IN THE RIGHT HAND POSITIONS. THIS HALT			
AM90	*		INDICATES EE-1 DID NOT COMPARE WITH FF-1.			
AM91	OG3	BCE	DG1,IA01,1	12	08805	B 08647 01001 1
AM92		B	SC1	7	08817	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AN23			*ROUTINE 61-CHECK SCNLB INSTRUCTION.			
AN24	D11	BNQ	ITR	7	08989	J 01334 Q
AN25		MLCWA	CC,0&X5	12	08996	D 01900 00*+0 X
AN26		SBR	C08	7	09008	G 01482 B
AN27		MLZS	@ @,C08	12	09015	D 29208 01482 2
AN28		SCNLB	0&X6,0&X5	12	09027	D 00*+0 00*+0 -
AN29		SBR	C09	7	09039	G 01487 B
AN30		MLZS	@ @,C09	12	09046	D 29208 01487 2
AN31		C	C08,C09	11	09058	C 01482 01487
AN32		BE	D12	7	09069	J 09091 S
AN33		B	SE1	7	09076	J 27220
AN34		H		1	09083	.
AN35			THE B ADDRESS REGISTER AT THE END OF THE SCNLB			
AN36			INSTRUCTION DID NOT COMPARE WITH THE B ADDRESS			
AN37			REGISTER AT THE END OF THE MLCWA INSTRUCTION.			
AN38		B	D13	7	09084	J 09117
AN39	D12	C	0&X5,CC	11	09091	C 00*+0 01900
AN40		BE	D13	7	09102	J 09117 S
AN41		B	SE1	7	09109	J 27220
AN42		H		1	09116	.
AN43			AFTER THE OPERATION OF THE SCNLB INSTRUCTION, THE			
AN44			CONTENTS OF ADDRESS EE DID NOT COMPARE WITH THE			
AN45			CONSTANT CC THAT WAS MOVED TO ADDRESS EE.			
AN46	D13	BCE	D11,TA01,1	12	09117	H 08989 01001 1
AN47		B	SC1	7	09129	J 27380
AN48			*ROUTINE 62-CALCULATE LEFT HAND ADDRESS -1 OF EE AND FF FIELDS			
AN49			CONTAINING CONSTANTS CC AND DD RESPECTIVELY.			
AN50	DJ1	BNQ	ITR	7	09136	J 01334 Q
AN51		MLCWA	CC,0&X5	12	09143	D 01900 00*+0 X
AN52		SBR	CQ1	7	09155	G 01601 B
AN53		MLCWA	DD,0&X6	12	09162	D 01911 00*+0 X
AN54		SBR	CQ2	7	09174	G 01606 B
AN55	DJ2	BCE	DJ1,TA01,1	12	09181	B 09136 01001 1
AN56		B	SC1	7	09193	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 45

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AN58			*ROUTINE 63-CHECK SCNL INSTRUCTION.			
AN59	DK1	BNQ	ITR	7	09200	J 01334 Q
AN60		SCNL	0&X5,0&X6	12	09207	D 00*0 00*0 0
AN61		SAR	CO8	7	09219	G 01482 A
AN62		SBR	CO9	7	09226	G 01487 B
AN63		C	CO2,CO25	11	09233	C 01467 01472
AN64		BL	DK2	7	09244	J 09284 T
AN65		C	CQ1,CO8	11	09251	C 01601 01482
AN66		BE	DK3	7	09262	J 09310 S
AN67		B	SE1	7	09269	J 27220
AN68		H		1	09276	.
AN69	*		AFTER THE OPERATION OF THE SCNL INSTRUCTION, THE			
AN70	*		CONTENTS OF THE A ADDRESS REG DID NOT COMPARE WITH			
AN71	*		THE LEFT ADDRESS -1 OF THE EE FIELD AS CALCULATED IN			
AN72	*		THE LAST ROUTINE.			
AN73		B	DK3	7	09277	J 09310
AN74	DK2	C	CQ2,CO9	11	09284	C 01606 01487
AN75		BE	DK3	7	09295	J 09310 S
AN76		B	SE1	7	09302	J 27220
AN77		H		1	09309	.
AN78	*		AFTER THE OPERATION OF THE SCNL INSTRUCTION, THE			
AN79	*		CONTENTS OF THE B ADDRESS REG DID NOT COMPARE WITH			
AN80	*		THE LEFT ADDRESS -1 OF THE FF FIELD AS CALCULATED IN			
AN81	*		THE LAST ROUTINE.			
AN82	DK3	BCE	DK1,TAD1,1	12	09310	B 09200 01001 1
AN83		B	SC1	7	09322	J 27380
			LOOP ROUTINE 63			
			STEP ROUTINE COUNTER TO 64			

PGLIN	LABEL	OPCCD	OPERAND	CT	ADDRS	INSTRUCTION
AN85			*ROUTINE 64-CHECK MLZ, MLNW INSTRUCTIONS WHEN ENDING ON A FIELD W/M			
AN86	DL1	BNQ	ITR	7	09329	J 01334 Q
AN87		MLCWA	CQ4,0EX5	12	09336	D 01617 00#0 X
AN88		MLZ	0EX6,0EX5	12	09348	D 00#0 00#0 B
AN89		MLNW	0EX6,0EX5	12	09360	D 00#0 00#0 E
AN90		C	0EX6,0EX5	11	09372	C 00#0 00#0
AN91		8E	DL2	7	09383	J 09398 S
AN92		B	SE1	7	09390	J 27220
AN93		H		1	09397	.
AN94	*		AFTER USING MLZ AND MLNW INSTRUCTIONS TO MOVE			
AN95	*		CONSTANT DD FROM ADDRESS FF TO ADDRESS EE, THE			
AN96	*		CONTENTS OF ADDRESS EE AND DD DID NOT COMPARE.			
AN97	DL2	8CE	DL1,TAD1,1	12	09398	B 09329 01001 I
AN98		B	SC1	7	09410	J 27380
AN99			*ROUTINE 65-CHECK MLN, MLZW INSTRUCTIONS WHEN ENDING ON A FIELD W/M			
A000	DM1	BNQ	ITR	7	09417	J 01334 Q
A001		MLCWA	CQ4,0EX5	12	09424	D 01617 00#0 X
A002		MLN	0EX6,0EX5	12	09436	D 00#0 00#0 A
A003		MLZW	0EX6,0EX5	12	09448	D 00#0 00#0 F
A004		C	0EX6,0EX5	11	09460	C 00#0 00#0
A005		8E	DM2	7	09471	J 09486 S
A006		B	SE1	7	09478	J 27220
A007		H		1	09485	.
A008	*		AFTER USING MLN AND MLZW INSTRUCTIONS TO MOVE			
A009	*		CONSTANT DD FROM ADDRESS FF TO ADDRESS EE, THE			
A010	*		CONTENTS OF ADDRESS EE DID NOT COMPARE WITH THE			
A011	*		CONSTANT DD.			
A012	DM2	8CE	DM1,TAD1,1	12	09486	B 09417 01001 I
A013		B	SC1	7	09498	J 27380
			LOOP ROUTINE 65			
			STEP ROUTINE COUNTER TO 66			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
A044			*ROUTINE 68-CHECK MLZ, MLNW INSTRUCTIONS WHEN ENDING ON B FIELD W/M			
A045	DP1	BNQ	ITR	7	09669	J 01334 Q
A046		MLWA	CQ4,0EX6	12	09676	D 01617 00*0 U
A047		MLCWA	CQ4,0EX5	12	09688	D 01617 00*0 X
A048		MLWA	DD,0EX5	12	0970D	D 01911 00*0 U
A049		MLZ	0EX6,0EX5	12	09712	D 00*0D 00*0 B
A050		MLNW	0EX6,0EX5	12	09724	D 00*0D 00*0 E
A051		C	0EX5,DD	11	09736	C 00*0D 01911
A052		BE	DP2	7	09747	J 09762 S
A053		B	SE1	7	09754	J 27220
A054		H		1	09761	.
A055			ROUTINE 68 ERROR			
A056			AFTER USING MLZ AND MLNW INSTRUCTIONS TO MOVE			
A057			CONSTANT DD FROM ADDRESS FF TO ADDRESS EE, THE			
A058			CONTENTS OF ADDRESS EE 010 NOT COMPARE WITH THE			
A059			CONSTANT 00.			
A059	DP2	BCE	0P1,TA01,1	12	09762	B 09669 010D1 1
A060		B	SC1	7	09774	J 27380
A061			*ROUTINE 69-CHECK MLN, MLZW INSTRUCTIONS WHEN ENDING ON R FIELD W/M			
A062	DQ1	BNQ	ITR	7	09781	J 01334 Q
A063		MLCWA	CQ4,0EX5	12	09788	D 01617 00*0 X
A064		MLWA	DD,0EX5	12	09800	D 01911 00*0 U
A065		MLN	0EX6,0EX5	12	09812	D 00*0D 00*0 A
A066		MLZW	0EX6,0EX5	12	09824	D 00*0D 00*0 F
A067		C	0EX5,00	11	09836	C 00*0D 01911
A068		BE	DQ2	7	09847	J 09862 S
A069		B	SE1	7	09854	J 2722D
A070		H		1	09861	.
A071			ROUTINE 69 ERROR			
A072			AFTER USING MLN AND MLZW INSTRUCTIONS TO MOVE			
A073			CONSTANT DD FROM ADDRESS FF TO ADDRESS EE, THE			
A074			CONTENTS OF ADDRESS EE 010 NOT COMPARE WITH THE			
A075			CONSTANT 00.			
A075	DQ2	BCE	DQ1,TA01,1	12	09862	B 09781 01001 1
A076		B	SC1	7	09874	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 49

PGLIN	LABEL	OPC00	OPERANO	CT	ADDRS	INSTRUCTION
A078						*ROUTINE 70-CHECK MLC, MLW INSTRUCTIONS WHEN ENDING ON B FIELD W/M.
A079	DR1	BNQ	ITR	7	09881	J 01334 Q
A080		MLCWA	CQ4,0&X5	12	09888	D 01617 00*0 X
A081		MLWA	00,0&X5	12	09900	D 01911 00*0 U
A082		MLC	0&X6,0&X5	12	09912	D 00*0 00*0 C
A083		MLW	0&X6,0&X5	12	09924	D 00*0 00*0 D
A084		C	0&X5,00	11	09936	C 00*0 01911
A085		BE	DR2	7	09947	J 09962 S
A086		B	SE1	7	09954	J 27220
A087	H			1	09961	.
A088	*					ROUTINE 70 ERROR
A089	*					AFTER USING MLC AND MLW INSTRUCTIONS TO MOVE
A090	*					CONSTANT 00 FROM ADDRESS FF TO ADDRESS EE, THE
A091	*					CONTENTS OF ADDRESS EE DID NOT COMPARE WITH THE
A092	DR2	BCE	DR1,TA01,1	12	09962	B 09881 01001 I
A093	B	SCI		7	09974	J 27380
A094						LOOP ROUTINE 70
A095	OS1	BNQ	ITR	7	09981	J 01334 Q
A096		MLCWA	CQ4,0&X5	12	09988	D 01617 00*0 X
A097		MLWA	00,0&X5	12	10000	D 01911 00*0 U
A098		MLCW	0&X6,0&X5	12	10012	D 00*0 00*0 G
A099	C	0&X5,00		11	10024	C 00*0 01911
AP00	BE	DS2		7	10035	J 10050 S
AP01	B	SE1		7	10042	J 27220
AP02	H			1	10049	.
AP03	*					ROUTINE 71 ERROR
AP04	*					AFTER USING AN MLCW INSTRUCTION TO MOVE CONSTANT DD
AP05	*					FROM ADDRESS FF TO ADDRESS EE, THE CONTENTS OF
AP06	DS2	BCE	DS1,TA01,1	12	10050	B 09981 01001 I
AP07	B	SCI		7	10062	J 27380
						LOOP ROUTINE 71
						STEP ROUTINE COUNTER TO 72

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 50

CT ADORS INSTRUCTION

OPCOU OPERAND

LABEL

PGLIN

*ROUTINE 72--CALCULATE RIGHT ADDRESSES PLUS 1 OF EE AND FF FIELDS

* CONTAINING CONSTANTS CC AND DD RESPECTIVELY.

PGLIN	LABEL	OPCOU	OPERAND	CT	ADORS	INSTRUCTION
AP09						
AP10						
AP11	DT1	BNQ	ITR	7	10069	J 01334 Q
AP12		MLCWA	EE,X7	12	10076	O 01916 00059 X
AP13		MLCWA	FF,X8	12	10088	D 01921 00064 X
AP14		A	C02,X7	11	10100	A 01467 00059
AP15		A	C025,X8	11	10111	A 01472 00064
AP16		MLCWA	X7,C08	12	10122	O 00059 01482 X
AP17		MLCWA	X8,C09	12	10134	O 00064 01487 X
AP18		S	C02,C08	11	10146	S 01467 01482
AP19		S	C025,C09	11	10157	S 01472 01487
AP20		C	C08,EE	11	10168	C 01482 01916
AP21		BU	DT2	7	10179	J 10211 /
AP22		C	C09,FF	11	10186	C 01487 01921
AP23		BU	DT2	7	10197	J 10211 /
AP24		B	DT3	7	10204	J 10219
AP25	DT2	B	SEL	7	10211	J 27220
AP26		H		1	10218	.
AP27	*		AFTER ADDING CONSTANT 1 TO CONSTANT 2, AND			
AP28	*		SUBTRACTING CONSTANT 1 FROM THE SUM, THE RESULT ODD			
AP29	*		NOT COMPARE WITH THE ORIGINAL CONSTANT 2. NOTE-THIS			
AP30	*		FAILURE MAY CAUSE ERRONEOUS ERROR INDICATIONS IN			
AP31	*		LATER ROUTINES			
AP32	DT3	BCE	DT1,TA01,1	12	10219	B 10069 01001 1
AP33		B	SC1	7	10231	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 51

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD OPERAND

AP35	*ROUTINE 73 CALCULATE RIGHT ADDRESSES OF EE AND FF FIELDS			
AP36	* CONTAINING CONSTANTS CC AND DD RESPECTIVELY.			
AP37	DU1 BNQ ITR BRANCH INQUIRY	7	10238	J 01334 Q
AP38	MLCWA X7,X9 SAVE INDEX REG 7-EE FIELD &1	12	10245	D 00059 00069 X
AP39	MLCWA X8,X10 SAVE INDEX REG 8-FF FIELD &1	12	10257	D 00064 00074 X
AP40	S &1,X9 CALCULATE ANSWER ONE	11	10269	S 29202 00069
AP41	S &1,X10 CALCULATE ANSWER TWO	11	10280	S 29202 00074
AP42	MLCWA X9,C08 SAVE DIFFERENCE ONE	12	10291	D 00069 01482 X
AP43	MLCWA X10,C09 SAVE DIFFERENCE TWO	12	10303	D 00074 01487 X
AP44	A &1,C08 CHECK FIRST ADD	11	10315	A 29202 01482
AP45	A &1,C09 CHECK SECOND ADD	11	10326	A 29202 01487
AP46	C X7,C08	11	10337	C 00059 01482
AP47	BU DU2 BRANCH-FIRST ADD OR SUB FAILED	7	10348	J 10380 /
AP48	C X8,C09	11	10355	C 00064 01487
AP49	BU DU2 BRANCH-SECOND ADD OR SUB FAILED	7	10366	J 10380 /
AP50	B DU3	7	10373	J 10388
AP51	B SE1 BRANCH TO ERROR ROUTINE	7	10380	J 27220
AP52	H ROUTINE 73 ERROR	1	10387	.
AP53	* AFTER SUBTRACTING 1 FROM A CONSTANT AND ADDING 1 TO			
AP54	* THE DIFFERENCE, THE RESULT DID NOT COMPARE WITH THE			
AP55	* ORIGINAL CONSTANT. NOTE-THIS FAILURE MAY CAUSE			
AP56	* ERRONEOUS ERROR INDICATIONS IN LATER ROUTINES.			
AP57	DU3 BCE DU1,IAD1,1 LOOP ROUTINE 73	12	10388	B 10238 01001 1
AP58	B SC1 STEP ROUTINE COUNTER TO 74	7	10400	J 27380

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AP60			*ROUTINE 74-CHECK SCNR INSTRUCTION.			
AP61	OVI	BNQ	ITR	7	10407	J 01334 Q
AP62		MLCWA	CC,0EX9	12	10414	D 01900 00.00 X
AP63		MLCWA	OD,0EX10	12	10426	D 01911 00.00 X
AP64		CW	0EX5,0EX6	11	10438	M 00.00 00.00
AP65		SW	0EX9,0EX10	11	10449	M 00.00 00.00
AP66		SCNR	0EX5,0EX6	12	10460	D 00.00 00.00 R
AP67		SAR	OV2&10	7	10472	G 10514 A
AP68		SHR	DV3&10	7	10479	G 10539 B
AP69		C	C02,C025	11	10486	C 01467 01472
AP70		BL	OV3	7	10497	J 10529 T
AP71	DV2	C	0EX7,0	11	10504	C 00.00 00000
AP72		BE	DV5	7	10515	J 10555 S
AP73		R	OV4	7	10522	J 10547
AP74	DV3	C	0EX8,0	11	10529	C 00.00 00000
AP75		BE	DV5	7	10540	J 10555 S
AP76	DV4	B	SE1	7	10547	J 27220
AP77		H		1	10554	.
AP78	*		ROUTINE 74 ERROR			
AP79	*		AFTER SCANNING THE EE AND FF FIELDS, THE CONTENTS OF			
AP80	*		THE ADDRESS REG CORRESPONDING TO THE SHORTEST FIELD			
AP81	*		DID NOT COMPARE WITH THE CORRECT RESULT AS			
AP82	DV5	BCE	DV1,IAD1,1	12	10555	B 10407 01001 I
AP83		B	SC1	7	10567	J 27380
			LOOP ROUTINE 74			
			STEP ROUTINE COUNTER TO 75			

1410/7010 CPU RELIABILITY TEST-40K & UP

PAGE 53

CU01 INSTRUCTION

LABEL OPCOD OPERAND

PGLIN

CT ADDR

*ROUTINE 75-CHECK SCNR INSTRUCTION.

AP85	DW1	8NQ	ITR	BRANCH INQUIRY	7	10574	J 01334 Q
AP86		MLCS	2*2,0EX7	R/M TO EE & CC FIELD LENGTH	12	10581	D 29254 00*00 3
AP87		MLZA	CQ4,0EX10	ELIMINATE ANY R/MS IN FF FIELD	12	10593	D 01617 00*00 S
AP88		MLCS	2*2,0EX10	R/M TO FF & DU FIELD LENGTH -1	12	10605	D 29254 00*00 3
AP89		SCNRR	0EX6,0EX5	SCAN FF AND EE FIELDS	12	10617	D 00*00 00*00 Y
AP90		SAR	COB	STORE AAR	7	10629	G 01482 A
AP91		C	COB,X8	CHECK SCAN OPERATION	11	10636	C 01482 00064
AP92		BE	DW2	BRANCH-OK	7	10647	J 10662 S
AP93		B	SE1	BRANCH TO ERROR ROUTINE	7	10654	J 27220
AP94		H		ROUTINE 75 ERROR	1	10661	.

ROUTINE 75 ERROR

* AFTER SCANNING THE FF AND EE FIELDS, THE CONTENTS OF
 * THE A ADDRESS REG DID NOT COMPARE WITH THE CORRECT
 * RESULT AS CALCULATED AND STORED IN INDEX REG 8 BY
 * A PREVIOUS ROUTINE.

AQ00	DW2	BCE	DW1,TAD1,1	LOOP ROUTINE 75	12	10662	B 10574 01001 1
AQ01		B	SCI	STEP ROUTINE COUNTER TO 76	7	10674	J 27380
AQ02				*ROUTINE 76-CHECK SCNR INSTRUCTION FOR STOPPING ON RECORD MARK			
AQ03	DX1	8NQ	ITR	BRANCH INQUIRY	7	10681	J 01334 Q
AQ04		SCNRM	0EX6,0EX5	SCAN FF AND EE FIELDS	12	10688	D 00*00 00*00 H
AQ05		SAR	COB	STORE AAR	7	10700	G 01482 A
AQ06		C	COB,X8	CHECK SCAN OPERATION	11	10707	C 01482 00064
AQ07		BE	DX2	BRANCH-OK	7	10718	J 10733 S
AQ08		B	SE1	BRANCH TO ERROR ROUTINE	7	10725	J 27220
AQ09		H		ROUTINE 76 ERROR	1	10732	.

ROUTINE 76 ERROR

* AFTER SCANNING THE FF AND EE FIELDS, THE CONTENTS OF
 * THE A ADDRESS REG DID NOT COMPARE WITH THE CORRECT
 * RESULT AS CALCULATED AND STORED IN INDEX REG 8 BY
 * A PREVIOUS ROUTINE.

AQ14	DX2	BCE	DX1,TAD1,1	LOOP ROUTINE 76	12	10733	B 10681 01001 1
AQ15		B	SCI	STEP ROUTINE COUNTER TO 77	7	10745	J 27380

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD OPERAND

*ROUTINE 77-CHECK SCNRG INSTRUCTION FOR STOPPING ON G/M-W/M.

AQ17	DY1	BNQ	ITR	BRANCH INQUIRY	7	10752	J	01334	Q
AQ18		MLCWS	AM2,0EX7	G/M-W/M TO EE & CC FIELD LENGTH	12	10759	D	29255	00+00 7
AQ19		MLCWS	AM2,0EX10	G/M-W/M TO FF & DD FIELD LENGTH -1	12	10771	D	29255	00+00 7
AQ20		SCNRG	0EX6,0EX5	SCAN FF AND EE FIELDS	12	10783	D	00+00	00+00 H
AQ21		SAR	CO8	STORE AAR	7	10795	G	01482	A
AQ22		C	CO8,X8	CHECK SCAN OPERATION	11	10802	C	01482	00064
AQ23		BE	OY2	BRANCH-OK	7	10813	J	10828	S
AQ24		B	SE1	BRANCH TO ERROR ROUTINE	7	10820	J	27220	
AQ25		H		ROUTINE 77 ERROR	1	10827	.		

* AFTER SCANNING THE FF AND EE FIELDS, THE CONTENTS OF
 * THE A ADDRESS REG DID NOT COMPARE WITH THE CORRECT
 * RESULT AS CALCULATED AND STORED IN INDEX REG 8 BY
 * A PREVIOUS ROUTINE.

AQ31	OY2	BCE	OY1,IA01,1	LOOP ROUTINE 77	12	10828	B	10752	01001 1
AQ32		B	SC1	STEP ROUTINE COUNTER TO 78	7	10840	J	27380	

*ROUTINE 78-CHECK SCNRG INSTRUCTION FOR STOPPING ON G/M-W/M

AQ34	OZ1	BNQ	ITR	BRANCH INQUIRY	7	10847	J	01334	Q
AQ35		MLZA	DO-1,99999EX10	REPLACE DO ZONE INTO FF FIELD	12	10854	D	01910	99RR9 S
AQ36		SCNRG	0EX6,0EX5	SCAN FF AND EE FIELDS	12	10866	D	00+00	00+00 Q
AQ37		SAR	CO8	STORE AAR	7	10878	G	01482	A
AQ38		C	CO8,X8	CHECK SCAN OPERATION	11	10885	C	01482	00064
AQ39		BE	DZ2	BRANCH-OK	7	10896	J	10911	S
AQ40		B	SE1	BRANCH TO ERROR ROUTINE	7	10903	J	27220	
AQ41		H		ROUTINE 78 ERROR	1	10910	.		

* AFTER SCANNING THE FF AND EE FIELDS, THE CONTENTS OF
 * THE A ADDRESS REG DID NOT COMPARE WITH THE CORRECT
 * RESULT AS CALCULATED AND STORED IN INDEX REG 8 BY
 * ROUTINE 72.

AQ42	DZ2	BCE	OZ1,IA01,1	LOOP ROUTINE 78	12	10911	B	10847	01001 1
AQ43		B	SC1	STEP ROUTINE COUNTER TO 79	7	10923	J	27380	

1410/7010 CPU RELIABILITY TEST-40K & UP

OPCODE OPERAND

LABEL

PGLIN

*ROUTINE 79-CHECK EE AND FF FIELDS FOR CORRECT CONTENTS.

AQ49	EA1	BNQ	ITR	BRANCH INQUIRY	7	10930	J 01334 9
AQ50		CH	0&X9	CLEAR RIGHT END W/M IN EE FIELD	6	10937	H 00.40
AQ51		SW	0&X5,0&X6	SET W/MS AT LEFT OF FIELDS EE-FF	11	10943	H 00.40 00.40
AQ52		C	0&X9,CC	CHECK CONTENTS OF ADDR EE FIELD	11	10954	C 00.40 01900
AQ53		BE	EA2	BRANCH-EE FIELD OK	7	10965	J 10980 S
AQ54		B	SE1	BRANCH TO ERROR ROUTINE	7	10972	J 27220
AQ55		H		ROUTINE 79 ERROR	1	10979	.
AQ56							
AQ57	*			THE SCAN OPERATIONS IN THE LAST FIVE ROUTINES SHOULD			
AQ58	*			NOT HAVE DISTURBED CONSTANT CC IN THE EE FIELD.			
AQ59	*			HOWEVER, THE EE FIELD AND CONSTANT CC DO NOT COMPARE			
AQ60	EA2	MLCWS	DD,0&X10	REPLACE RIGHT END CHARACTER	12	10980	D 01911 00.40 7
AQ61		C	0&X10,DD	CHECK CONTENTS OF ADDR FF FIELD	11	10992	C 00.40 01911
AQ62		BE	EA3	BRANCH-FF FIELD OK	7	11003	J 11018 S
AQ63		B	SE1	BRANCH TO ERROR ROUTINE	7	11010	J 27220
AQ64		H		ROUTINE 79 ERROR	1	11017	.
AQ65	*			THE SCAN OPERATIONS IN THE LAST FIVE ROUTINES SHOULD			
AQ66	*			NOT HAVE DISTURBED CONSTANT DD IN THE FF FIELD.			
AQ67	*			HOWEVER, THE FF FIELD AND CONSTANT DD DO NOT COMPARE			
AQ68	EA3	BCE	EA1,TAD1,1	LOOP ROUTINE 79	12	11018	B 10930 01001 1
AQ69		B	SC1	STEP ROUTINE COUNTER TO 80	7	11030	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 56

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AQ71	*ROUTINE 80-CHECK MRN, MRZW INSTRUCTIONS WHEN ENDING ON A FIELD W/M					
AQ72	EB1	BNQ	ITR	7	11037	J 01334 Q
AQ73		MLCWA	CP2E12,11EX5	12	11044	D 01568 00+1 X
AQ74		MLCWA	CP2E12,11EX6	12	11056	D 01568 00+J1 X
AQ75		MLCWA	CC,0EX9	12	11068	D 01900 00+.0 X
AQ76		CW	0EX5	6	11080	D 00+.0
AQ77		SW	0EX9	6	11086	D 00+.0
AQ78		MRN	0EX5,0EX6	12	11092	D 00+.0 00+.0 9
AQ79		MRZW	0EX5,0EX6	12	11104	D 00+.0 00+.0 6
AQ80		SBR	EB2E10	7	11116	G 11133 B
AQ81	EB2	C	0EX7,0	11	11123	C 00+MO 00000
AQ82		SBR	EB3E10	7	11134	G 11166 B
AQ83		BE	EB3	7	11141	J 11156 S
AQ84		B	SE1	7	11148	J 27220
AQ85	H			1	11155	.
AQ86	*		AFTER USING MRN AND MRZW INSTRUCTIONS TO MOVE			
AQ87	*		CONSTANT CC,CONTAINING A WORD MARK AT THE RIGHT, FROM			
AQ88	*		ADDRESS EE TO ADDRESS FF, THE CONTENTS OF THE EE			
AQ89	*		FIELD PLUS ONE DID NOT COMPARE WITH THE CONTENTS OF			
AQ90	*		THE FF FIELD PLUS ONE. THE WORD MARK OR THE LAST			
AQ91	*		CHARACTER WAS NOT PROPERLY MOVED.			
AQ92	EB3	C	99999EX9,0	11	11156	C 99RZ9 00000
AQ93	BE	EB4		7	11167	J 11182 S
AQ94	B	SE1		7	11174	J 27220
AQ95	H			1	11181	.
AQ96	*		AFTER USING MRN AND MRZW INSTRUCTIONS TO MOVE			
AQ97	*		CONSTANT CC FROM ADDRESS EE TO ADDRESS FF, THE			
AQ98	*		CONTENTS OF THE EE FIELD DID NOT COMPARE WITH THE			
AQ99	*		CONTENTS OF THE FF FIELD.			
AR00	EB4	BCE	EB1,TAD1,1	12	11182	B 11037 01001 1
AR01	B	SC1		7	11194	J 27380
			LOOP ROUTINE 80			
			STEP ROUTINE COUNTER TO 81			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 57

CT ADDRS INSTRUCTION

LA8EL OPC00 OPERAND

PGLIN

AR03	*ROUTINE 81-CHECK MRZ, MRNW INSTRUCTIONS WHEN ENOING ON A FIELD W/M			
AR04	EC1	BNQ	1TR	BRANCH INQUIRY
AR05		MLCWA	CP2&12,11&X6	CLEAR ADDR FF-1 THRU FF&11
AR06		MRZ	0&X5,0&X6	CC ZONE FROM ADDRESS EE TO FF
AR07		MRNW	0&X5,0&X6	CC NUM,W/M FROM ADDRESS EE TO FF
AR08		SDR	EC2&10	STORE ADDRESS FF &LENGTH OF CC
AR09		C	0&X7,0	CHECK MDVE OF W/M,RIGHT CHAR
AR10	EC2	SBR	EC3&10	STORE ADDRESS FF&LENGTH OF CC-2
AR11		8E	EC3	BRANCH-OK
AR12		B	SE1	BRANCH TO ERROR ROUTINE
AR13		H		ROUTINE 81 ERROR
AR14	*			AFTER USING MRZ AND MRNW INSTRUCTIONS TO MOVE
AR15	*			CONSTANT CC,CONTAINING A WORD MARK AT THE RIGHT,FROM
AR16	*			ADDRESS EE TO ADDRESS FF, THE CONTENTS OF THE EE
AR17	*			FIELD PLUS ONE DID NOT COMPARE WITH THE CONTENTS OF
AR18	*			THE FF FIELD PLUS ONE. THE WDRO MARK OR THE LAST
AR19	*			CHARACTER WAS NOT PROPERLY MOVED.
AR20	EC3	C	9999&X9,0	CHECK REMAINDER OF MDVED FIELO
AR21		8E	EC4	BRANCH-MOVES OK
AR22		B	SE1	BRANCH TO ERROR ROUTINE
AR23		H		ROUTINE 81 ERROR
AR24	*			AFTER USING MRZ AND MRNW INSTRUCTIONS TO MOVE
AR25	*			CONSTANT CC FROM ADDRESS EE TO ADDRESS FF, THE
AR26	*			CONTENTS OF THE EE FIELD DID NOT COMPARE WITH THE
AR27	*			CONTENTS OF THE FF FIELD.
AR28	EC4	BCE	EC1,IAD1,1	LODP ROUTINE 81
AR29		B	SC1	STEP ROUTINE COUNTER TO 82

7	11201	J	01334	Q
12	11208	D	01568	00+J1 X
12	11220	Q	00+*0	00+*0 0
12	11232	Q	00+*0	00+*0 0
7	11244	G	11261	B
11	11251	C	00+MO	00000
7	11262	G	11294	B
7	11269	J	11284	S
7	11276	J	27220	
1	11283	.		

11	11284	C	99R29	00000
7	11295	J	11310	S
7	11302	J	27220	
1	11309	.		

12	11310	B	11201	01001 1
7	11322	J	27380	

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 58

CT ADDR INSTRUCTION

OPC00 OPERAND

LABEL

PGLIN

```

AR31 *ROUTINE 82-CHECK MRC, MRW INSTRUCTIONS WHEN ENDING ON A FIELD W/M.
AR32 ED1 BNQ ITR BRANCH INQUIRY
AR33 MLCWA CP2&12,11&X6 CLEAR ADDR FF-1 THRU FF&11
AR34 MRC 0&X5,0&X6 CONSTANT CC FROM ADDRESS EE TO FF
AR35 MRW 0&X5,0&X6 CC W/M FROM ADDRESS EE TO FF
AR36 SBR ED2&10 STORE ADDRESS FF &LENGTH OF CC
AR37 C 0&X7,0 CHECK MOVE OF W/M,RIGHT CHAR
AR38 SBR ED3&10 STORE ADDRESS FF&LENGTH OF CC-2
AR39 BE ED3 BRANCH-OK
AR40 B SE1 BRANCH TO ERROR ROUTINE
AR41 H ROUTINE 82 ERROR
AR42 *
AR43 * AFTER USING MRC AND MRW INSTRUCTIONS TO MOVE
AR44 * CONSTANT CC,CONTAINING A WORD MARK AT THE RIGHT,FROM
AR45 * ADDRESS EE TO ADDRESS FF, THE CONTENTS OF THE EE
AR46 * FIELD PLUS ONE DID NOT COMPARE WITH THE CONTENTS OF
AR47 * THE FF FIELD PLUS ONE. THE WORD MARK OR THE LAST
AR48 * CHARACTER WAS NOT PROPERLY MOVED.
AR49 E03 C 9999&X9,0 CHECK REMAINDER OF MOVED FIELD
AR50 BE ED4 BRANCH-MOVES OK
AR51 B SE1 BRANCH TO ERROR ROUTINE
AR52 H ROUTINE 82 ERROR
AR53 *
AR54 * AFTER USING MRC AND MRW INSTRUCTIONS TO MOVE
AR55 * CONSTANT CC FROM ADDRESS EE TO ADDRESS FF, THE
AR56 * CONTENTS OF THE EE FIELD DID NOT COMPARE WITH THE
AR57 * CONTENTS OF THE FF FIELD.
BCE ED1,IAD1,I LOOP ROUTINE 82
B SC1 STEP ROUTINE COUNTER TO 83

```

```

7 11329 J 01334 Q
12 11336 D 01568 00+J1 X
12 11348 D 00+0 00+0 #
12 11360 D 00+0 00+0 #
7 11372 G 11389 B
11 11379 C 00+M0 00000
7 11390 G 11422 B
7 11397 J 11412 S
7 11404 J 27220
1 11411 .

```

```

11 11412 C 99R29 00000
7 11423 J 11438 S
7 11430 J 27220
1 11437 .

```

```

12 11438 B 11329 01001 I
7 11450 J 27380

```

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 59
CT ADORS INSTRUCTION

PGLIN

LABEL

OPCOD OPERANO

AR59	•	ROUTINE 83-CHECK MRCW INSTRUCTION WHEN ENDING ON A FIELD WORD MARK			
AR60	EE1	BNQ ITR	7	11457	J 01334 Q
AR61		MLCWA CP2C12,11CX6			
AR62		MRCW 0CX5,0CX6	12	11464	D 01568 00+J1 X
AR63		SBR EE2C10	12	11476	D 00+0 00+0 M
AR64	EE2	C 0CX7,0	7	11488	G 11505 B
AR65		SBR EE3C10	11	11495	C 00+M0 00000
AR66		BE EE3	7	11506	G 11538 B
AR67		B SE1	7	11513	J 11528 S
AR68		H	7	11520	J 27220
AR69	•	AFTER USING AN MRCW INSTRUCTION TO MOVE CONSTANT CC,	1	11527	.
AR70	•	CONTAINING A WORD MARK AT THE RIGHT, FROM ADDRESS EE TO			
AR71	•	ADDRESS FF, THE CONTENTS OF THE EE FIELD PLUS ONE			
AR72	•	DID NOT COMPARE WITH CONTENTS OF THE FF FIELD PLUS			
AR73	•	ONE. THE WORD MARK OR THE LAST CHARACTER WAS NOT			
AR74	•	PROPERLY MOVED.			
AR75	EE3	C 99999EX9,0	11	11528	C 99RZ9 00000
AR76		BE EE4	7	11539	J 11554 S
AR77		B SE1	7	11546	J 27220
AR78		H	1	11553	.
AR79	•	AFTER USING AN MRCW INSTRUCTION TO MOVE CONSTANT CC			
AR80	•	FROM ADDRESS EE TO ADDRESS FF, THE CONTENTS OF THE			
AR81	•	EE FIELD DID NOT COMPARE WITH THE CONTENTS OF THE FF			
AR82	•	FIELD.			
AR83	EE4	BCE EE1, IAD1,1	12	11554	B 11457 01001 I
AR84		B SCI	7	11566	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

OPCODE OPERAND

LABEL

PGLIN

```

AR86 *ROUTINE 84-CHECK MRN, MRZW INSTRUCTIONS WHEN ENDING ON R FIELD W/M
AR87 EF1
AR88 BNC ITR
AR89 MLCWA CP2&12,11&X5
AR90 MLCWA CP2&12,11&X6
AR91 MLCA CC,0&X9
AR92 SW 0&X9
AR93 MRW 0&X5,0&X6
AR94 CW 0&X9
AR95 MRN 0&X5,0&X6
AR96 MRZW 0&X5,0&X6
AR97 SBR EF2&10
AR98 MRW CP2&1,0&X6
AR99 C 0&X7,0
AS00 BE EF3
AS01 B SE1
AS02 H

```

```

* AFTER MOVING CONSTANT CC FROM ADDRESS EE TO ADDRESS
* FF, THE CONTENTS OF THE EE FIELD DID NOT COMPARE
* WITH THE CONTENTS OF THE FF FIELD. THIS ERROR HALT
* WILL OCCUR IF THE DATA AT EE AND FF ARE DIFFERENT,
* OR IF MRZW FAILED TO STOP ON THE B FIELD WORD MARK.
EF3 HCE EF1,TAU1,1
R SCI
STEP ROUTINE COUNTER TO 85

```

CU01 PAGE 60

CT ADDR INSTRUCTION

```

7 11573 J 01334 Q
12 11580 D 01568 00*11 X
12 11592 D 01568 00*11 X
12 11604 D 01900 00*10 T
6 11616 , 00*10
12 11622 D 00*10 00*10 a
6 11634 D 00*10
12 11640 D 00*10 00*10 9
12 11652 D 00*10 00*10 9
7 11664 G 11693 B
12 11671 D 01557 00*10 a
11 11683 C 00*10 00000
7 11694 J 11709 S
7 11701 J 27220
1 11708 .

```

```

12 11709 B 11573 01001 1
7 11721 J 27380

```

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 61

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AS10			*ROUTINE 85-CHECK MRZ, MRNW INSTRUCTIONS WHEN ENDING ON B FIELD W/M			
AS11	EG1	RNQ	ITR	7	11728	J 01334 Q
AS12		MLCWA	CP2E12,11EX6	12	11735	D 01568 00+J1 X
AS13		SW	0EX9	6	11747	, 00.+0
AS14		MRW	0EX5,0EX6	12	11753	D 00+0 00+0 0
AS15		CW	0EX9	6	11765	D 00.+0
AS16		MRZ	0EX5,0EX6	12	11771	D 00+0 00+0 0
AS17		MRNW	0EX5,0EX6	12	11783	D 00+0 00+0 0
AS18		SBR	EG2E10	7	11795	G 11824 B
AS19		MRW	CP2E1,0EX6	12	11802	D 01557 00+0 0
AS20	EG2	C	0EX7,0	11	11814	C 00+M0 00000
AS21		BE	EG3	7	11825	J 11840 S
AS22		B	SE1	7	11832	J 27220
AS23		H		1	11839	.
AS24	*		AFTER MOVING CONSTANT CC FROM ADDRESS EE TO ADDRESS			
AS25	*		FF, THE CONTENTS OF THE EE FIELD DID NOT COMPARE			
AS26	*		WITH THE CONTENTS OF THE FF FIELD. THIS ERROR HALT			
AS27	*		WILL OCCUR IF THE DATA AT EE AND FF ARE DIFFERENT,			
AS28	*		OR IF MRNW FAILED TO STOP ON THE B FIELD WORD MARK.			
AS29	EG3	BCE	EG1,JA01,1	12	11840	B 11728 01001 1
AS30		B	SC1	7	11852	J 27380

ROUTINE 85 ERROR

ROUTINE 85

STEP ROUTINE COUNTER TO 86

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AS32			*ROUTINE 86-CHECK MRC, MRW INSTRUCTIONS WHEN ENDING ON B FIELD W/M.			
AS33	EH1	RNQ	ITR	7	11859	J 01334 Q
AS34		MLCHA	CP2&12,11&X6	12	11866	D 01568 00#J1 X
AS35		SW	0&X9	6	11878	, 00.#0
AS36		MRW	0&X5,0&X6	12	11884	D 00#0 00#0 0
AS37		CM	0&X9	6	11896	0 00.#0
AS38		MRC	0&X5,0&X6	12	11902	D 00#0 00#0 0 #
AS39		MRW	0&X5,0&X6	12	11914	0 00#0 00#0 0
AS40		SBR	EH2&10	7	11926	G 11955 B
AS41		MRW	CP2&1,0&X6	12	11933	D 01557 00#0 0
AS42	EH2	C	0&X7,0	11	11945	C 00#M0 00000
AS43		BE	EH3	7	11956	J 11971 S
AS44		B	SE1	7	11963	J 27220
AS45		H		1	11970	.
AS46	*		AFTER MOVING CONSTANT CC FROM ADDRESS EE TO ADDRESS			
AS47	*		FF, THE CONTENTS OF THE EE FIELD DID NOT COMPARE			
AS48	*		WITH THE CONTENTS OF THE FF FIELD. THIS ERROR HALT			
AS49	*		WILL OCCUR IF THE DATA AT EE AND FF ARE DIFFERENT,			
AS50	*		OR IF MRW FAILED TO STOP ON THE B FIELD WCRD MARK.			
AS51	EH3	BCE	EH1,TA01,1	12	11971	B 11859 01001 1
AS52	B		SC1	7	11983	J 27380
			STEP ROUTINE COUNTER TO 87			
			LOOP ROUTINE 86			
			ROUTINE 86 ERROR			
			BRANCH INQUIRY			
			CLEAR ADDRESS FF-1 THRU FF&11			
			SET W/M TO RIGHT OF EE FIELD			
			SET RIGHT CC W/M IN FF FIELD			
			CLEAR W/M TO RIGHT OF EE FIELD			
			CC FROM EE FIELD TO FF FIELD			
			CHECK STOPPING ON B FIELD W/M			
			STORE ADDRESS FF&LENGTH OF CC			
			CLEAR CC WCRD MARK FROM ADDR FF			
			CHECK MOVES			
			BRANCH-MOVES OK			
			BRANCH TO ERROR ROUTINE			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AS54			*ROUTINE 87-CHECK MRCW INSTRUCTION WHEN ENDING ON 8 FIELD WORD MARK			
AS55	E11	BNQ	ITR	7	11990	J 01334 Q
AS56		MLCWA	CP2&12,11&X6	12	11997	D 01568 00+J1 X
AS57		SW	0&X9	6	12009	00+0
AS58		MRW	0&X5,0&X6	12	12015	D 00+0 00+0 0
AS59		CW	0&X9	6	12027	00+0
AS60		MRCW	0&X5,0&X6	12	12033	D 00+0 00+0 0 M
AS61		SBR	E12&10	7	12045	G 12074 B
AS62		MRW	CP2&1,0&X6	12	12052	D 01557 00+0 0
AS63	E12	C	0&X7,0	11	12064	C 00+M0 00000
AS64		BE	E13	7	12075	J 12090 S
AS65		B	SE1	7	12082	J 27220
AS66		H		1	12089	.
AS67	*		AFTER MOVING CONSTANT CC FROM ADDRESS EE TO ADDRESS			
AS68	*		FF, THE CONTENTS OF THE EE FIELD DID NOT COMPARE			
AS69	*		WITH THE CONTENTS OF THE FF FIELD. THIS ERROR HALT			
AS70	*		WILL OCCUR IF THE DATA AT EE AND FF ARE DIFFERENT,			
AS71	*		OR IF MRCW FAILED TO STOP ON THE 8 FIELD WORD MARK.			
AS72	E13	BCE	E11,TAD1,1	12	12090	B 11990 01001 1
AS73		B	SC1	7	12102	J 27380
AS74			*ROUTINE 88-SET UP WORKING AREA FOR CHECKING LEFT TO RIGHT MOVES TO			
AS75	*		RECORD MARKS.			
AS76	EJ1	BNQ	ITR	7	12109	J 01334 Q
AS77		MLCW	CC,0&X9	12	12116	D 01900 00+0 G
AS78		CW	0&X5	6	12128	00+0
AS79		MLCWS	@ @,0&X7	12	12134	D 29257 00+M0 7
AS80		MLCWS	@ @,0&X5-1	12	12146	D 29208 99Z79 7
AS81		MLCWS	@ @,0&X6-1	12	12158	D 29208 99ZR9 7
AS82		BCE	EJ1,TAD1,1	12	12170	B 12109 01001 1
AS83		B	SC1	7	12182	J 27380

PGLIN	LABEL	DPCDD	OPERAND	CT	ADDRS	INSTRUCTION
AS85			*ROUTINE 89-CHECK MRNR, MRZWR INSTRUCTIONS.			
AS86	EK1	BNQ	ITR	7	12189	J 01334 Q
AS87		MLCWA	DD,D&X1D	12	12196	D 01911 D0.0 X
AS88		MRNR	0&X5,D&X6	12	12208	D 00*0 00*0 Z
AS89		SAR	EK2&5	7	12220	G 12251 A
AS90		MRZWR	0&X5,0&X6	12	12227	D 00*0 00*0 S
AS91		SBR	EK2&10	7	12239	G 12256 B
AS92	EK2	SCNLS	0.0	12	12246	D 00000 00000
AS93		SAR	EK3&10	7	12258	G 12289 A
AS94		SAR	EK4&10	7	12265	G 12309 A
AS95		SBR	EK4&5	7	12272	G 12304 B
AS96	EK3	BCE	EK4.0.†	12	12279	B 12299 00000 †
AS97	B		SE1	7	12291	J 27220
AS98	H			1	12298	.
AS99	*		ROUTINE 89 ERROR			
AT00	*		AFTER OPERATION OF THE MRNR INSTRUCTION, THE ADDRESS			
AT01	*		IN THE A ADDRESS REG MINUS ONE WAS SAVED IN THE B			
AT02	*		FIELD OF THE BCE INSTRUCTION. THE FAILURE OF THE BCE			
AT03	*		INSTRUCTION TO BRANCH INDICATES THE LAST ADDRESS			
AT04	*		MOVED DID NOT CONTAIN A RECORD MARK AS IT SHOULD.			
AT05	EK4	C	0.0	11	12299	C 00000 D0000
AT06	BE	EK5		7	12310	J 12325 S
AT07	B	SE1		7	12317	J 27220
AT08	H			1	12324	.
AT09	*		ROUTINE 89 ERROR			
AT10	*		AFTER USING MRNR AND MRZWR INSTRUCTIONS TO MOVE			
AT11	*		CONSTANT CC, DR A PORTION OF CONSTANT CC, FROM THE			
AT12	*		EE FIELD TO THE FF FIELD, THE TWO FIELDS DID NOT			
AT13	*		COMPARE.			
AT14	EK5	BCE	EK1,IAD1,1	12	12325	B 12189 D1001 1
AT15	B	SC1		7	12337	J 27380
AT16			LOOP ROUTINE 89			
AT17			STEP ROUTINE COUNTER TO 90			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 65

CT ADORS INSTRUCTION

OPC00 OPERANO

LABEL

PGLIN

*ROUTINE 90-CHECK MRZR, MRNWR INSTRUCTIONS.

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
AT15						
AT16	EL1	BNQ	ITR	7	12344	J 01334 Q
AT17		MLCWA	DO,0EX10	12	12351	O 01911 00,0 X
AT18		MRZR	0EX5,0EX6	12	12363	O 00,0 00,0 #
AT19		SAR	EL2E5	7	12375	G 12406 A
AT20		MRNWR	0EX5,0EX6	12	12382	O 00,0 00,0 S
AT21		SBR	EL2E10	7	12394	G 12411 B
AT22	EL2	SCNLS	O,0	12	12401	O 00000 00000
AT23		SAR	EL3E10	7	12413	G 12444 A
AT24		SAR	EL4E10	7	12420	G 12464 A
AT25		SBR	EL4E5	7	12427	G 12459 B
AT26	EL3	BCE	EL4,0,0	12	12434	B 12454 00000 #
AT27		B	SE1	7	12446	J 27220
AT28		H		1	12453	.

ROUTINE 90 ERROR

AFTER OPERATION OF THE MRZR INSTRUCTION, THE ADDRESS
IN THE A ADDRESS REG MINUS ONE WAS SAVED IN THE B
FIELD OF THE BCE INSTRUCTION. THE FAILURE OF THE BCE
INSTRUCTION TO BRANCH INDICATES THE LAST ADDRESS
MOVEO OIO NOT CONTAIN A RECORD MARK AS IT SHOULD.

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
AT34	EL4	C	O,0	11	12454	C 00000 00000
AT35		BE	EL5	7	12465	J 12480 S
AT36		B	SE1	7	12472	J 27220
AT37		H		1	12479	.

ROUTINE 90 ERROR

AFTER USING MRZR AND MRNWR INSTRUCTIONS TO MOVE
CONSTANT CC, OR A PORTION OF CONSTANT CC, FROM THE
EE FIELD TO THE FF FIELD, THE TWO FIELDS DID NOT
COMPARE.

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
AT42	EL5	BCE	EL1,TA01,1	12	12480	B 12344 01001 1
AT43		B	SC1	7	12492	J 27380

LOOP ROUTINE 90
STEP ROUTINE COUNTER TO 91

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AT45		*ROUTINE 91-CHECK MRCR, MRWR INSTRUCTIONS.				
AT46	EM1	BNO	ITR	7	12499	J 01334 Q
AT47		MLCWA	DO,0EX10	12	12506	D 01911 00000 X
AT48		MRCR	0EX5,0EX6	12	12518	D 00000 00000 *
AT49		SAR	EM2&5	7	12530	G 12561 A
AT50		MRWR	0EX5,0EX6	12	12537	D 00000 00000 *
AT51		SBR	EM2&10	7	12549	G 12566 B
AT52	EM2	SCNLS	0,0	12	12556	D 00000 00000
AT53		SAR	EM3&10	7	12568	G 12599 A
AT54		SAR	EM4&10	7	12575	G 12619 A
AT55		SBR	EM4&5	7	12582	G 12614 B
AT56	EM3	BCE	EM4,0,*	12	12589	B 12609 00000 *
AT57		B	SE1	7	12601	J 27220
AT58		H		1	12608	.
AT59	*		AFTER OPERATION OF THE MRCR INSTRUCTION, THE ADDRESS			
AT60	*		IN THE A ADDRESS REG MINUS ONE WAS SAVED IN THE B			
AT61	*		FIELD OF THE BCE INSTRUCTION. THE FAILURE OF THE BCE			
AT62	*		INSTRUCTION TO BRANCH INDICATES THE LAST ADDRESS			
AT63	*		MOVED DID NOT CONTAIN A RECORD MARK AS IT SHOULD.			
AT64	EM4	C	0,0	11	12609	C 00000 00000
AT65		BE	EM5	7	12620	J 12635 S
AT66		B	SE1	7	12627	J 27220
AT67		H		1	12634	.
AT68	*		AFTER USING MRCR AND MRWR INSTRUCTIONS TO MOVE			
AT69	*		CONSTANT CC, OR A PORTION OF CONSTANT CC, FROM THE			
AT70	*		EE FIELD TO THE FF FIELD, THE TWO FIELDS DID NOT			
AT71	*		COMPARE.			
AT72	EM5	BCE	EM1,TA01,1	12	12635	B 12499 01001 1
AT73		B	SC1	7	12647	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION	CU01
AT75		*ROUTINE 92-CHECK MRCWR INSTRUCTION.					
AT76	EN1	8NQ	ITR	7	12654	J 01334 Q	
AT77		MLCWA	00,0&X10	12	12661	D 01911 00,0 X	
AT78		MRCWR	0&X5,0&X6	12	12673	D 00*0 00*0 S	
AT79		SAR	EN2&5	7	12685	G 12704 A	
AT80		S8R	EN2&10	7	12692	G 12709 8	
AT81	EN2	SCNLS	0,0	12	12699	D 00000 00000	
AT82		SAR	EN3&10	7	12711	G 12742 A	
AT83		SAR	EN4&10	7	12718	G 12762 A	
AT84		S8R	EN4&5	7	12725	G 12757 8	
AT85	EN3	8CE	EN4,0,*	12	12732	8 12752 00000 *	
AT86	8	SE1		7	12744	J 27220	
AT87	H			1	12751	.	
AT88	*		AFTER OPERATION OF THE MRCWR INSTRUCTION, THE				
AT89	*		ADDRESS IN THE A ADDRESS REG MINUS ONE WAS SAVED IN				
AT90	*		THE 8 FIELD OF THE BCE INSTRUCTION. THE FAILURE OF				
AT91	*		THE BCE INSTRUCTION TO BRANCH INDICATES THE LAST				
AT92	*		ADDRESS MOVED DID NOT CONTAIN A RECORD MARK AS IT				
AT93	*		SHOULD.				
AT94	EN4	C	0,0	11	12752	C 00000 00000	
AT95	BE	EN5		7	12763	J 12778 S	
AT96	B	SE1		7	12770	J 27220	
AT97	H			1	12777	.	
AT98	*		AFTER USING AN MRCWR INSTRUCTION TO MOVE CONSTANT				
AT99	*		CC, OR A PORTION OF CONSTANT CC, FROM THE EE FIELD				
AU00	*		TO THE FF FIELD, THE TWO FIELDS DID NOT COMPARE.				
AU01	EN5	8CE	EN1,IA01,1	12	12778	8 12654 01001 1	
AU02	8	SC1		7	12790	J 27380	

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AU04			*ROUTINE 93-CHECK MRNM, MRZWM INSTRUCTIONS.			
AU05	E01	BNQ	ITR _G	7	12797	J 01334 Q
AU06		MLCWA	2M2,0EX7	12	12804	D 29255 00+00 X
AU07		MLCWA	DD,0EX1D	12	12816	D 01911 00+00 X
AU08		MRNM	0EX5,D&X6	12	12828	D 00+00 00+00 I
AU09		SAR	E02E5	7	12840	G 12871 A
AU10		MRZWM	0EX5,0EX6	12	12847	D 00+00 00+00 I
AU11		SBR	E02E1D	7	12859	G 12876 B
AU12	E02	SCNLS	0,0	12	12866	D 00000 00000
AU13		SAR	E05E10	7	12878	G 12955 A
AU14		SBR	E05E5	7	12885	G 12950 B
AU15		SBR	E06E5	7	12892	G 12968 B
AU16		SBR	E03E10	7	12899	G 12923 B
AU17		SBR	E04E10	7	12906	G 12935 B
AU18	E03	BCE	E05,0,* _G	12	12913	B 12945 00000 * _G
AU19	E04	BCE	E06,0,* _H	12	12925	B 12963 00000 * _H
AU20		B	SE1	7	12937	J 27220
AU21	H			1	12944	.
AU22	*		AFTER OPERATION OF THE MRZWM, THE ADDRESS IN BAR			
AU23	*		MINUS ONE WAS SAVED IN THE TWO BCE INSTRUCTIONS. THE			
AU24	*		FAILURE OF BOTH BCE INSTRUCTIONS TO BRANCH INDICATES			
AU25	*		THE MRZWM DID NOT STOP ON A RECORD MARK OR GM/WM			
AU26	E05	C	0,0	11	12945	C 00000 00000
AU27		BE	E09	7	12956	J 13008 S
AU28	E06	SCNLS	0,100	12	12963	D 00000 00100
AU29		SAR	E07E5	7	12975	G 12987 A
AU30	E07	C	0,0EX9	11	12982	C 00000 00+00
AU31		BE	E09	7	12993	J 13008 S
AU32	E08	B	SE1	7	13000	J 27220
AU33	H			1	13007	.
AU34	*		MRNM AND MRZWM INSTRUCTIONS SHOULD HAVE MOVED CC, OR			
AU35	*		A PORTION OF CC, FROM THE EE FIELD TO THE FF FIELD.			
AU36	*		THE TWO FIELDS SHOULD HAVE COMPARED EQUAL.			
AU37	E09	BCE	E01,7AD1,1	12	13008	B 12797 01001 I
AU38		B	SC1	7	13020	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 69

PGLIN	LABEL	OPCOD	OPERANO	CT	ADRS	INSTRUCTION
AU40		*ROUTINE 94--CHECK MRZM, MRNWM INSTRUCTIONS.				
AU41	EP1	BNQ	ITR	7	13027	J 01334 Q
AU42		MLCWA	2M2,0EX7	12	13034	D 29255 00#M0 X
AU43		MLCWA	OD,0EX10	12	13046	D 01911 00#0 X
AU44		MRZM	0EX5,0EX6	12	13058	D 00#0 00#0 M
AU45		SAR	EP2E5	7	13070	G 13101 A
AU46		MRNWM	0EX5,0EX6	12	13077	D 00#0 00#0 B
AU47		SBR	EP2E10	7	13089	G 13106 8
AU48	EP2	SCNLS	0,0	12	13096	O 00000 00000
AU49		SAR	EP5E10	7	13108	G 13185 A
AU50		SBR	EP5E5	7	13115	G 13180 8
AU51		SBR	EP6E5	7	13122	G 13198 B
AU52		SBR	EP3E10	7	13129	G 13153 8
AU53		SBR	EP4E10	7	13136	G 13165 B
AU54	EP3	BCE	EP5,0,0	12	13143	B 13175 00000 #
AU55	EP4	BCE	EP6,0,0	12	13155	B 13193 00000 M
AU56		B	SE1	7	13167	J 27220
AU57	H			1	13174	.
AU58	*		AFTER OPERATION OF THE MRNWM, THE ADDRESS IN 8A			
AU59	*		MINUS ONE WAS SAVED IN THE TWO BCE INSTRUCTIONS. THE			
AU60	*		FAILURE OF BOTH BCE INSTRUCTIONS TO BRANCH INDICATES			
AU61	*		THE MRNWM DID NOT STOP ON A RECORD MARK OR GM/WM.			
AU62	EP5	C	0,0	11	13175	C 00000 00000
AU63		BE	EP9	7	13186	J 13238 S
AU64	EP6	SCNLS	0,100	12	13193	O 00000 00100
AU65		SAR	EP7E5	7	13205	G 13217 A
AU66	EP7	C	0,0EX9	11	13212	C 00000 00#0
AU67		RE	EP9	7	13223	J 13238 S
AU68	EP8	B	SE1	7	13230	J 27220
AU69	H			1	13237	.
AU70	*		MRZM AND MRNWM INSTRUCTIONS SHOULD HAVE MOVED CC, OR			
AU71	*		A PORTION OF CC, FROM THE EE FIELD TO THE FF FIELD.			
AU72	*		THE TWO FIELDS SHOULD HAVE COMPARED EQUAL.			
AU73	EP9	BCE	EP1,TA01,1	12	13238	H 13027 01001 1
AU74		B	SC1	7	13250	J 27380
						STEP ROUTINE COUNTER TO 95

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AU76		*ROUTINE 95-CHECK MRCM, MRWM INSTRUCTIONS.				
AU77	EQ1	BNQ	ITR	7	13257	J 01334 Q
AU78		MLCWA	0A,0EX7	12	13264	D 29255 00#M0 X
AU79		MLCWA	0D,0EX10	12	13276	D 01911 00#0 X
AU80		MRCM	0EX5,0EX6	12	13288	D 00#0 00#0 .
AU81		SAR	EQ2E5	7	13300	G 13331 A
AU82		MRWM	0EX5,0EX6	12	13307	D 00#0 00#0 .
AU83		SBR	EQ2E10	7	13319	G 13336 B
AU84	EQ2	SCNLS	0,0	12	13326	D 00000 00000
AU85		SAR	EQ5E10	7	13338	G 13415 A
AU86		SBR	EQ5E5	7	13345	G 13410 B
AU87		SHR	EQ6E5	7	13352	G 13428 B
AU88		SBR	EQ3E10	7	13359	G 13383 B
AU89		SBR	EQ4E10	7	13366	G 13395 B
AU90	EQ3	HCE	EQ5,0,†	12	13373	B 13405 00000 †
AU91	EQ4	BCE	EQ6,0,M	12	13385	B 13423 00000 M
AU92		B	SE1	7	13397	J 27220
AU93		H		1	13404	.
AU94	*	AFTER OPERATION OF THE MRWM, THE ADDRESS IN BAR				
AU95	*	MINUS ONE WAS SAVED IN THE TWO BCE INSTRUCTIONS. THE				
AU96	*	FAILURE OF BOTH BCE INSTRUCTIONS TO BRANCH INDICATES				
AU97	*	THE MRWM DID NOT STOP ON A RECORD MARK OR GM/WM.				
AU98	EQ5	C	0,0	11	13405	C 00000 00000
AU99		BE	EQ9	7	13416	J 13468 S
AV00	EQ6	SCNLS	0,100	12	13423	D 00000 00100
AV01		SAR	EQ7E5	7	13435	G 13447 A
AV02	EQ7	C	0,0EX9	11	13442	C 00000 00#0
AV03		BE	EQ9	7	13453	J 13468 S
AV04	EQ8	B	SE1	7	13460	J 27220
AV05		H		1	13467	.
AV06	*	ROUTINE 95 ERROR				
AV07	*	MRCM AND MRWM INSTRUCTIONS SHOULD HAVE MOVED CC, OR				
AV08	*	A PORTION OF CC, FROM THE EE FIELD TO THE FF FIELD.				
AV09		THE TWO FIELDS SHOULD HAVE COMPARED EQUAL.				
AV10	EQ9	BCE	EQ1, IAD1,1	12	13468	B 13257 01001 1
		B	SC1	7	13480	J 27380
		STEP ROUTINE COUNTER TO 96				

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 71

PGLIN	LABEL	OPCODE	OPERAND	CT	ADRS	INSTRUCTION
AV12		*ROUTINE 96-CHECK MRCWM INSTRUCTION.				
AV13	ER1	BNQ	ITR	7	13487	J 01334 Q
AV14		MLCWA	0M0,00X7	12	13494	D 29255 00#M0 X
AV15		MLCWA	00,00X10	12	13506	D 01911 00#0 X
AV16		MRCWM	00X5,00X6	12	13518	O 00#0 00#0 M
AV17		SAR	ER205	7	13530	G 13549 A
AV18		SBR	ER2010	7	13537	G 13554 B
AV19	ER2	SCNLS	0,0	12	13544	D 00000 00000
AV20		SAR	ER5010	7	13556	G 13633 A
AV21		SBR	ER505	7	13563	G 13628 B
AV22		SBR	ER605	7	13570	G 13646 B
AV23		SBR	ER3010	7	13577	G 13601 B
AV24		SBR	ER4010	7	13584	G 13613 B
AV25	ER3	BCE	ER5,0,0	12	13591	B 13623 00000 #
AV26	ER4	BCE	ER6,0,0	12	13603	B 13641 00000 M
AV27	B	SE1		7	13615	J 27220
AV28	H			1	13622	.
AV29	*	AFTER OPERATION OF THE MRCWM INSTRUCTION, THE				
AV30	*	ADDRESS IN THE B ADDRESS REG MINUS ONE WAS SAVED IN				
AV31	*	THE TWO BCE INSTRUCTIONS. THE FAILURE OF BOTH BCE				
AV32	*	INSTRUCTIONS TO BRANCH INDICATES THE MOVE DID NOT				
AV33	*	STOP ON EITHER A RECORD MARK OR GROUP MARK, WORD MARK				
AV34	ER5	C	0,0	11	13623	C 00000 00000
AV35	BE	ER9		7	13634	J 13686 S
AV36	SCNLS	0,100		12	13641	D 00000 00100
AV37	SAR	ER705		7	13653	G 13665 A
AV38	C	0,00X9		11	13660	C 00000 00#0
AV39	BE	ER9		7	13671	J 13686 S
AV40	B	SE1		7	13678	J 27220
AV41	H			1	13685	.
AV42	*	AFTER USING AN MRCWM INSTRUCTION TO MOVE CONSTANT				
AV43	*	CC, OR A PORTION OF CONSTANT CC, FROM THE EE FIELD				
AV44	*	TO THE FF FIELD, THE TWO FIELDS DID NOT COMPARE.				
AV45	ER9	ER1,1AD1,1		12	13686	B 13487 01001 1
AV46	B	SC1		7	13698	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP.

CU01 PAGE 72

PGLIN	LABEL	OPCODE	OPERAND	CT	ADRS	INSTRUCTION
AV48			*ROUTINE 97-CHECK MRNG, MRZWG INSTRUCTIONS.			
AV49	ES1	BNQ	ITR	7	13705	J 01334 Q
AV50		MLCWS	0A0,0EX9	12	13712	O 29255 00,0 7
AV51		MLCWA	DD,0EX10	12	13724	O 01911 00,0 X
AV52		MLCWS	0A0,0EX6	12	13736	O 29255 00,0 7
AV53		MRNG	0EX5,0EX6	12	13748	O 00,0 00,0 R
AV54		SBR	ES2E5	7	13760	G 13784 B
AV55		MRZWG	0EX5,0EX6	12	13767	D 00,0 00,0 ;
AV56	ES2	SCNLS	0,100	12	13779	D 00000 00100
AV57		SAR	ES3E10	7	13791	G 13815 A
AV58		SAR	ES4E5	7	13798	G 13821 A
AV59	ES3	CM	0EX9,0	11	13805	D 00,0 00000
AV60	ES4	C	0,0EX9	11	13816	C 00000 00,0
AV61		BE	ES5	7	13827	J 13842 S
AV62		B	SE1	7	13834	J 27220
AV63	H			1	13841	.
AV64	*		AFTER USING MRNG AND MRZWG INSTRUCTIONS TO MOVE			
AV65	*		CONSTANT CC FROM THE EE FIELD TO THE FF FIELD, THE			
AV66	*		TWO FIELDS DID NOT COMPARE.			
AV67	ES5	BCE	ES1,TA01,1	12	13842	B 13705 01001 1
AV68		B	SC1	7	13854	J 27380
			LOOP ROUTINE 97			
			STEP ROUTINE COUNTER TO 98			

141D/701D CPU RELIABILITY TEST-40K & UP

CU01 PAGE 73

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
AV70			*ROUTINE 98-CHECK MRZG, MRNWG INSTRUCTIONS-			
AV71	ET1	BNQ	ITR	7	13861	J 01334 Q
AV72		MLCWS	2MA,0EX9	12	13868	D 29255 00.40 7
AV73		MLCWA	DD,0EX1D	12	13880	D 01911 00.00 X
AV74		MLCWS	2MA,0EX6	12	13892	D 29255 00.40 7
AV75		MRZG	0EX5,0EX6	12	13904	D 0044D 00.40 :
AV76		SBR	ET2E5	7	13916	G 13940 H
AV77		MRNWG	0EX5,0EX6	12	13923	D 0044D 00.40 R
AV78	ET2	SCNLS	0,100	12	13935	D 00000 0010D
AV79		SAR	ET3E1D	7	13947	G 13971 A
AV80		SAR	ET4E5	7	13954	G 13977 A
AV81	ET3	CW	0EX9,0	11	13961	D 00.40 00000
AV82	ET4	C	0,0EX9	11	13972	C 00000 00.40
AV83		BE	ET5	7	13983	J 13998 S
AV84		B	SE1	7	1399D	J 27220
AV85		H		1	13997	.
AV86	*		AFTER USING MRZG AND MRNWG INSTRUCTIONS TO MOVE			
AV87	*		CONSTANT CC FROM THE EE FIELD TO THE FF FIELD, THE			
AV88	*		TWO FIELDS DID NOT COMPARE.			
AV89	ET5	BCE	ET1,TAD1,1	12	13998	B 13861 01001 1
AV90		B	SCI	7	14010	J 27380
			ROUTINE 98 ERROR			
			LOOP ROUTINE 98			
			STEP ROUTINE COUNTER TO 99			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AV92		*ROUTINE 99-CHECK MRWG, MRWG INSTRUCTIONS.				
AV93	E01	BNQ	ITR	7	14017	J 01334 Q
AV94		MLCWS	2A2,0EX9	12	14024	D 29255 00.0 7
AV95		MLCWA	DD,0EX10	12	14036	D 01911 00.0 X
AV96		MLCWS	2A2,0EX6	12	14048	D 29255 00.0 7
AV97		MRWG	0EX5,0EX6	12	14060	D 00.0 00.0 5
AV98		SBR	EU2E5	7	14072	G 14096 B
AV99		MRWG	0EX5,0EX6	12	14079	D 00.0 00.0 *
AW00	E02	SCNLS	0.100	12	14091	D 00000 00100
AW01		SAR	EU3E10	7	14103	G 14127 A
AW02		SAR	EU4E5	7	14110	G 14133 A
AW03	E03	CW	0EX9,0	11	14117	D 00.0 00000
AW04	E04	C	0.0EX9	11	14128	C 00000 00.0
AW05		BE	EU5	7	14139	J 14154 S
AW06		B	SE1	7	14146	J 27220
AW07		H		1	14153	.
AW08	*	AFTER USING MRWG AND MRWG INSTRUCTIONS TO MOVE				
AW09	*	CONSTANT CC FROM THE EE FIELD TO THE FF FIELD, THE				
AW10	*	TWO FIELDS DID NOT COMPARE.				
AW11	E05	BCE	EU1,TAD1,1	12	14154	B 14017 01001 1
AW12		B	SC1	7	14166	J 27380
						STEP ROUTINE COUNTER TO 100

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 75

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD OPERAND

•ROUTINE100-CHECK MRCWG INSTRUCTION.

AW14	EV1	BNQ	ITR	BRANCH INQUIRY	7	14173	J 01334 Q
AW15		MLCWS	AM2,0EX9	G/M,W/M TO EE FIELD RIGHT	12	14180	D 29255 00.0 7
AW16		MLCWA	DD,0EX10	CONSTANT DD TO FF FIELD RIGHT	12	14192	D 01911 00.0 X
AW17		MLCWS	AM2,0EX6	G/M,W/M TO ADDRESS FF	12	14204	D 29255 00.0 7
AW18		MRCWG	0EX5,0EX6	CONSTANT CC FROM EE TO FF FIELD	12	14216	D 00.0 00.0 L
AW19		SBR	EV2&5	STORE BAR IN SCAN INSTRUCTION	7	14228	G 14240 B
AW20		SCNLS	0,100	CALCULATE ADDRESS MOVE STOPPED ON	12	14235	D 00000 00100
AW21	EV2	SAR	EV3&10	STORE FOR CLEARING WORD MARK	7	14247	G 14271 A
AW22		SAR	EV4&5	STORE FOR COMPARE CHECK	7	14254	G 14277 A
AW23		CW	0EX9,0	CLEAR W/MS OVER G/MS TO ALLOW COM	11	14261	D 00.0 00000
AW24	EV3	C	0,0EX9	COMPARE FF AND EE FIELDS	11	14272	C 00000 00.0
AW25	EV4	BE	EV5	BRANCH-MOVES OK	7	14283	J 14298 S
AW26		B	SE1	BRANCH TO ERROR ROUTINE	7	14290	J 27220
AW27		H		ROUTINE100 ERROR	1	14297	.
AW28							
AW29	•			AFTER USING AN MRCWG INSTRUCTION TO MOVE CONSTANT CC			
AW30	•			FROM THE EE FIELD TO THE FF FIELD, THE TWO FIELDS			
AW31	•			DID NOT COMPARE.			
AW32	EV5	BCE	EV1,1AD1,1	LOOP ROUTINE100	12	14298	B 14173 01001 I
AW33		B	SC1	STEP ROUTINE COUNTER 10101	7	14310	J 27380

PGLIN	LABEL	OPCOD	OPERANO	CT	ADORS	INSTRUCTION
AW35			*ROUTINE101-CHECK SERIAL MOVE LEFT.			
AW36			MLCWA 2000002,C09	12	14317	D 29196 01487 X
AW37			MLCWA 2000002,C08	12	14329	D 29196 01482 X
AW38	EV6		BNQ ITR	7	14341	J 01334 Q
AW39			MLCWA CC,0EX5	12	14348	D 01900 00+0 X
AW40			MLWA CC,1EX5	12	14360	D 01900 00+1 U
AW41			SBR C08	7	14372	G 01482 B
AW42			SCNLA 1EX5,2EX5	12	14379	D 00+1 00+2 B
AW43			SBR C09	7	14391	G 01487 B
AW44			CW 2EX5	6	14398	D 00+2
AW45			MLWB 2EX5,1EX5	12	14404	D 00+2 00+1 M
AW46			SAR X1	7	14416	G 00029 A
AW47			SBR X2	7	14423	G 00034 B
AW48			C X1,C09	11	14430	C 00029 01487
AW49			BE EV7	7	14441	J 14456 S
AW50			B SE1	7	14448	J 27220
AW51			H	1	14455	.
AW52			• AFTER SERIAL MLWB, AAR DID NOT CONTAIN ADDRESS EE &2			
AW53			• MINUS THE LENGTH OF CC. X1 CONTAINS AAR CONTENTS.			
AW54	EV7		C X2,C08	11	14456	C 00034 01482
AW55			BE EV8	7	14467	J 14508 S
AW56			B SE1	7	14474	J 27220
AW57			H	1	14481	.
AW58			• AFTER SERIAL MLWB, BAR DID NOT CONTAIN ADDRESS EE &1			
AW59			• MINUS THE LENGTH OF CC. X2 CONTAINS BAR CONTENTS.			
AW60	EV9		C CC,0EX5	11	14482	C 01900 00+0
AW61			BE EV8	7	14493	J 14508 S
AW62			B SE1	7	14500	J 27220
AW63			H	1	14507	.
AW64			• AFTER SERIAL MLWB, CONSTANT CC DID NOT COMPARE			
AW65			• WITH DATA AT ADDRESS EE.			
AW66	EV8		BCE EV6,TAD1,1	12	14508	B 14341 01001 1
AW67			B SC1	7	14520	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AW69						
AW70						
AW71						
AW72						
AW73						
AW74						
AW75						
AW76						
AW77						
AW78						
AW79						
AW80						
AW81						
AW82						
AW83						
AW84						
AW85						
AW86						
AW87						
AW88						
AW89						
AW90						
AW91						
AW92						
AW93						
AW94						
AW95						
AW96						
AW97						
AW98						
AW99						
AX00						
AX01						
AX02						
AX03						
AX04						
AX05						

*ROUTINE102-CHECK SERIAL MOVE RIGHT.

EW6 BNQ ITR

CW 2&X5,3&X5 FIND ADDRESSES EE&1 & EE&2

SAR C08 SAVE ADDRESS EE &1 IN C08

SBR C09 SAVE ADDRESS EE &2 IN C09

SCNLA DD,2&X5 FIND ADDR EE &2 -DD LENGTH

SBR EW8&10 SAVE FOR USE AS MRCW B FIELD ADDR

SCNLA DD,1&X5 FIND ADDRESS EE&1 MINUS DD LENGHT

SBR EW7&5 SAVE TO CLEAR DD W/M

SBR EW8&5 SAVE FOR USE AS MRCW A FIELD ADDR

SBR EW12&5 SAVE TO MOVE HI OPRFR POSITION

MLCWA DD,0&X5 STORE DD IN ADDRESS EE

CW 0 CW OVER CONSTANT DD

SW 1&X5 SW AT EE &1 TO STOP SERIAL MOVE

MLCS 0,EW10&11 DD HI ORDER TO BCE n MOO FOR CHK

MRCW 0,0 *SERIAL MOVE RIGHT

SAR X1 AAR SHOULD EQUAL EE&1

SBR X2 BAR SHOULD EQUAL EE&2

C X1,C08 CHECK AAR RESULT

BE EW9

B SE1 BRANCH TO ERROR ROUTINE

H ROUTINE102 ERROR

* CONTENTS OF AAR AFTER MRCW DID NOT EQUAL ADDR EE &1.

* AAR CONTENTS ARE STORED IN INDEX REG ONE.00025-00029

EW9 C X2,C09 CHECK BAR RESULT

BE EW10

B SE1 BRANCH TO ERROR ROUTINE

H ROUTINE102 ERROR

* CONTENTS OF BAR AFTER MRCW DID NOT EQUAL ADDR EE &2.

* BAR CONTENTS ARE STORED IN INDEX REG TWO.00030-00034

EW10 BCE EW11,1&X5, BRANCH IF CHAR MOVED SERIALY OK

B SE1 BRANCH TO ERROR ROUTINE

H ROUTINE102 ERROR

* THE SERIAL MRCW SHOULD HAVE MOVED THE HIGH ORDER

* CHARACTER OF CONSTANT DD TO ADDRESS EE PLUS ONE.

* THIS SHOULD HAVE CAUSED THE BCE TO BRANCH.

EW11 BCE EW6,1A01,1 LOOP ROUTINE102

PGLIN	LABEL	OPCODE	OPERAND	STEP ROUTINE COUNTER	TO103	CT	ADDRS	INSTRUCTION
AX06		B	SC1			7	14757	J 27380
AX07	*ROUTINE103-CHECK BCE INSTRUCTION.							
AX08	EW1	BNQ	ITR		BRANCH INQUIRY	7	14764	J 01334 Q
AX09		MLCS	CC,0EX5		RANDOM CHARACTER TO ADDRESS EE	12	14771	D 01900 00##0 3
AX10		SW	0EX5		SET W/M FOR COMPARE CHECK	6	14783	, 00##0
AX11		MLCS	DD,EW2E11		OBTAIN RANDOM D MODIFIER	12	14789	D 01911 14812 3
AX12	EW2	BCE	EW3,0EX5,		CHECK BCE	12	14801	B 14846 00##0
AX13		C	DD,0EX5		SHOULD BCE HAVE BRANCHED	11	14813	C 01911 00##0
AX14		BU	EW4		BRANCH-NO-INSTRUCTION OK	7	14824	J 14872 /
AX15		B	SE1		BRANCH TO ERROR ROUTINE	7	14831	J 27220
AX16		H			ROUTINE103 ERROR	1	14838	.
AX17	*				THE BCE INSTRUCTION DID NOT BRANCH ALTHOUGH THE			
AX18	*				COMPARE INSTRUCTION INDICATED THE CHARACTERFS WERE			
AX19	*				EQUAL.			
AX20		B	EW4		ROUTINE COMPLETE WITH ERROR	7	14839	J 14872
AX21	EW3	C	DD,0EX5		WAS IT OK FOR THE BCE TO BRANCH	11	14846	C 01911 00##0
AX22		BE	EW4		BRANCH-YES-INSTRUCTION OK	7	14857	J 14872 S
AX23		B	SE1		BRANCH TO ERROR ROUTINE	7	14864	J 27220
AX24		H			ROUTINE103 ERROR	1	14871	.
AX25	*				THE BCE INSTRUCTION BRANCHED ALTHOUGH THE COMPARE			
AX26	*				INSTRUCTION INDICATED THE CHARACTERS WERE NOT EQUAL.			
AX27	EW4	BCE	EW1,IAD1,1		LOOP ROUTINE103	12	14872	B 14764 01001 1
AX28		H	SC1		STEP ROUTINE COUNTER TO104	7	14884	J 27380
AX29	*ROUTINE104-CHECK BBE INSTRUCTION.							
AX30	EX1	BNQ	ITR		BRANCH INQUIRY	7	14891	J 01334 Q
AX31		MLCS	CC,0EX5		RANDOM CHARACTER TO ADDRESS EE	12	14898	D 01900 00##0 3
AX32		BBE	EX3,0EX5,M		CHECK BBE	12	14910	W 14949 00##0 M
AX33		BCE	EX4,0EX5,		BRANCH-BBE INSTRUCTION OK	12	14922	B 14961 00##0
AX34	EX2	B	SE1		BRANCH TO ERROR ROUTINE	7	14934	J 27220
AX35		H			ROUTINE104 ERROR	1	14941	.
AX36	*				THE BBE INSTRUCTION FAILED TO BRANCH WHEN IT SHOULD,			
AX37	*				OR BRANCHED WHEN IT SHOULD NOT HAVE.			
AX38		B	EX4		ROUTINE COMPLETE WITH ERROR	7	14942	J 14961
AX39	EX3	BCE	EX2,0EX5,		BRANCH-BBE INSTRUCTION FAILED	12	14949	B 14934 00##0
AX40	EX4	BCE	EX1,IAD1,1		LOOP ROUTINE104	12	14961	B 14891 01001 1
AX41		B	SC1		STEP ROUTINE COUNTER TO105	7	14973	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AX43			*ROUTINE105-CHECK BRANCH ON WORD MARK OR ZONE EQUAL INSTRUCTION.			
AX44	EY1	BNQ	1TR	7	14980	J 01334 Q
AX45		MLCWS	DD,0EX5	12	14987	D 01911 00+0 7
AX46		MLCS	CC,EY5E11	12	14999	D 01900 15245 3
AX47		SCNLA	0EX5,101	12	15011	D 00+0 00101 B
AX48		SBR	C08	7	15023	G 01482 B
AX49		MLCS	a1a,c09	12	15030	D 29167 01487 3
AX50		C	C08,000100a	11	15042	C 01482 29250
AX51		BE	EY2	7	15053	J 15072 S
AX52		MLCS	a a,c09	12	15060	D 29208 01487 3
AX53	EY2	MLZS	DD,EY0	12	15072	D 01911 15274 2
AX54		MLZS	CC,EY8	12	15084	D 01900 15275 2
AX55		MLCS	a1a,c09-1	12	15096	D 29167 01486 3
AX56		C	EY0,EY8	11	15108	C 15274 15275
AX57		BE	EY3	7	15119	J 15138 S
AX58		MLCS	a a,c09-1	12	15126	D 29208 01486 3
AX59	EY3	BCE	EY4,C09,	12	15138	B 15174 01487
AX60		MLCS	a1a,EY9	12	15150	D 29167 15273 3
AX61		BRE	EY5,CC,1	12	15162	W 15234 01900 1
AX62	EY4	MLCS	a a,EY9	12	15174	D 29208 15273 3
AX63		BCE	EY5,C09-1,	12	15186	B 15234 01486
AX64		MLCS	a1a,EY9	12	15198	D 29167 15273 3
AX65		BRE	EY5,CC,2	12	15210	W 15234 01900 2
AX66		MLCS	a a,EY9	12	15222	D 29208 15273 3
AX67	EY5	BWZ	EY6,0EX5,	12	15234	V 15276 00+0 3
AX68		BCE	EY7,EY9,	12	15246	B 15288 15273
AX69	EY1	B	SE1	7	15258	J 27220
AX70		H		1	15265	.
AX71	*		THE RIGHTMOST CHARACTER OF CONSTANT CC WAS USED FOR			
AX72	*		THE D MODIFIER OF THE INSTRUCTION. THE RIGHTMOST			
AX73	*		CHARACTER OF CONSTANT DD WAS USED AS THE CHARACTER			
AX74	*		BEING CHECKED. IF EY9 IS A 1 THE INSTRUCTION FAILED			
AX75	*		TO BRANCH WHEN IT SHOULD. IF EY9 IS BLANK, THE			
AX76	*		INSTRUCTION BRANCHED WHEN IT SHOULD NOT HAVE.			
AX77		B	EY7	7	15266	J 15288
			ROUTINE ENDED WITH ERROR			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
AX79	EY9	DCW	a a	1	15273	YES/NO INDICATOR
AX80	EY0	DCW	a a	1	15274	ZONE STORAGE FOR COMPARISON
AX81	EY8	DCW	a a	1	15275	
AX82	EY6	BCE	EYV1,EY9,	12	15276	BRANCH-ERROR-INSTRUCTION BRANCHED
AX83	EY7	BCE	EY1,IAD1,1	12	15288	LOOP ROUTINE105
AX84		B	SCI	7	15300	STEP ROUTINE COUNTER T0106
AX85	*ROUTINE106-		RECONSTRUCT CONSTANT 00 AS THE MCS INSTRUCTION IN THE			
AX86	*		NEXT ROUTINE SHOULD CO.			
AX87		NCP		1	15307	N
AX88	ERPA	BBE	*C8,SYS1C5,1	12	15308	W 15327 01261 1
AX89		B	EZ1	7	15320	J 15407
AX90		CW	ERPA	6	15327	D 15308
AX91		MLCWA	ERPW,CR5C5	12	15333	D 28733 01770 X
AX92		MLCWA		1	15345	D
AX93		MLCS	ERPW-4,ERP8C11	12	15346	D 28729 23048 3
AX94		CW	ERPW-5	6	15358	D 28728
AX95		SAR	ERPCE5	7	15364	G 23207 A
AX96		MLCWA	ERPX,a,0,-a	12	15371	D 28738 29262 X
AX97		MLCWA	ERPY,a,0,a	12	15383	D 28741 29265 X
AX98		MLCS	a,a,GG2C11	12	15395	D 29266 24267 3
AX99	EZ1	BNQ	ITR	7	15407	J 01334 Q
AY00		MLCWA	DD,EZ9	12	15414	D 01911 15673 X
AY01		SBR	EZ3C10	7	15426	G 15467 B
AY02		MLZS	a,a,EZ9	12	15433	D 29208 15673 2
AY03	EZ2	MLCS	a1a,C08	12	15445	D 29167 01482 3
AY04	EZ3	SCNR	a1a,0	12	15457	D 29167 00000 8
AY05		SBR	EZ3C10	7	15469	G 15467 B
AY06		SBR	EZ4C5	7	15476	G 15520 B
AY07		SBR	EZ6C5	7	15483	G 15589 B
AY08		SBR	EZ8C10	7	15490	G 15636 B
AY09		C	EZ3C10,CEZ12	11	15497	C 15467 29271
AY10		BE	EZ12	7	15508	J 15674 S
AY11	EZ4	MLCS	0,EZ5C11	12	15515	D 00000 15538 3
AY12	EZ5	BCE	EZ11,CR6,0	12	15527	B 15645 01779 0
AY13		BCE		1	15539	B
AY14		BCE		1	15540	B
AY15		BCE		1	15541	B

SBR FOR FIRST ADDRESS
 CLEAR UNITS ZONE
 SET SUPPRESS INDICATOR
 SCAN TO NEXT CHARACTER
 SBR FOR NEXT ADDRESS
 SBR TO CHECK FOR SIG DIG,0--BLANK
 SBR TO CHECK FOR . OR -
 SBR FOR BLANKING CHARACTER
 ARE ALL CHARACTERS CHECKED
 BRANCH-YES
 SET BCE D MODIFIER
 BRANCH-CHAR IS SIG DIGIT 1-9
 DITTO
 DITTO
 DITTO

PGLIN	LABEL	OPCOD	OPERAND	CT	ADRS	INSTRUCTION
AY16		BCE	DITTO	1	15542	B
AY17		BCE	DITTO	1	15543	B
AY18		BCE	DITTO	1	15544	B
AY19		BCE	DITTO	1	15545	B
AY20		RCE	DITTO	1	15546	B
AY21		BCE	BRANCH-CHAR IS 0-0- OR BLANK	6	15547	B 15584
AY22		BCE	DITTO	6	15553	B 15584
AY23		BCE	DITTO	6	15559	B 15584
AY24		BCE	DITTO	6	15565	B 15584
AY25		BCE	DITTO	6	15571	B 15584
AY26		B	START SUPPRESSING	7	15577	J 15445
AY27	EZ6	MLCS	0,EZ7&11	12	15584	D 0000 15607 3
AY28	EZ7	BCE	E73,CR5,0	12	15596	B 15457 01765 0
AY29		RCE	EZ3	6	15608	B 15457
AY30		BCE	EZ3,CO8,	12	15614	B 15457 01482
AY31	EZ8	MLCS	a a,0	12	15626	D 29208 00000 3
AY32		B	EZ3	7	15638	J 15457
AY33	EZ11	MLCS	a a,CO8	12	15645	D 29208 01482 3
AY34		B	EZ3	7	15657	J 15457
AY35	EZ9	DCW	a a	10	15673	
AY36	EZ12	RCE	EZ1,TAD1,1	12	15674	B 15407 01001 1
AY37		B	SC1	7	15686	J 27380
AY38	*ROUTINE107-CHECK MCS INSTRUCTION.					
AY39	FA1	BNQ	ITR	7	15693	J 01334 Q
AY40		MCS	DD,0&X6	11	15700	Z 01911 00+0.0
AY41		C	0&X6,EZ9	11	15711	C 00+0.0 15673
AY42		BE	FA2	7	15722	J 15737 S
AY43		B	SEI	7	15729	J 27220
AY44		H	ROUTINE107 ERROR	1	15736	.
AY45	*	THE RESULT OF THE MCS INSTRUCTION DID NOT COMPARE				
AY46	*	WITH THE RESULT CALCULATED BY THE LAST ROUTINE.				
AY47	FA2	C	EZ9,0&X6	11	15737	C 15673 00+0.0
AY48		BH	FA3	7	15748	J 15763 U
AY49		B	SEI	7	15755	J 27220
AY50		H	ROUTINE107 ERROR	1	15762	.
AY51	*	THE FAILURE OF THE COMPARE TO CAUSE A BRANCH HIGH				
AY52	*	INDICATES THE MCS RESULT IN THE FF FIELD HAD A WORD				

PGLIN	LABEL	OPCODE	OPERAND	MARK. IT SHOULD NOT.	CT	ADDRS	INSTRUCTION
AY53							
AY54	FA3	BCE	FAL,TAD1.1	LOOP ROUTINE107	12	15763	8 15693 01001 1
AY55		8	SC1	STEP ROUTINE COUNTER T0108	7	15775	J 27380
AY56				ROUTINE108-CHECK SW AND CW INSTRUCTIONS.			
AY57	FBI	BNQ	ITR	BRANCH INQUIRY	7	15782	J 01334 Q
AY58		MLCWA	a a,X1		12	15789	D 29165 00029 X
AY59		MLWA	CN8,0EX5	CLEAR ANY W/MS AT ADDR EE	12	15801	D 01418 00+0 U
AY60		MLWA	CN8,0EX6	CLEAR ANY W/MS AT ADDR FF	12	15813	D 01418 00+0 U
AY61		SW	0EX5	SW AT EE	6	15825	00+0
AY62		SW		SW AT EE-1	1	15831	
AY63		SW	0EX6	SW AT FF	6	15832	00+0
AY64		SW	0EX6-1,0EX5-2	SW AT FF-1 AND EE-2	11	15838	992R9 992Z8
AY65		SW		SET W/M AT FF-2 AND EE-3	1	15849	
AY66		BW	*EX,0EX5	IS THERE A W/M AT ADDR EE	12	15850	V 15869 00+0 1
AY67		B	FH3	NO	7	15862	J 15981
AY68		BW	*EX,0EX5-1	IS THERE A W/M AT ADDR EE-1	12	15869	V 15888 992Z9 1
AY69		B	F83	NO	7	15881	J 15983
AY70		BW	*EX,0EX5-2	IS THERE A W/M AT ADDR EE-2	12	15888	V 15907 992Z8 1
AY71		B	F83	NO	7	15900	J 15983
AY72		BW	*EX,0EX5-3	IS THERE A W/M AT ADDR EE-3	12	15907	V 15926 992Z7 1
AY73		B	F83	NO	7	15919	J 15983
AY74		BW	*EX,0EX6	IS THERE A W/M AT ADDR FF	12	15926	V 15945 00+0 1
AY75		B	F83	NO	7	15938	J 15983
AY76		BW	*EX,0EX6-1	IS THERE A W/M AT FF-1	12	15945	V 15964 992R9 1
AY77		B	F83	NO	7	15957	J 15983
AY78		BW	F84,0EX6-2	IS THERE A W/M AT ADDR FF-2	12	15964	V 15998 992R8 1
AY79		B	*E1	NO	7	15976	J 15983
AY80		SBR	X1	SAVE ERROR BRANCH ADDRESS IN X1	7	15983	G 00029 H
AY81		B	SEI	BRANCH TO ERROR ROUTINE	7	15990	J 27220
AY82		H		ROUTINE108 ERROR	1	15997	
AY83				AT LEAST ONE OF THE SW INSTRUCTIONS FAILED. INDEX			
AY84				REG. 1 CONTAINS ERROR BRANCH ADDRESS.			

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CU01	CT	ADDRS	INSTRUCTION
AY86	F84	CW	0EX5		6	1598	00*0
AY87		CW			1	16004	
AY88		CW	0EX6		6	16005	00*0
AY89		CW	0EX6-1,0EX5-2		11	16011	99ZR9 99ZZ8
AY90		CW			1	16022	
AY91		BW	F85,0EX5		12	16023	V 16114 00*0 1
AY92		BW	F85,0EX5-1		12	16035	V 16114 99ZZ9 1
AY93		BW	F85,0EX5-2		12	16047	V 16114 99ZZ8 1
AY94		BW	F85,0EX5-3		12	16059	V 16114 99ZZ7 1
AY95		HW	F85,0EX6		12	16071	V 16114 00*0 1
AY96		BW	F85,0EX6-1		12	16083	V 16114 99ZR9 1
AY97		BW	F85,0EX6-2		12	16095	V 16114 99ZR8 1
AY98		B	F86		7	16107	J 16129
AY99		SR	X1		7	16114	G 00029 8
AZ00	F85	B	SE1		7	16121	J 27220
AZ01		H			1	16128	
AZ02	*			AT LEAST ONE OF THE CW INSTRUCTIONS FAILED. INDEX			
AZ03	*			REG. 1 CONTAINS ERROR BRANCH ADDRESS.			
AZ04	F86	BCE	F81,1A01,1		12	16129	B 15782 01001 1
AZ05		B	SC1	STEP ROUTINE COUNTER T0109	7	16141	J 27380
AZ06	*ROUTINE109-CHECK NOP INSTRUCTION. THE ONLY ERROR INDICATIONS FOR						
AZ07	*			THIS ROUTINE WILL BE PRODUCED BY CPU ALARM CIRCUITS.			
AZ08	FC1	BNQ	ITR	BRANCH INQUIRY	7	16148	J 01334 Q
AZ09		NOP			1	16155	N
AZ10		DC	2 &-/STUVWXYZ08		14	16169	
AZ11		NOP			1	16170	N
AZ12		DC	2#2,1MM*,123456789		17	16187	
AZ13		BCE	FC1,1A01,1	LOOP ROUTINE109	12	16188	B 16148 01001 1
AZ14		B	SC1	STEP ROUTINE COUNTER T0110	7	16200	J 27380

CT ADDR INSTRUCTION

PGLIN

LABLEL

OPCOD

OPERAND

*ROUTINE110-CHECK INDEX REGISTER SELECTION.

BRANCH INQUIRY

BNQ

ITR

G

MLCWS

2MA,X15&1

MRCWG

X1-4,C21

MRCWG

C19,X1-4

C

84&X15,C20

BU

FD2

C

80&X14,C20-5

BU

FD2

C

76&X13,C20-10

BU

FD2

C

72&X12,C20-15

BU

FD2

C

68&X11,C20-20

BU

FD2

C

64&X10,C20-25

BU

FD2

C

60&X9,C20-30

BU

FD2

C

56&X8,C20-35

BU

FD2

C

52&X7,C20-40

BU

FD2

C

48&X6,C20-45

BU

FD2

C

44&X5,C20-50

BU

FD2

C

40&X4,C20-55

BU

FD2

C

36&X3,C20-60

BU

FD2

C

32&X2,C20-65

BU

FD2

C

28&X1,C20-70

BU

FD2

SAVE ALL INDEX REG CONTENTS
LOAD IX REGS WITH REG NUMBERS

BRANCH-REG 15 FAILED

BRANCH-REG 14 FAILED

BRANCH-REG 13 FAILED

BRANCH-REG 12 FAILED

BRANCH-REG 11 FAILED

BRANCH-REG 10 FAILED

BRANCH-REG 9 FAILED

BRANCH-REG 8 FAILED

BRANCH-REG 7 FAILED

BRANCH-REG 6 FAILED

BRANCH-REG 5 FAILED

BRANCH-REG 4 FAILED

BRANCH-REG 3 FAILED

BRANCH-REG 2 FAILED

BRANCH-REG 1 FAILED

7	16207	J	01334	Q
12	16214	D	29255	00100 7
12	16226	D	00025	28550 L
12	16238	D	01922	00025 L
11	16250	C	00MH4	01996
7	16261	J	16527	/
11	16268	C	00MQ0	01991
7	16279	J	16527	/
11	16286	C	00MX6	01986
7	16297	J	16527	/
11	16304	C	00M72	01981
7	16315	J	16527	/
11	16322	C	00F8	01976
7	16333	J	16527	/
11	16340	C	00O4	01971
7	16351	J	16527	/
11	16358	C	00W0	01966
7	16369	J	16527	/
11	16376	C	00S6	01961
7	16387	J	16527	/
11	16394	C	00E2	01956
7	16405	J	16527	/
11	16412	C	00M8	01951
7	16423	J	16527	/
11	16430	C	00U4	01946
7	16441	J	16527	/
11	16448	C	0040	01941
7	16459	J	16527	/
11	16466	C	000C6	01936
7	16477	J	16527	/
11	16484	C	000L2	01931
7	16495	J	16527	/
11	16502	C	000S8	01926
7	16513	J	16527	/

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 85

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRES	INSTRUCTION
AZ52		B	FO4	7	16520	J 16548
AZ53	F02	SBR	FO3&4	7	16527	G 16539 B
AZ54		NOP		1	16534	N
AZ55	F03	OC	00000	5	16535	
AZ56		B	SE1	7	16540	J 27220
AZ57		H		1	16547	.
AZ58	*					ROUTINE110 ERROR
AZ59	*					INDEX REGISTER SELECTION FAILURE. THE ERROR BRANCH
AZ60	*					LOCATION STORED IN F03 INDICATES THE HIGHEST INDEX
AZ61		MRCWG	C21,X1-4	12	16548	O 28550 00025 L
AZ62		BCE	FO1,TAD1,1	12	16560	B 16207 01001 1
AZ63		B	SC1	7	16572	J 27380
AZ64						STEP ROUTINE COUNTER TO111
AZ65		BNQ	1TR	7	16579	J 01334 Q
AZ66		SCNLS	30000,30000	12	16586	O 30000 30000
AZ67		CW	QQ4&1,QQ3&1	11	16598	D 16641 16626
AZ68		DCW	QJ2	1	16609	
AZ69		B	SE1	7	16610	J 27220
AZ70		H		1	16617	.
AZ71	*					ROUTINE111 ERROR
AZ72		B	QQ6	7	16618	J 16673
AZ73		B	SE1	7	16625	J 27220
AZ74		H		1	16632	.
AZ75	*					ROUTINE111 ERROR
AZ76	*					THE CHAINED BRANCH AT QQ2 BRANCHED TO THE CONTENTS
AZ77		B	QQ6	7	16633	J 16673
AZ78		SBR	CO8	7	16640	G 01482 B
AZ79		C	CO8,&QQ5	11	16647	C 01482 29276
AZ80		BE	QQ6	7	16658	J 16673 S
AZ81		B	SE1	7	16665	J 27220
AZ82		H		1	16672	.
AZ83	*					ROUTINE111 ERROR
AZ84	*					AFTER PERFORMING THE CHAINED BRANCH AT QQ2, THE
AZ85		BCE	QQ1,TAD1,1	12	16673	B 16579 01001 1
AZ86		B	SC1	7	16685	J 27380
						BAR CONTENTS WERE NOT EQUAL TO THE ADDRESS OF QQ5.
						LOOP ROUTINE111
						STEP ROUTINE COUNTER TO112

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADORS INSTRUCTION

OPC00 OPERAND

LABEL

PGLIN

*ROUTINE112-CHECK CHAINING OF MOVES.

PGLIN	LABEL	OPC00	OPERAND	CT	ADORS	INSTRUCTION
AZ88	Q07	BNQ	ITR	7	16692	J 01334 Q
AZ89		MLCWA	CC,0EX5	12	16699	D 01900 00*0 X
AZ90		MLCWA	0EX5,0EX6	12	16711	D 00*0 00*0 X
AZ91		SAR	QQ12E5	7	16723	G 16774 A
AZ92		SBR	QQ12L10	7	16730	G 16779 B
AZ93		MLCWA	0EX5,0EX6	12	16737	D 00*0 00*0 X
AZ94	Q09	SW		1	16749	
AZ95		CW		1	16750	
AZ96		MLCWA	CC	6	16751	D 01900
AZ97	Q010	MLCWA	0EX5,0EX6	12	16757	D 00*0 00*0 X
AZ98	Q011	C	0,0	11	16769	C 00000 00000
AZ99	Q012	OCW	AD2	1	16780	
BA00	Q013	C	CC	6	16781	C 01900
BA01	Q014	BE	QQ8	7	16787	J 16802 S
BA02		B	SE1	7	16794	J 27220
BA03		H		1	16801	
BA04						
BA05						
BA06						
BA07						
BA08						
BA09	Q08	BCE	QQ7,TA01.1	12	16802	B 16692 01001 1
BA10		B	SC1	7	16814	J 27380
BA11						
BA12						
BA13	FEL	BNQ	ITR	7	16821	J 01334 Q
BA14		MLCS	212,C026	12	16828	D 29167 01473 3
BA15		C	C02,C025	11	16840	C 01467 01472
BA16		BL	FE2	7	16851	J 16870 T
BA17		MLCS	202,C026	12	16858	D 29166 01473 3
BA18	FE2	BCE	FEL,TA01.1	12	16870	B 16821 01001 1
BA19		B	SC1	7	16882	J 27380

BRANCH INQUIRY
 CC TO ADDRESS EE
 CC FROM EE TO FF
 SAVE NEXT LEFT A ADDRESS
 SAVE NEXT LEFT B ADDRESS
 REPEAT MOVE TO SET D MOD TO X
 STEP AAR & BAR-LEAVE D MOD ALONE
 STEP AAR & BAR-LEAVE D MOD ALONE
 CC TO NEXT ADDRESS LEFT
 REPLACE CC AT FF WITH CC
 STEP AAR & BAR 1-BLANK D MODIFIER
 THIS SCNLS SHOULD STEP AAR&BAR 1
 CHECK COMPLETE CHAIN
 BRANCH-OK
 BRANCH TO ERROR ROUTINE
 ROUTINE112 ERROR

CHAIN FROM QQ9 THROUGH QQ10 SHOULD HAVE PLACED CC
 AT FF, STEPPED BAR TWICE AND PLACED CC AGAIN. CHAIN
 FROM QQ11 THROUGH QQ14 SHOULD HAVE MOVED CC TO FF,
 STEPPED BAR TWICE AND COMPARED EQUAL.
 LOOP ROUTINE112
 STEP ROUTINE COUNTER T0113

*ROUTINE113-FIND OUT IF CONSTANT AA OR CONSTANT BB HAS A LONGER
 FIELD LENGTH.
 BRANCH INQUIRY
 SET CONSTANT LENGTH INDICATOR
 BRANCH-BB IS SHORTER THAN AA
 CLEAR CONSTANT LENGTH INDICATOR
 LOOP ROUTINE113
 STEP ROUTINE COUNTER T01140

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 87

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BA21	ROUTINE114-CHECK ZA, BAV INSTRUCTIONS AND ARITH. OVFLD INDICATOR.					
BA22	FF1	BNQ	ITR	7	16889	J 01334 Q
BA23		MLCWA	a, X4	12	16896	D 29165 00044 X
BA24		MLCWA	a	6	16908	D 29165
BA25		MLCWA	a	6	16914	D 29165
BA26		MLCWA	a	6	16920	D 29165
BA27		MLWA	AA, 0EX5	12	16926	D 01878 00*0 U
BA28		BAV	*E1	7	16938	J 16945 Z
BA29		MLZS	a-a, FF3E11	12	16945	O 29277 17010 2
BA30		BZN	FF2, CC, -	12	16957	V 16981 01900 K
BA31		MLZS	aE2, FF3E11	12	16969	O 29278 17010 2
BA32	FF2	ZA	CC, 0EX5	11	16981	M 01900 00*0
BA33		BAV	FF6	7	16992	J 17166 Z
BA34	FF3	BZN	FF4, 0EX5,	12	16999	V 17019 00*0 2
BA35		B	SE1	7	17011	J 27220
BA36		H		1	17018	.
BA37	*		AFTER OPERATION OF THE ZA INSTRUCTION, THE RESULTANT			
BA38	*		SIGN DID NOT HAVE THE SAME POLARITY AS THE SIGN OF			
BA39	*		CONSTANT CC.			
BA40	FF4	MLZS	AA, 0EX5	12	17019	D 01878 00*0 2
BA41		MLCWA	AA, 0EX6	12	17031	D 01878 00*0 X
BA42		SBR	X3	7	17043	G 00039 B
BA43	FF5	ZA	0EX6	6	17050	M 00*0
BA44		SAR	X1	7	17056	G 00029 A
BA45		SBR	X2	7	17063	G 00034 B
BA46		BAV	FF6	7	17070	J 17166 Z
BA47		C	X1, X3	11	17077	C 00029 00039
BA48		BU	*E12	7	17088	J 17106 /
BA49		C	X2, X3	11	17095	C 00034 00039
BA50		BE	*E9	7	17106	J 17121 S
BA51		B	SE1	7	17113	J 27220
			ROUTINE114 ERROR			
			CONSTANT AA SIGN TO RESULT AT EE			
			CONSTANT AA TO ADDRESS FF			
			SAVE ADDRESS FOR CHECKING			
			ZA CONSTANT AA AT ADDRESS FF			
			SAVE ADDRESSES FOR CHECKING			
			BRANCH-ERROR, ARITH OVFLD ON			
			WAS AAR OK AFTER ZA 0EX6			
			GO IF ERROR			
			WAS BAR OK AFTER ZA 0EX6			
			GO IF OK			
			BRANCH TO ERROR ROUTINE			

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BA53	H			1	17120	ROUTINE114 ERROR
BA54	*		THE CONTENTS OF THE AAR AND/OR BAR WERE INCORRECT AFTER			
BA55	*		THE ZA INSTRUCTION AT FF5. AAR IS IN X1, BAR IS IN X2.			
BA56	*		CORRECT AAR-BAR CONTENTS IS IN X3.			
BA57		MLCWA	0EX6,AANUM	12	17121	D 00+0 28636 X
BA58	C		0EX5,AANUM	11	17133	C 00+0 28636
BA59	BE	FF9		7	17144	J 17188 S
BA60	B	SE1		7	17151	J 27220
BA61	H			1	17158	.
BA62	*		AT FF2, A ZA,A,B INSTRUCTION WAS PERFORMED ON			
BA63	*		CONSTANT CC. AT FF5, A ZA,A INSTRUCTION WAS			
BA64	*		PERFORMED ON CONSTANT AA. THE TWO RESULTS SHOULD			
BA65	*		HAVE BEEN EXACTLY THE SAME. THEY DID NOT COMPARE.			
BA66	*		THIS ERROR WILL CAUSE FAILURE INDICATIONS IN SOME			
BA67	*		FOLLOWING ARITHMETIC CHECK ROUTINES.			
BA68	B	FF9		7	17159	J 17188
BA69	S8R	FF7E5		7	17166	G 17186 B
BA70	B	SE1		7	17173	J 27220
BA71	H			1	17180	.
BA72	*		A BAV INSTRUCTION BRANCHED TO THIS ERROR HALT AFTER			
BA73	*		THE OPERATION OF ONE OF THE TWO ZA INSTRUCTIONS. THE			
BA74	*		ARITHMETIC OVERFLOW INDICATOR SHOULD NOT BE ON.			
BA75	B	0		7	17181	J 00000
BA76	BCE	FF1,TAD1.1		12	17188	B 16889 01001 1
BA77	B	SC1		7	17200	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 89

CT ADDR INSTRUCTION

OPCOO OPERAND

LABEL

PGLIN

*ROUTINE115-CHECK ZS INSTRUCTION.

BA79	FG1	BNO	ITR	BRANCH INQUIRY	7	17207	J 01334 Q
BA80		MLWA	88,0EX5	CONSTANT 88 W/M TO ADDRESS EE	12	17214	O 01889 00*0 0
BA81		MLZS	2EX2,FG3E11	SET BZN D MODIFIER POSITIVE	12	17226	D 29278 17284 2
BA82		BZN	FG2,OD,-	BRANCH-CONSTANT DD IS NEGATIVE	12	17238	V 17262 01911 K
BA83		MLZS	2-2,FG3E11	SET BZN O MODIFIER NEGATIVE	12	17250	D 29277 17284 2
BA84	FG2	ZS	DD,0EX5	ZS CONSTANT DD TO ADDRESS EE	11	17262	: 01911 00*0
BA85	FG3	BZN	FG4,0EX5,	BRANCH-RESULTING SIGN IS OK	12	17273	V 17293 00*0 2
BA86		B	SE1	BRANCH TO ERROR ROUTINE	7	17285	J 27220
BA87		H		ROUTINE115 ERROR	1	17292	.

* AFTER OPERATION OF THE ZS INSTRUCTION, THE RESULTANT

* SIGN DID NOT HAVE THE OPPOSITE POLARITY OF THE SIGN

* OF CONSTANT DD.

BA92	FG4	MLZS	2-2,0EX5	- SIGN TO RESULT AT ADDRESS EE	12	17293	D 29277 00*0 2
BA93		BZN	FG5,8B,E		12	17305	V 17329 01889 8
BA94		MLZS	2EX2,0EX5	& SIGN TO RESULT AT ADDRESS EE	12	17317	O 29278 00*0 2
BA95	FG5	MLCWA	88,0EX6	CONSTANT 88 TO ADDRESS FF	12	17329	D 01889 00*0 X
BA96	FG6	ZS	0EX6	ZS CONSTANT 88 AT ADDRESS FF	6	17341	: 00*0
BA97		C	0EX5,0EX6	CHECK ZS,A,B RESULT AGAINST ZS,A	11	17347	C 00*0 00*0
BA98		BE	FG7	BRANCH-BOTH ZS RESULTS OK	7	17358	J 17380 S
BA99		B	SE1	BRANCH TO ERROR ROUTINE	7	17365	J 27220
BA00		H		ROUTINE115 ERROR	1	17372	.

* AT FG2, A ZS,A,B INSTRUCTION WAS PERFORMED ON

* CONSTANT DD. AT FG6, A ZS,A INSTRUCTION WAS

* PERFORMED ON CONSTANT 88. THE TWO RESULTS SHOULD

* HAVE BEEN EXACTLY THE SAME. THEY DID NOT COMPARE.

* THIS ERROR WILL CAUSE FAILURE INDICATIONS IN SOME

* FOLLOWING ARITHMETIC CHECK ROUTINES.

BA07		B	FG8		7	17373	J 17404
BA08	FG7	MLZS	88,0EX6	88 SIGN TO RESULT	12	17380	D 01889 00*0 2
BA09		MLCWA	0EX6,BBNUM	STORE 88 MINUS ALL 8 BITS	12	17392	D 00*0 28647 X
BA10	FG8	BCE	FG1,TAD1,1	LOOP ROUTINE115	12	17404	B 17207 01001 1
BA11		B	SC1	STEP ROUTINE COUNTER TO116	7	17416	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRESSES	INSTRUCTIONS
8813			*ROUTINE116-CHECK ONE FIELD SUBTRACT AND BZ INSTRUCTION.			
8814	FH1	BNQ	ITR	7	17423	J 01334 Q
8815		MLCWA	BB,0&X5	12	17430	D 01889 00*0 X
8816		SBR	X3	7	17442	G 00039 B
8817		S	0&X5	6	17449	S 00*0
8818		SAR	X1	7	17455	G 00029 A
8819		SBR	X2	7	17462	G 00034 B
8820		BZ	FH2	7	17469	J 17484 V
8821		B	SE1	7	17476	J 27220
8822		H		1	17483	.
8823	*		THE ONE FIELD SUBTRACT OPERATION FAILED TO CAUSE A			
8824	*		BRANCH ON ZERO BALANCE.			
8825	FH2	C	X1,X3	11	17484	C 00029 00039
8826		BU	*G12	7	17495	J 17513 /
8827		C	X2,X3	11	17502	C 00034 00039
8828		BE	*G9	7	17513	J 17528 S
8829		B	SE1	7	17520	J 27220
8830		H		1	17527	.
8831	*		THE CONTENTS OF AAR AND/OR BAR WERE INCORRECT AFTER THE			
8832	*		S 0&X5 INSTRUCTION.AAR IS IN X1, BAR IS IN X2, CORRECT			
8833	*		AAR-BAR CONTENTS IS IN X3.			
8834		MLNWA	BB,0&X5	12	17528	D 01889 00*0 V
8835		SW	0&X5	6	17540	, 00*0
8836		C	BB,0&X5	11	17546	C 01889 00*0
8837		BE	FH3	7	17557	J 17572 S
8838		B	SE1	7	17564	J 27220
8839		H		1	17571	.
8840	*		THE SIGN BIT CONFIGURATION IS NOT THE SAME AS IT WAS			
8841	*		BEFORE THE SUBTRACT OPERATION.			
8842	FH3	BCE	FH1,TA01,1	12	17572	B 17423 01001 1
8843		B	SC1	7	17584	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 91

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
BB45			*ROUTINE117A-CHECK 2 FIELD A00 AND SUBTRACT OPERATIONS WHEN THE A			
BB46			* FIELD IS SHORTER THAN, OR EQUAL TO, THE B FIELD.			
BB47	F11	BCE	FJ1,C026,I	12	17591	B 17963 01473 1
BB48	F12	BNQ	ITR	7	17603	J 01334 Q
BB49		MLCWA	AA,0&X5	12	17610	D 01878 00*0 X
BB50		SBR	X2	7	17622	G 00034 B
BB51		MLCWA	BB,0&X6	12	17629	D 01889 00*0 X
BB52		SBR	F13&10	7	17641	G 17658 B
BB53	F13	MRCW	@ @,0	12	17648	O 29208 00000 M
BB54		SBR	F14&5	7	17660	G 17672 B
BB55	F14	CW	0	6	17667	D 00000
BB56		SAR	*&11	7	17673	G 17690 A
BB57		SCNLS	*,00000	12	17680	O 17691 00000
BB58		SHR	X4	7	17692	G 00044 B
BB59		A	0&X5,0&X6	11	17699	A 00*0 00*0
BB60		SAR	X1	7	17710	G 00029 A
BB61		SBR	X3	7	17717	G 00039 B
BB62		BAV	F15	7	17724	J 17845 Z
BB63		C	X1,X2	11	17731	C 00029 00034
BB64		BU	*&12	7	17742	J 17760 /
BB65		C	X3,X4	11	17749	C 00039 00044
BB66		BE	*&9	7	17760	J 17775 S
BB67		B	SE1	7	17767	J 27220
BB68		H		1	17774	.
BB69	*		THE CONTENTS OF THE AAR AND/OR BAR WERE INCORRECT			
BB70	*		FOLLOWING THE ABOVE A 0&X5,0&X6 INSTRUCTION,X1 CONTAINS			
BB71	*		ACTUAL AAR CONTENTS-X2 CONTAINS CORRECT CONTENTS.X3			
BB72	*		CONTAINS ACTUAL BAR CONTENTS-X4 CONTAINS CORRECT CONTENTS			

ROUTINE117AERROR

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 92

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRESSES	INSTRUCTION
8874		MLCWA	00X6,CAL	12	17775	D 00*0 01451 X
8875		S	00X5,00X6	11	17787	S 00*0 00*0
8876		BAV	F15	7	17798	J 17845 Z
8877		BZ	F19	7	17805	J 17867 V
8878	F112	C	00X6,88NUM	11	17812	C 00*0 28647
8879		BE	F17	7	17823	J 17918 S
8880		B	SE1	7	17830	J 27220
8881		H		1	17837	.
8882	*		AA PLUS 88 MINUS AA DID NOT EQUAL 88. SUM IS STORED			
8883	*		AT CAL, DIFFERENCE IS STORED AT ADDRESS FF.			
8884		B	F17	7	17838	J 17918
8885	F15	S8R	F16	7	17845	G 17860 B
8886		B	SE1	7	17852	J 27220
8887		H		1	17859	.
8888	*		BRANCH ON OVERFLOW OCCURRED FOLLOWING THE ADD OR			
8889	*		SUBTRACT OPERATION. THE B FIELD WAS LONG ENOUGH.			
8890	F16	B	0	7	17860	J 00000
8891	F19	MLZS	CAL,F110&11	12	17867	D 01451 17890 2
8892	F110	BZN	F111,00X6,0	12	17879	V 17899 00*0 2
8893		B	SE1	7	17891	J 27220
8894		H		1	17898	.
8895	*		THE CONFIGURATION OF THE B FIELD SIGN CHANGED DURING			
8896	*		THE SUBTRACT OPERATION ALTHOUGH THE ZERO RESULT			
8897	*		INDICATOR WAS SET.			
8898	F111	MLZS	88NUM,00X6	12	17899	D 28647 00*0 2
8899		B	F112	7	17911	J 17812
8900	F17	C	AA,00X5	11	17918	C 01878 00*0
8901		BE	F18	7	17929	J 17944 S
8902		B	SE1	7	17936	J 27220
8903		H		1	17943	.
8904	*		THE ADD OR SUBTRACT OPERATION CHANGED THE CONTENTS			
8905	*		OF THE A FIELD.			
8906	F18	BCE	F12,YAD1,1	12	17944	B 17603 01001 1
8907		B	FK7	7	17956	J 18503

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADDR INSTRUCTION

PGLIN

OPCODE OPERAND

LABEL

*ROUTINE117B-CHECK 2 FIELD ADD OPERATION WHEN THE A FIELD IS LONGER

* THAN THE B FIELD.

FJ1

BNQ ITR

BRANCH INQUIRY

7 17963 J 01334 Q

FJ1

MLCWA AA,0EX5

CONSTANT AA TO ADDRESS EE

12 17970 D 01878 00*0 X

FJ1

MLCWA BB,0EX6

CONSTANT BB TO ADDRESS FF

12 17982 D 01889 00*0 X

FJ1

A 0EX5,0EX6

ADD AA TO 88

11 17994 A 00*0 00*0

FJ5

BAV FJ2

BRANCH-OVFLO INDICATOR TURNED ON

7 18005 J 18094 Z

FJ5

MLCWA 0EX6,CAL

SAVE SUM IN CAL

12 18012 D 00*0 01451 X

FJ5

S 0EX5,0EX6

CHECK ADDITION

11 18024 S 00*0 00*0

FJ5

8Z FJ7

BRANCH ON ZERO RESULT

7 18035 J 18075 V

FJ8

C 0EX6,B8NUM

BRANCH-ADDITION,SUBTRACTION OK

11 18042 C 00*0 28647

FJ8

BE FJ6

BRANCH TO ERROR ROUTINE

7 18053 J 18157 S

FJ8

B SE1

ROUTINE117BERROR

7 18060 J 27220

FJ8

H

RESULT OF ADD OPERATION WAS INCORRECT. RESULT IS

1 18067

FJ6

STORED IN LOCATION CAL.

ROUTINE ENDED WITH ERROR

7 18068 J 18157

FJ7

MLZS B8NUM,0EX6

CORRECT ZERO RESULT SIGN

12 18075 D 28647 00*0 2

FJ8

B FJ8

INSERT 1 BECAUSE OF OVERFLOW

7 18087 J 18042

FJ2

SCNLA 0EX6,1EX6

12 18094 D 00*0 00*0 1 B

FJ2

SAR FJ4&10

7 18106 G 18136 A

FJ3

SBR FJ3&5

7 18113 G 18125 B

FJ3

CW 0

6 18120 D 00000

FJ4

MLCWS 212,0

12 18126 D 29167 00000 7

FJ4

MLWA 88,0EX5

12 18138 D 01889 00*0 U

FJ5

B FJ5

RETURN TO CHECK SUM

7 18150 J 18012

FJ6

8CE FJ1,TA01,1

LOOP ROUTINE117B

12 18157 B 17963 01001 1

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADORS	INSTRUCTION
8C60	FK5	MLCHA	00X6,CA1	12	18302	D 00X6 01451 X
8C61		A	00X5,00X6	11	18314	A 00X6 00X6
8C62		BZ	FK10	7	18325	J 18447 V
8C63	FK9	C	00X6,8BNUM	11	18332	C 00X6 28647
8C64		BE	FK6	7	18343	J 18491 S
8C65		B	SE1	7	18350	J 27220
8C66		H		1	18357	.
8C67	*		RESULT OF SUBTRACT OPERATION WAS INCORRECT. RESULT			
8C68	*		IS STORED IN LOCATION CAL.			
8C69		B	FK6	7	18358	J 18491
8C70	FK8	MLZS	BBNUM,00X6	12	18365	O 28647 00X6 2
8C71		B	FK9	7	18377	J 18332
8C72	FK2	SCNLA	00X6,10X6	12	18384	O 00X6 00X6 1 B
8C73		SAR	FK4010	7	18396	G 18426 A
8C74		SBR	FK305	7	18403	G 18415 B
8C75	FK3	CW	0	6	18410	D 00000
8C76	FK4	MLCWS	010,0	12	18416	O 29167 00000 7
8C77		MLWA	88,00X5	12	18428	O 01889 00X6 U
8C78		B	FK5	7	18440	J 18302
8C79	FK10	ZA	BB,CA2	11	18447	M 01889 01462
8C80		BZ	FK11	7	18458	J 18472 V
8C81		B	FK9	7	18465	J 18332
8C82	FK11	MLZS	CA2,00X6	12	18472	O 01462 00X6 2
8C83		B	FK9	7	18484	J 18332
8C84	FK6	8CE	FK1,1AD1,1	12	18491	B 18169 01001 1
8C85	FK7	B	SC1	7	18503	J 27380
			LOOP ROUTINE117C			
			STEP ROUTINE COUNTER 10110			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRESSES	INSTRUCTIONS
BC87		*ROUTINE118-CHECK 1 FIELD ADD OPERATION.				
BC88	FL1	BNQ	ITR	7	18510	J 01334 Q
BC89		MLCWA	AA,0EX5	12	18517	D 01878 00*0 X
BC90		A	0EX5	6	18529	A 00*0
BC91		BAV	FL2	7	18535	J 18598 Z
BC92	FL7	MLCWA	0EX5,CAL	12	18542	D 00*0 01451 X
BC93		S	AA,0EX5	11	18554	S 01878 00*0
BC94		C	0EX5,AANUM	11	18565	C 00*0 28636
BC95		BE	FL8	7	18576	J 18649 S
BC96		B	SE1	7	18583	J 27220
BC97		H		1	18590	.
		ROUTINE118 ERROR				
	*	RESULT OF ADDITION INCORRECT. SUM STORED IN CAL.				
BC98		B	FL8	7	18591	J 18649 S
BC99		SCNLA	0EX5,1EX5	12	18598	D 00*0 00*1 B
BD00	FL2	SAR	FL4&10	7	18610	G 18640 A
BD01		SBR	FL3&5	7	18617	G 18629 B
BD02		CW	0	6	18624	D 00000
BD03	FL3	MLCWS	012,0	12	18630	D 29167 00000 7
BD04	FL4	B	FL7	7	18642	J 18542
BD05		BCE	FL1,TAD1,1	12	18649	B 18510 01001 1
BD06	FL8	B	SC1	7	18661	J 27380
BD07		STEP ROUTINE COUNTER TO119				

BRANCH INQUIRY
CONSTANT AA TO ADDRESS EE

BRANCH-OVFLO INDICATOR TURNED ON
SAVE SUM IN CAL
CHECK ADDITION

BRANCH-ADDITION, SUBTRACTION OK
BRANCH TO ERROR ROUTINE

ROUTINE118 ERROR

ROUTINE ENDED WITH ERROR
INSERT 1 BECAUSE OF OVERFLOW

RETURN TO ROUTINE
LOOP ROUTINE118
STEP ROUTINE COUNTER TO119

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD OPERAND

*ROUTINE119-CHECK 1 FIELD ZS OPERATION.

8D09	FN1	BNQ	ITR	BRANCH INQUIRY	7	18668	J	01334	Q
8D10		MLCWA	BB,0EX5	CONSTANT BB TO ADDRESS EE	12	18675	D	01889	00*+0 X
8D11		MLNS	BB,FN6E11		12	18687	D	01889	18734 1
8D12		MLCS	BB,FN3E11	INSERT BB UNITS IN ACE CHK INST	12	18699	D	01889	18764 3
8D13		MLZS	2E2,FN3E11	INSERT PLUS IN BCE CHECK INSTRUCT	12	18711	D	29278	18764 2
8D14		BCE	FN2,BB,-	BRANCH-BB IS NEGATIVE	12	18723	B	18747	01889 -
8D15	FN6	MLZS	2-2,FN3E11	INSERT MINUS IN BCE CHK INSTRUCT	12	18735	D	29277	18764 2
8D16		ZS	0EX5	ZS CONSTANT BB IN ADDR EE	6	18747	,	00*+0	
8D17	FN2	BCE	FN4,0EX5,0	BRANCH-RESULTANT SIGN CORRECT	12	18753	B	18773	00*+0 0
8D18	FN3	B	SE1	BRANCH TO ERROR ROUTINE	7	18765	J	27220	
8D19		H		ROUTINE119 ERROR	1	18772	.		

THE RESULT OF THE ZS INSTRUCTION HAD AN INCORRECT

SIGN.

8D22	FN4	MLZS	BB,0EX5	BB SIGN TO ZS RESULT	12	18773	D	01889	00*+0 2
8D23		C	8BNUM,0EX5	CHECK RESULTANT NUMERICS	11	18785	C	28647	00*+0
8D24		BE	FN5	BRANCH-NUMERICS OK	7	18796	J	18811	S
8D25		B	SE1	BRANCH TO ERROR ROUTINE	7	18803	J	27220	
8D26		H		ROUTINE119 ERROR	1	18810	.		

THE RESULT OF THE ZS INSTRUCTION WAS INCORRECT.

LOOP ROUTINE119

8D28	FN5	BCE	FN1,IAD1,1	STEP ROUTINE COUNTER TO120	12	18811	B	18668	01001 1
8D29		B	SC1		7	18823	J	27380	

8D30

1410/7010 CPU RELIABILITY TEST-40X & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8032			*ROUTINE120-CALCULATE RESULT OF CONSTANT BB DIVIDED BY CONSTANT AA.			
8033	FQ1	BNQ	ITR	7	18830	J 01334 Q
8034		MLNWA	AANUM,DIVISR	12	18837	D 28636 19354 V
8035		MLCWA	CS1,SIMDVD	12	18849	D 28668 19375 X
8036		MLNA	BBNUM,SIMDVD	12	18861	D 28647 19375 /
8037		SBR	FQ3&10	7	18873	G 19003 B
8038		SBR	FQ14&10	7	18880	G 19014 B
8039		SBR	FQ5&5	7	18887	G 19118 B
8040		SBR	FQ7&10	7	18894	G 19026 B
8041		SBR	FQ8&10	7	18901	G 19212 B
8042		SBR	FQ13&5	7	18908	G 19219 B
8043		A	Q1,FQ7&10	11	18915	A 29202 19026
8044		MLCWA	CS1-10,LSTTRL	12	18926	D 28658 19398 X
8045		MLCWA	LSTTRL	6	18938	D 19398
8046		MLCWA	CS1,QUOREM	12	18944	D 28668 19420 X
8047		SCNLA	AA,QUOREM	12	18956	D 01B78 19420 B
8048		SCNLA	BB	6	18968	D 01889
8049		SBR	FQ6&10	7	18974	G 19182 B
8050		MLZS	BB,QUOREM	12	18981	D 01889 19420 2
8051	FQ3	S	LSTTRL,0	11	18993	S 19398 00000
8052	FQ14	MLZS	@ 2,0	12	19004	D 29208 00000 2
8053	FQ7	BCE	FQ8,0,*	12	19016	B 19202 00000 *
8054		MRCR	SIMDVD-1B,SIMDVD-19	12	19028	D 19357 19356 *
8055		MLCWA	CS1-10,LSTTRL	12	19040	D 28658 19398 X
8056		MLNWA	DIVISR,TRLDVS	12	19052	D 19354 19387 V
8057		SBR	FQ4&10	7	19064	G 19099 H
8058		CW	TRLDVS	6	19071	D 19387
8059		MLWB	CS1,TRLDVS	12	19077	D 28668 19387 M

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 99

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
BD61	FQ4	MLCWS	222,0	12	19089	D 29166 00000 7
BD62		MLCS	222,TRLDIG	12	19101	D 29166 19399 3
BD63	FQ5	C	0,TRLDVS	11	19113	C 00000 19387
BD64		BH	FQ6	7	19124	J 19172 U
BD65		A	E1,TRLOIG	11	19131	A 29202 19399
BD66		MLCA	TRLDVS,LSITRL	12	19142	D 19387 19398 1
BD67		A	DIVISR,TRLOVS	11	19154	A 19354 19387
BD68		B	FQ5	7	19165	J 19113
BD69	FQ6	MLCS	TRLDIG,0	12	19172	D 19399 00000 3
BD70		A	E1,FQ6E10	11	19184	A 29202 19182
BD71		B	FQ3	7	19195	J 18993
BD72	FQ8	MLWA	AA,0	12	19202	D 01878 00000 U
BD73	FQ13	MLNA	0,QUOREM	12	19214	D 00000 19420 /
BD74		SBR	*E11	7	19226	G 19243 H
BD75		SCNLS	100,0	12	19233	D 00100 00000
BD76		SBR	FQ9E10	7	19245	G 19269 B
BD77		SBR	FQ11E10	7	19252	G 19317 B
BD78	FQ9	MLZS	222,0	12	19259	D 29278 00000 2
BD79		MLZS	AA,FQ10E11	12	19271	D 01878 19306 2
BD80		MLNS	BB,FQ10E11	12	19283	D 01889 19306 1
BD81	FQ10	HCE	FQ12,88,0	12	19295	B 19319 01889 0
BD82	FQ11	MLZS	2-2,0	12	19307	D 29277 00000 2
BD83	FQ12	BCE	FQ1,TA01,1	12	19319	B 18830 01001 1
BD84		B	SC1	7	19331	J 27380
BD85		B	FRI	7	19338	J 19421
BD86	DIVISR	DCW	2	10	19354	
BD87	SIMDVS	DCW	2	21	19375	
BD88		DCW	2+2	1	19376	
BD89	TRLDVS	DCW	2	11	19387	
BD90	LSITRL	DCW	2	11	19398	
BD91	TRLDIG	DCW	2	1	19399	
BD92	QUOREM	DCW	2	21	19420	

CLEAR TRIAL DIGIT

CDM SHIFTD DIVND TO TRIAL DVSR

BRANCH-THIS DIGIT FOUND

ADD ONE TO TRIAL DIGIT

TRIAL DIVSOR TO LAST OK TRL DIVSR

ADD DIVISOR TO TRIAL DIVSOR

TRIAL DIGIT TO QUDTIENT

STEP QUOTIENT ADDRESS RIGHT ONE

FIND NEXT DIGIT OF QUOTIENT

DIVISOR W/M TO SHIFTD DIVND

REMAINDER TO REMAINDER AREA

STEP BAR LEFT ONE

STORE UNITS ADDRESS DF QUDTIENT

DITTO

SET PROPER SIGN IN QUDTIENT

LDOOP ROUTINE120

STEP ROUTINE COUNTER TO121

TO NEXT ROUTINE

DIVISOR-CONST AA MINUS SIGN

2 SIMULATED DIVIDEND

STOP INDICATOR

TRIAL DIVISOR

LAST OK TRIAL DIVISOR

TRIAL DIGIT

2 QUDTIENT-REMAINDER SIM AREA

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BD94		*ROUTINE121-CHECK DIVIDE INSTRUCTION.				
BD95	FR1	BNQ	ITR	7	19421	J 01334 Q
BD96		MLCWA	CS1,0&X5	12	19428	D 28668 00*0 X
BD97		SCNLA	AA,0&X5	12	19440	D 01878 00*0 B
BD98		SCNLA	BB	6	19452	D 01889
BD99		SBR	FR2&5	7	19458	G 19470 B
BE00	FR2	SW	O	6	19465	00000
BE01		ZA	B8,0&X5	11	19471	M 01889 00*0 S
BE02		SCNLA	BB,1&X5	12	19482	D 01889 00*1 B
BE03		SHR	FR3&10	7	19494	G 19530 B
BE04		MLCWA	AA,0&X6	12	19501	D 01878 00*0 X
BE05		BDV	*&1	7	19513	J 19520 W
BE06	FR3	D	0&X6,0	11	19520	X 00*0 00000
BE07		BDV	FR5	7	19531	J 19571 W
BE08	FR6	C	QUOREM,0&X5	11	19538	C 19420 00*0
BE09		RE	FR7	7	19549	J 19626 S
BE10		B	SE1	7	19556	J 27220
BE11		H		1	19563	.
BE12	*		THE QUOTIENT-REMAINDER FIELD DID NOT COMPARE WITH			
BE13	*		THE ANSWER CALCULATED, AND STORED AT ADDRESS LABELED			
BE14	*		QUOREM, BY THE LAST ROUTINE.			
BE15	FR4	B	FR7	7	19564	J 19626
BE16	FR5	BDV	FR8	7	19571	J 19611 W
BE17	FR9	ZA	AA,0&X6	11	19578	M 01878 00*0
BE18		BZ	FR4	7	19589	J 19564 V
BE19		B	SE1	7	19596	J 27220
BE20		H		1	19603	.
BE21	*		THE DIVIDE OPERATION TURNED ON THE DIVIDE OVERFLOW			
BE22	*		INDICATOR WHEN IT SHOULD NOT HAVE.			
BE23		B	FR6	7	19604	J 19538
BE24	FR8	B	SE1	7	19611	J 27220
BE25		H		1	19618	.
BE26	*		THE BDV FAILS TO TURN OFF DIVIDE OVERFLOW.			
BE27		B	FR9	7	19619	J 19578
BE28	FR7	BCE	FR1,TAD1.1	12	19626	B 19421 01001 1
BE29		B	SC1	7	19638	J 27380

PGLIN	LABEL	OPC00	OPERANO	CT	ADRS	INSTRUCTION
BE31						
BE32						
BE33						
BE34						
BE35						
BE36						
BE37						
BE38						
BE39						
BE40						
BE41						
BE42						
BE43						
BE44						
BE45						
BE46						
BE47						
BE48						
BE49						
BE50						
BE51						
BE52						
BE53						
BE54						
BE55						
BE56						
BE57						
BE58						
BE59						
BE60						
BE61						
BE62						
BE63						
BE64						
BE65						

*ROUTINE122-CHECK MULTIPLY INSTRUCTION.

F01 BNQ ITR BRANCH INQUIRY

MLCWA CS1,F04&20 CLEAR PRODUCT STORAGE

MLCWA CS1,0&X5 CLEAR PRODUCT FIELD AT ADDRESS EE

MLCWA CS1-8

SCNLA AA,0&X5-1 FINO MULTIPLIER ADDRESS

SBR F02&10 STORE MULTIPLIER ADDRESS

SBR F03&5 STORE AS QUOTIENT ACOR FOR CHECK

SBR F05&10 STORE FOR DIVIOE REMAINDER CHECK

MLCWA 88,0 STORE CONST 88 AS MULTIPLIER

MLCWA AA,0&X6 STORE AA AT FF AS MULTIPLICAND

M 0&X6,0&X5 MULTIPLY AA BY 88

MLCWA 0&X5,F04&20 STORE PRODUCT

SCNLA 0&X5,1&X5 LEGTHEN FIELD FOR DIVIOE CHECK

SBR F08&10 STORE DIVIOENO ADDRESS

CN

SAR *&11

MLWA AA,0

O 0&X6,0

BOV F07

C O,88NUM

BE F05

B SEI

H

ROUTINE122 ERROR

* THE PRODUCT OF AA AND 88 DIVIOEO BY AA DID NOT EQUAL

* 88. THE PRODUCT IS STORED IN ADDRESS LABELED F04.

B F05

DCW 2

F04 2 PRODUCT STORAGE

F07 F04&20,0&X6 IS PRODUCT ZERO

BZ F05

B SEI

H

ROUTINE122 ERROR

* DIVIDING THE PRODUCT BY THE MULTIPLICAND AA, CAUSED

* A DIVIOE OVERFLOW. THIS INDICATES AA IS ZERO AND THE

* PRODUCT SHOULD BE ZERO. THE PRODUCT IS NOT ZERO.

PGLIN	LA8EL	OPCOD	OPERANO	CT	ADORS	INSTRUCTION
8E67	FD5	MRW	CS1,0	12	19899	0 2866B 00000 @
8E68		MLZS	CS1,0EX5	12	19911	D 2866B 00+0 2
8E69		C	CS1,0EX5	11	19923	C 2866B 00+0
8E70		8E	FO6	7	19934	J 19949 S
8E71		8	SE1	7	19941	J 27220
8E72		H		1	19948	.
*			THE RESULT OF DIVIDING THE PRODUCT OF AA AND BB BY			
*			AA HAD A REMAINDER OTHER THAN ZERO.			
8E75	FO6	BCE	FO1, IAD1,1	12	19949	8 19645 01001 1
8E76		8	SC1	7	19961	J 27380
8E77			STEP ROUTINE COUNTER TO123			
8E78	FPI	8NQ	ITR	7	19968	J 01334 Q
8E79		MLCWA	00,0EX5	12	19975	D 01911 00+0 X
8E80		MLCWA	0EX5,0EX6	12	19987	D 00+0 00+0 X
8E81		CW	0EX6	6	19999	D 00+0
8E82		MLWA	00,0EX6-1	12	20005	0 01911 992R9 U
8E83		C	0EX6,0EX5	11	20017	C 00+0 00+0
8E84		8E	FP2	7	20028	J 20050 S
8E85		8	SE1	7	20035	J 27220
8E86		H		1	20042	.
*			ADDRESS FF DID NOT COMPARE WITH ADDRESS EF. ADDR EE			
*			CONTAINS CONSTANT OD. AOR FF CONTAINS CONST ON			
*			WITH THE WORD MARK MOVED ONE POSITION LEFT.			
8E90		8	FP4	7	20043	J 20086
8E91	FP2	8H	FP3	7	20050	J 20078 U
8E92		BL	FP3	7	20057	J 20078 T
8E93		BU	FP3	7	20064	J 20078 /
8E94		B	FP4	7	20071	J 20086
8E95	FP3	B	SE1	7	20078	J 27220
8E96		H		1	20085	.
*			THE ABOVE COMPARE SET THE HIGH AND/OR LOW AND/OR			
*			UNEQUAL INDICATOR.			
*			IT SHOULD HAVE SET ONLY THE EQUAL INDICATOR.			
8F00	FP4	C	0EX5,0EX6	11	20086	C 00+0 00+0
8F01		8H	FP5	7	20097	J 20119 U

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01

ADORS

CT

OPC00 OPERANO

LABEL

PGLIN

BF03	B	SE1	BRANCH TO ERROR ROUTINE	7	20104	J 27220
BF04	H		ROUTINE123 ERROR	1	20111	.
BF05	*		COMPARING ADDRESS EE WITH ADDRESS FF DID NOT SET THE			
BF06	*		COMPARE HIGH INDICATOR. THE FIELD AT ADDR FF IS ONE			
BF07	*		CHARACTER LONGER THAN THE FIELD AT ADDRESS EE.			
BF08	B	FP7		7	20112	J 20148
BF09	BE	FP6		7	20119	J 20140 S
BF10	BL	FP6		7	20126	J 20140 T
BF11	BU	FP7	BRANCH-OK	7	20133	J 20148 /
BF12	B	SE1	BRANCH TO ERROR ROUTINE	7	20140	J 27220
BF13	H		ROUTINE123 ERROR	1	20147	.
BF14	*		THE ABOVE COMPARE SET THE EQUAL AND/OR LOW			
BF15	*		INDICATOR OR FAILED TO SET THE HIGH AND/OR UNEQUAL			
BF16	*		INDICATOR.			
BF17	FP7	MLCWA	00 TO ADDRESS FF	12	20148	D 01911 00#0 X
BF18		MLCS	MAKE 00 AT EE HIGH	12	20160	D 29279 00#0 3
BF19		MLCS	MAKE 00 AT FF LOW	12	20172	D 29208 00#0 3
BF20	C	0EX5,0EX6		11	20184	C 00#0 00#0
BF21	BL	FP8	BRANCH-OK	7	20195	J 20217 T
BF22	B	SE1	BRANCH TO ERROR ROUTINE	7	20202	J 27220
BF23	H		ROUTINE123 ERROR	1	20209	.
BF24	*		THE LOW INDICATOR SHOULD HAVE BEEN SET BY THE ABOVE			
BF25	*		COMPARE OPERATION.			
BF26	B	FP10		7	20210	J 20246
BF27	BH	FP9		7	20217	J 20238 U
BF28	BE	FP9		7	20224	J 20238 S
BF29	BU	FP10	BRANCH-OK	7	20231	J 20246 /
BF30	B	SE1	BRANCH TO ERROR ROUTINE	7	20238	J 27220
BF31	H		ROUTINE123 ERROR	1	20245	.
BF32	*		THE ABOVE COMPARE SET THE HIGH AND/OR EQUAL			
BF33	*		INDICATOR OR FAILED TO SET THE LOW AND/OR UNEQUAL			
BF34	*		INDICATOR.			
BF35	BCE	FP1,TA01,1	LOOP ROUTINE123	12	20246	B 19968 01001 1
BF36	B	SCI	STEP ROUTINE COUNTER TO124	7	20258	J 27380

PGLIN	LABEL	OPC00	OPERAND	CT	ADDRS	INSTRUCTION
BF38			*ROUTINE124-CHECK CS INSTRUCTION.			
BF39	FS1	BNQ	ITR	7	20265	J 01334 Q
BF40		CS	0EX5	6	20272	/ 00*0
BF41		SBR	X1	7	20278	G 00029 B
BF42		C	X1,0992	11	20285	C 00029 29281
BF43		BE	FS2	7	20296	J 20311 S
BF44		B	SE1	7	20303	J 27220
BF45		H		1	20310	.
BF46			CLEAR STORAGE INSTRUCTION STOPPED ON WRONG ADDRESS.			
BF47			THE LAST ADDRESS CLEARED MINUS ONE IS STORED IN X1.			
BF48	FS2	MLNA	EE,FS3&10	12	20311	D 01916 20333 /
BF49	FS3	B8E	FS5,0,M	12	20323	W 20393 00000 M
BF50		SBR	FS3&10	7	20335	G 20333 B
BF51		SHR	X1	7	20342	G 00029 B
BF52		C	X1,0992	11	20349	C 00029 29281
BF53		BU	FS3	7	20360	J 20323 /
BF54	FS4	CS	FS6,0EX5	11	20367	/ 20408 00*0
BF55		B	SE1	7	20378	J 27220
BF56		H		1	20385	.
BF57			THE CLEAR STORAGE AND BRANCH INSTRUCTION FAILED TO			
BF58			BRANCH.			
BF59		B	FS6	7	20386	J 20408
BF60	FS5	B	SE1	7	20393	J 27220
BF61		H		1	20400	.
BF62			THE FIRST CLEAR STORAGE INSTRUCTION FAILED TO CLEAR			
BF63			STORAGE. THE HIGHEST ADDRESS NOT CLEARED IS STORED			
BF64			IN INDEX REG 1.			
BF65		B	FS4	7	20401	J 20367
BF66	FS6	BCE	FS1,YAD1,1	12	20408	B 20265 01001 1
BF67		B	SC1	7	20420	J 27380
			STEP ROUTINE COUNTER TO125			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8F69			*ROUTINE125-FORM HIGH, LOW AND EQUAL CONSTANTS FOR TABLE LOOKUP			
8F70	*		INSTRUCTION CHECK ROUTINES.			
8F71	FT1	BNQ	ITR	7	20427	J 01334 Q
8F72		MLWA	CR4&16.EQUAL	12	20434	D 01733 20676 U
8F73		MLWA	CR4&16	6	20446	D 01733
8F74		MLWA	CR4&16	6	20452	O 01733
8F75		MLCA	DD.HIGH	12	20458	O 01911 20642 T
8F76		SBR	FT2&10	7	20470	G 20487 B
8F77	FT2	MLCWA	HIIND.O	12	20477	D 20693 00000 X
8F78		MLCA	DD.LOW	12	20489	D 01911 20659 T
8F79		SBR	FT3&10	7	20501	G 20518 B
8F80	FT3	MLCWA	LOIND.O	12	20508	O 20698 00000 X
8F81		MLCA	DD.EQUAL	12	20520	D 01911 20676 T
8F82		SBR	FT4&10	7	20532	G 20549 B
8F83	FT4	MLCS	AA.O	12	20539	D 01878 00000 3
8F84		SBR	FT5&10	7	20551	G 20568 B
8F85	FT5	MLCWA	EQIND.O	12	20558	D 20704 00000 X
8F86		CW	SEARCH	6	20570	B 20687
8F87		MLWA	DD.SEARCH-1	12	20576	D 01911 20686 U
8F88		MLC8	EQUAL.SEARCH	12	20588	D 20676 20687 L
8F89		BCE	FT1.TAD1.1	12	20600	B 20427 01001 1
8F90		B	SCI	7	20612	J 27380
8F91		B	FUI	7	20619	J 20731
8F92			*TABLE LOOKUP CHECK CONSTANTS. THE LEFT PORTION OF THE HIGH, LOW			
8F93	*		AND EQUAL CONSTANTS ARE FUNCTIONS DESCRIBING THE RIGHT			
8F94	*		PORTION WHICH IS THE TABLE ARGUMENT.			
8F95	HIGH	DC	@HIGH	17	20642	@ DD WITH CHAR 9 PLACED AT LEFT
8F96	LOW	DC	@LOW	17	20659	@ DD WITH BLANK PLACED AT LEFT
8F97	EQUAL	DC	@EQUAL	17	20676	@ DD WITH AA UNITS PLACED AT LEFT
8F98	SEARCH	DCW	@	11	20687	@ DD WITH AA UNITS PLACED AT LEFT
8F99	HIIND	DCW	@HIGH.9@	6	20693	
8G00	LOIND	DCW	@LOW.@	5	20698	
8G01	EQIND	DCW	@EQUAL.@	6	20704	
8G02	FSIND	DCW	@FIRST.6.@	8	20712	
8G03	ENIND	DCW	@END.@	4	20716	
8G04	FOUND	DCW	@	14	20717	@ FUNCTION LOOKUP STOPPED AT

PGLIN	LABEL	OPCDD	OPERAND	CT	ADDRS	INSTRUCTION
BG06			*ROUTINE126-FORM TABLE OF HIGH CONSTANTS WITH A LOW FOLLOWED BY AN			
BG07	*		EQUAL FOR USE BY NEXT ROUTINE.			
BG08	FU1	BNQ	ITR	7	20731	J 01334 Q
BG09		MLCWA	HIGH,39998	12	2073B	D 20642 3999B X
BG10		SBR	FU2&10	7	20750	G 20779 B
BG11		MLWA	HIGH,39997	12	20757	D 20642 39997 U
BG12		MLCWA	FSIND,0	12	20769	D 20712 00000 X
BG13	FU2	MLCWA	HIGH	6	20781	D 20642
BG14		MLCWA	HIGH	6	20787	D 20642
BG15		MLCWA	LOW	6	20793	D 20659
BG16		MLCWA	HIGH	6	20799	D 20642
BG17		MLCWA	HIGH	6	20805	D 20642
BG18		MLCWA	EQUAL	6	20811	D 20676
BG19		MLCWA	HIGH	6	20817	D 20642
BG20		MLCWA	HIGH	6	20823	D 20642
HG21		SW		1	20829	*
HG22		SBR	FU3&10	7	20830	G 20847 B
BG23	FU3	MLCWA	ENIND,0	12	20837	D 20716 00000 X
BG24		BCE	FU1,TAD1,1	12	20849	B 20731 01001 1
HG25		B	SC1	7	20861	J 27380
BG26			*ROUTINE127-CHECK LE, LLE, LOOKUP TO ANY, AND LOOKUP TO END			
BG27	*		INSTRUCTIONS USING TABLE GENERATED BY LAST ROUTINE.			
HG28	FV1	BNQ	ITR	7	20868	J 01334 Q
BG29		LE	SEARCH,3999B *** LOOKUP EQUAL ***	12	20875	T 20687 39998 2
BG30		SBR	X1	7	20887	G 00029 B
BG31		SBR	FV2&5	7	20894	G 20913 B
BG32		SBR	FV3&10	7	20901	G 20957 B
BG33	FV2	MLC	O,FOUNDE13	12	20908	D 00000 20730 C
BG34		MLC	CS2	6	20920	D 2867B
BG35		BU	FV20	7	20926	J 20972 /
BG36		BE	FV3	7	20933	J 20947 S
HG37		B	FV20	7	20940	J 20977
BG38	FV3	C	EQIND,0	11	20947	C 20704 00000
BG39		BE	FV4	7	20958	J 20987 S
BG40		B	FV20	7	20965	J 20972
BG41	FV20	SBR	X2	7	20972	G 00034 B
			SAVE ERROR BRANCH ADDRESS IN X2			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01

CT ADORS INSTRUCTION

PGLIN LABEL

OPCOD OPERANO

BG43	B	SE1	BRANCH TO ERROR ROUTINE	7	20979	J 27220
BG44	H		ROUTINE127 ERROR	1	20986	.
BG45	*		LE DID NOT STOP ON EQUAL,OR THE UNEQUAL INOICATOR			
BG46	*		CAME ON,OR THE EQUAL INDICATOR STAYED OFF.			
BG47	*		X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.			
BG48	*		DISPLAY A00R LABELED-FOUND-TO SEE THE FUNCTION FOUND			
BG49	FV4	LLE	SEARCH,39998 *** LOOKUP LOW OR EQUAL ***	12	20987	T 20687 39998 3
BG50		SBR	X1	7	20999	G 00029 B
BG51		SBR	FV565	7	21006	G 21025 B
BG52		SBR	FV6610	7	21013	G 21069 B
BG53	FV5	MLC	O,FOUND0613	12	21020	D 00000 20730 C
BG54		MLC	CS2	6	21032	O 28678
BG55		BE	FV30	7	21038	J 21084 S
BG56		BL	FV6	7	21045	J 21059 T
BG57	B	FV30	BRANCH-ERROR	7	21052	J 21084
BG58	C	LOINO-1,0	OIO LLE STOP ON LOW	11	21059	C 20697 00000
BG59	BE	FV7	BRANCH-YES-OK	7	21070	J 21099 S
BG60	B	FV30	BRANCH-ERROR	7	21077	J 21084
BG61	FV30	SBR	X2	7	21084	G 00034 B
BG62	B	SE1	SAVE ERROR BRANCH ADDRESS IN X2	7	21091	J 27220
BG63	H		BRANCH TO ERROR ROUTINE	1	21098	.
BG64	*		ROUTINE127 ERROR			
BG65	*		LLE OIO NOT STOP ON LOW AS IT SHOULD,OR THE EQUAL			
BG66	*		INDICATOR CAME ON,OR THE LOW INDICATOR STAYED OFF.			
BG67	*		X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.			
BG68	FV7	OCW	AT2	1	21099	
BG69		OC	SEARCH	5	21104	20687
BG70		OC	399987	6	21110	
BG71		SBR	X1	7	21111	G 00029 B
BG72		SBR	FV865	7	21118	G 21137 B
BG73		SBR	FV9610	7	21125	G 21185 B
BG74	FV8	MLC	O,FOUND0613	12	21132	D 00000 20730 C
BG75		MLC	CS2	6	21144	O 28678
BG76		S	65,FV9610	11	21150	S 29282 21185
BG77	BH	FV9		7	21161	J 21175 U
BG78	B	FV40	BRANCH-ERROR	7	21168	J 21305

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8G80	FV9	C	FSIND,0	11	21175	C 20712 00000
8G81		8E	FV10	7	21186	J 21215 S
8G82		B	FV40	7	21193	J 21305
8G83		S8R	X2	7	21200	G 00034 B
8G84		B	SE1	7	21207	J 27220
8G85		H		1	21214	.
8G86	*					ROUTINE127 ERROR
8G87	*					THE LOOKUP TO ANY INSTRUCTION DID NOT STOP ON THE
8G88	*					FIRST ARGUMENT IN TABLE,OR THE HIGH INDICATOR STAYED
8G89	*					OFF.
8G90	*					X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.
8G91	FV10	DCW	AT2	1	21215	*** LOOKUP TO END ***
8G92		DC	SEARCH	5	21220	20687
8G93		DC	@39998 @	6	21226	
8G94		S8R	X1	7	21227	G 00029 B
8G95		S8R	FV12@10	7	21234	G 21290 B
8G96		S8R	FV11@5	7	21241	G 21253 B
8G97	FV11	MLC	0,FOUND@13	12	21248	D 00000 20730 C
8G98		MLC	CS2	6	21260	D 28678
8G99		BH	FV12	7	21266	J 21280 U
8H00		B	FV40	7	21273	J 21305
8H01	FV12	C	ENIND,0	11	21280	C 20716 00000
8H02		8E	FV13	7	21291	J 21320 S
8H03		B	FV40	7	21298	J 21305
8H04	FV40	S8R	X2	7	21305	G 00034 B
8H05		B	SE1	7	21312	J 27220
8H06		H		1	21319	.
8H07	*					ROUTINE127 ERROR
8H08	*					THE LOOKUP TO END INSTRUCTION DID NOT STOP ON THE
8H09	*					END OF THE TABLE,OR THE HIGH INDICATOR STAYED OFF.
8H10	*					X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.
8H11	FV13	8CE	FV1,YAD1,1	12	21320	B 20868 01001 1
8H12		B	SC1	7	21332	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
-------	-------	--------	---------	----	-------	-------------

*ROUTINE128-FORM TABLE OF HIGH CONSTANTS WITH AN EQUAL FOLLOWED BY

* A LOW FOR USE BY NEXT ROUTINE.

BH14	FW1	BNQ	ITR	7	21339	J 01334 Q
BH15		SCNLA	39998,39998	12	21346	D 39998 39998 B
BH16		SCNLA		1	21358	D
BH17		SCNLA		1	21359	D
BH18		SAR	FW2&10	7	21360	G 21377 A
BH19		MLCWA	EQUAL,0	12	21367	D 20676 0000 X
BH20		MLCWA	HIGH	6	21379	O 20642
BH21		MLCWA	HIGH	6	21385	O 20642
BH22		MLCWA	LOW	6	21391	D 20659
BH23		BCE	FW1,TA01,1	12	21397	B 21339 01001 I
BH24		B	SCI	7	21409	J 27380

*ROUTINE129-CHECK LL, AND LLE INSTRUCTIONS USING TABLE GENERATED BY

* LAST ROUTINE.

BH25	FX1	BNQ	ITR	7	21416	J 01334 Q
BH26		LL	SEARCH,39998 ***	12	21423	T 20687 39998 I
BH27		SBR	X1	7	21435	G 00029 B
BH28		SBR	FX2&5	7	21442	G 21461 B
BH29		SBR	FX3&10	7	21449	G 21498 B
BH30	FX2	MLC	O,FOUND&13	12	21456	O 00000 20730 C
BH31		MLC	CS2	6	21468	D 28678
BH32		BL	FX3	7	21474	J 21488 T
BH33		B	FX10	7	21481	J 21513
BH34	FX3	C	LOIND-1,0	11	21488	C 20697 00000
BH35		BE	FX4	7	21499	J 21528 S
BH36		B	FX10	7	21506	J 21513
BH37	FX10	SBR	X2	7	21513	G 00034 B
BH38		B	SE1	7	21520	J 27220
BH39		H		1	21527	.

LL DID NOT STOP ON LOW,OR THE LOW INDICATOR STAYED

OFF.

X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.

DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BH49	FX4	LLE	SEARCH,39998	12	21528	T 20687 39998 3
BH50		SBR	X1	7	21540	G 00029 B
BH51		SBR	FX5&5	7	21547	G 21566 B
BH52		SBR	FX6&10	7	21554	G 21610 B
BH53	FX5	MLC	0,FOUND&13	12	21561	D 00000 20730 C
BH54		MLC	CS2	6	21573	D 28678
BH55		BU	FX20	7	21579	J 21625 /
BH56		BE	FX6	7	21586	J 21600 S
BH57		B	FX20	7	21593	J 21625
BH58	FX6	C	EQIND,0	11	21600	C 20704 00000
BH59		BE	FX7	7	21611	J 21640 S
BH60		B	FX20	7	21618	J 21625
BH61	FX20	SBR	X2	7	21625	G 00034 H
BH62		B	SEL	7	21632	J 27220
BH63		H		1	21639	.
BH64	*		LLE DID NOT STOP ON EQUAL,OR THE UNEQUAL INDICATOR			
BH65	*		CAME ON,OR THE EQUAL INDICATOR STAYED OFF.			
BH66	*		X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.			
BH67	*		DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND			
BH68	FX7	RCE	FX1,TAD1,1	12	21640	B 21416 01001 1
BH69		B	SCI	7	21652	J 27380

*** LOOKUP LOW OR EQUAL***
 SAVE FOR CHECK
 STORE FUNCTION FOUND
 CLEAR REMAINDER OF STORAGE
 BRANCH-ERROR
 BRANCH-ERROR
 DID LLE STOP ON EQUAL
 BRANCH-YES-OK
 BRANCH-ERROR
 SAVE ERROR BRANCH ADDRESS IN X2
 BRANCH TO ERROR ROUTINE
 ROUTINE129 ERROR
 LOOP ROUTINE129
 STEP ROUTINE COUNTER TO130

CT ADDR INSTRUCTION

OPCODE OPERAND

LABEL

PGLIN

*ROUTINE130--FORM TABLE OF LOW CONSTANTS WITH A HIGH FOLLOWED BY AN

* EQUAL FOR USE BY NEXT ROUTINE.

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDR	INSTRUCTION
BH71						
BH72						
BH73	FY1	BQ	ITR	7	21659	J 01334 Q
BH74		MLCWA	LOW,39998	12	21666	D 20659 39998 X
BH75		SBR	FY2&10	7	21678	G 21707 B
BH76		MLWA	LOW,39997	12	21685	D 20659 39997 U
BH77	FY2	MLCWA	FSIND,0	12	21697	D 20712 00000 X
BH78		MLCWA	LOW	6	21709	D 20659
BH79		MLCWA	LOW	6	21715	D 20659
BH80		MLCWA	HIGH	6	21721	D 20642
BH81		MLCWA	LOW	6	21727	D 20659
BH82		MLCWA	LOW	6	21733	D 20659
BH83		MLCWA	EQUAL	6	21739	D 20676
BH84		MLCWA	LOW	6	21745	D 20659
BH85		MLCWA	LOW	6	21751	D 20659
BH86		SW		1	21757	*
BH87		SBR	FY3&10	7	21758	G 21775 B
BH88	FY3	MLCWA	ENIND,0	12	21765	D 20716 00000 X
BH89		BCE	FY1,TA01,1	12	21777	B 21659 01001 I
BH90		B	SC1	7	21789	J 27380

ADD END INDICATOR TO TABLE END

LOOP ROUTINE130

STEP ROUTINE COUNTER 10131

CT ADDR INSTRUCTION

LABEL

PGLIN

OPCODE OPERAND

*ROUTINE131-CHECK LEH INSTRUCTION USING TABLE GENERATED BY LAST

* ROUTINE.

BH92	FZ1	BNQ	ITR	BRANCH INQUIRY	7	21796	J	01334	Q
BH93		LEH	SEARCH,39998	***LOOKUP EQUAL OR HIGH***	12	21803	T	20687	39998 6
BH94		SBR	X1	SAVE FOR CHECK	7	21815	G	00029	B
BH95		SBR	FZ2E5		7	21822	G	21841	B
BH96		SBR	FZ3E10		7	21829	G	21885	B
BH97		MLC	O,FOUND13	STORE FUNCTION FOUND	12	21836	D	00000	20730 C
BH98		MLC	CS2	CLEAR REMAINDER OF FIELD	6	21848	D	28678	
BH99		BE	FZ10	BRANCH-ERROR	7	21854	J	21900	S
B100		BH	FZ3		7	21861	J	21875	U
B101		B	FZ10	BRANCH-ERROR	7	21868	J	21900	
B102		C	HIIND-1,0	DID LEH STOP ON HIGH	11	21875	C	20692	00000
B103		BE	FZ4	BRANCH-YES-OK	7	21886	J	21915	S
B104		B	FZ10	BRANCH-ERROR	7	21893	J	21900	
B105		SBR	X2	SAVE ERROR BRANCH ADDRESS	7	21900	G	00034	H
B106		B	SE1	BRANCH TO ERROR ROUTINE	7	21907	J	27220	
B107		H		ROUTINE131 ERROR	1	21914	.		
B108		*		LEH DID NOT STOP ON HIGH,OR THE EQUAL INDICATOR					
B109		*		CAME ON,OR THE HIGH INDICATOR STAYED OFF.					
B110		*		X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.					
B111		*		DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND					
B112		FZ4	BCE	FZ1,TAD1,1	12	21915	B	21796	01001 1
B113		B	SC1	STEP ROUTINE COUNTER TO132	7	21927	J	27380	

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8117	ROUTINE132--FORM TABLE OF LOW CONSTANTS WITH AN EQUAL FOLLOWED BY					
8118	• A HIGH FOR USE BY NEXT ROUTINE.					
8119	GA1	BNQ	ITR	7	21934	J 01334 Q
8120		SCNLA	39998,39998	12	21941	D 39998 39998 B
8121		SCNLA		1	21953	D
8122		SCNLA		1	21954	D
8123		SAR	GA2&10	7	21955	G 21972 A
8124	GA2	MLCWA	EQUAL,0	12	21962	D 20676 00000 X
8125		MLCWA	LOW	6	21974	D 20659
8126		MLCWA	LOW	6	21980	D 20659
8127		MLCWA	HIGH	6	21986	D 20642
8128		BCE	GA1,TAD1,1	12	21992	B 21934 01001 1
8129		B	SC1	7	22004	J 27380
8130	ROUTINE133--CHECK LH AND LEH INSTRUCTIONS USING TABLE GENERATED BY					
8131	• LAST ROUTINE.					
8132	GB1	BNQ	ITR	7	22011	J 01334 Q
8133		LH	SEARCH,39998	12	22018	T 20687 39998 4
8134		SBR	X1	7	22030	G 00029 B
8135		SBR	GB3&10	7	22037	G 22093 B
8136		SBR	GB2&5	7	22044	G 22056 B
8137	GB2	MLC	O.FOUND&13	12	22051	D 00000 20730 C
8138		MLC	CS2	6	22063	D 28678
8139		BH	GB3	7	22069	J 22083 U
8140		B	GB10	7	22076	J 22108
8141	GB3	C	HIIND-1,0	11	22083	C 20692 00000
8142		RE	GB4	7	22094	J 22123 S
8143		B	GB10	7	22101	J 22108
8144	GB10	SBR	X2	7	22108	G 00034 B
8145		B	SE1	7	22115	J 27220
8146		H		1	22122	•
8147	• LH DID NOT STOP ON HIGH OR THE HIGH INDICATOR STAYED					
8148	• OFF.					
8149	• X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.					
8150	• DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND					

ROUTINE133 ERROR

LH DID NOT STOP ON HIGH OR THE HIGH INDICATOR STAYED

OFF.

X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.

DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8152	G84	LEH	SEARCH,39998	12	22123	T 20687 39998 6
8153		SBR	X1	7	22135	G 00029 8
8154		SBR	G85E5	7	22142	G 22161 H
8155		SBR	G86E10	7	22149	G 22205 H
8156	G85	MLC	O,FOUND0E13	12	22156	D 00000 20730 C
8157		MLC	CS2	6	22168	D 28678
8158		BU	G820	7	22174	J 22220 /
8159		BE	G86	7	22181	J 22195 S
8160		B	G820	7	22188	J 22220
8161	G86	C	EQINO,0	11	22195	C 20704 00000
8162		BE	G87	7	22206	J 22235 S
8163		B	G820	7	22213	J 22220
8164	G820	SBR	X2	7	22220	G 00034 H
8165		B	SE1	7	22227	J 27220
8166		H		1	22234	.
8167	*		LEH DID NOT STOP ON EQUAL,OR THE UNEQUAL INDICATOR			
8168	*		CAME ON,OR THE EQUAL INDICATOR STAYED OFF.			
8169	*		X1 CONTAINS 8AR,X2 CONTAINS ERROR BRANCH ADDRESS.			
8170	*		DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND			
8171	G87	BCE	G81,TAD1,1	12	22235	B 22011 01001 1
8172		B	SC1	7	22247	J 27380

STORE FUNCTION FOUND
 CLEAR REMAINDER OF STORAGE
 BRANCH-ERROR
 BRANCH-ERROR
 DID LEH STOP ON EQUAL
 BRANCH-YES-OK
 BRANCH-ERROR
 SAVE ERROR BRANCH ADDRESS IN X2
 BRANCH TO ERROR ROUTINE
 ROUTINE133 ERROR

LEH DID NOT STOP ON EQUAL,OR THE UNEQUAL INDICATOR
 CAME ON,OR THE EQUAL INDICATOR STAYED OFF.
 X1 CONTAINS 8AR,X2 CONTAINS ERROR BRANCH ADDRESS.
 DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND
 LOOP ROUTINE133
 STEP ROUTINE COUNTER TO134

CT ADDR INSTRUCTION

PGLIN LABEL DPCOD OPERAND

*ROUTINE134--FORM TABLE OF EQUAL CONSTANTS WITH A HIGH FOLLOWED BY A

* LOW FOR USE BY NEXT ROUTINE.

PGLIN	LABEL	DPCOD	OPERAND	CT	ADDR	INSTRUCTION
8174						
8175						
8176	GC1	BNQ	ITR	7	22254	J 01334 Q
8177		MLCWA	EQUAL,39998	12	22261	D 20676 39998 X
8178		SBR	GC2&10	7	22273	G 22302 B
8179		MLWA	EQUAL,39997	12	22280	D 20676 39997 U
8180	GC2	MLCWA	FSIND,0	12	22292	D 20712 00000 X
8181		MLCWA	EQUAL	6	22304	D 20676
8182		MLCWA	EQUAL	6	22310	D 20676
8183		MLCWA	HIGH	6	22316	D 20642
8184		MLCWA	EQUAL	6	22322	D 20676
8185		MLCWA	EQUAL	6	22328	D 20676
8186		MLCWA	LOW	6	22334	D 20659
8187		MLCWA	EQUAL	6	22340	D 20676
8188		MLCWA	EQUAL	6	22346	D 20676
8189		SBR	GC3&5	7	22352	G 22364 B
8190	GC3	SW	0	6	22359	00000
8191		SBR	GC4&10	7	22365	G 22382 B
8192	GC4	MLCWA	ENIND,0	12	22372	D 20716 00000 X
8193		BCE	GC1,TAD1,1	12	22384	B 22254 01001 1
8194		B	SCI	7	22396	J 27380

TERMINATE TABLE

ADD END INDICATOR TO TABLE END

LOOP ROUTINE134

STEP ROUTINE COUNTER TO135

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
B196			*ROUTINE135-CHECK LLH INSTRUCTION USING TABLE GENERATED BY LAST			
B197			ROUTINE.			
B198	GD1	BNQ	ITR	7	22403	J 01334 Q
B199		LLH	SEARCH,39998 *** LOOKUP LOW OR HIGH ***	12	22410	T 20687 39998 5
B200		SHR	X1	7	22422	G 00029 B
B201		SHR	GD2E5	7	22429	G 22448 B
B202		SBR	GD3E10	7	22436	G 22492 B
B203	GD2	MLC	0,FOUNDE13	12	22443	D 00000 20730 C
B204		MLC	CS2	6	22455	D 28678
B205		BL	GD10	7	22461	J 22507 T
B206		BH	GD3	7	22468	J 22482 U
B207		B	GD10	7	22475	J 22507
B208	GD3	C	HIIND-1,0	11	22482	C 20692 00000
B209		BE	GD4	7	22493	J 22515 S
B210		H	GD10	7	22500	J 22507
B211	GD10	SHK	X2	7	22507	G 00034 B
B212		H		1	22514	.
B213			LLH DID NOT STOP ON HIGH,OR THE LOW INDICATOR CAME			
B214			ON,OR THE HIGH INDICATOR STAYED OFF.			
B215			X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.			
B216			DISPLAY ADDR LABELED-FOUND-TO SEE THE FUNCTION FOUND			
B217	GD4	BCE	GD1,TAD1,1	12	22515	B 22403 01001 1
B218		B	SCI	7	22527	J 27380
B219			*ROUTINE136-FORM TABLE OF EQUAL CONSTANTS WITH A LOW FOLLOWED BY A			
B220			HIGH FOR USE BY THE NEXT ROUTINE.			
B221	GE1	BNQ	ITR	7	22534	J 01334 Q
B222		SCNLA	39998,39998	12	22541	D 39998 39998 B
B223		SCNLA		1	22553	D
B224		SCNLA		1	22554	D
B225		SAR	GE2E10	7	22555	G 22572 A
B226	GE2	MLCWA	LOW,0	12	22562	D 20659 00000 X
B227		MLCWA	EQUAL	6	22574	D 20676
B228		MLCWA	EQUAL	6	22580	D 20676
B229		MLCWA	HIGH	6	22586	D 20642
B230		BCE	GE1,TAD1,1	12	22592	B 22534 01001 1
B231		B	SCI	7	22604	J 27380

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BJ33		*ROUTINE137-CHECK LLH INSTRUCTION USING TABLE GENERATED BY LAST				
BJ34	*	ROUTINE.				
BJ35	GF1	BNQ	ITR	7	22611	J 01334 Q
BJ36		LLH	SEARCH,39998 ***	12	22618	T 20687 39998 S
BJ37		SBR	X1	7	22630	G 00029 B
BJ38		SBR	GF2&5	7	22637	G 22656 B
BJ39		SBR	GF3&10	7	22644	G 22700 B
BJ40	GF2	MLC	0,FOUND&13	12	22651	D 00000 20730 C
BJ41		MLC	CS2	6	22663	D 28678
BJ42		BH	GF10	7	22669	J 22715 U
BJ43		BL	GF3	7	22676	J 22690 T
BJ44		B	GF10	7	22683	J 22715
BJ45	GF3	C	LOINO-1,0	11	22690	C 20697 00000
BJ46		BE	GF4	7	22701	J 22730 S
BJ47		B	GF10	7	22708	J 22715
BJ48	GF10	SBR	X2	7	22715	G 00034 B
BJ49		B	SE1	7	22722	J 27220
BJ50	H			1	22729	.
BJ51	*	LLH DID NOT STOP ON LOW,OR THE HIGH INDICATOR CAME				
BJ52	*	ON&OR THE LOW INDICATOR STAYED OFF.				
BJ53	*	X1 CONTAINS BAR,X2 CONTAINS ERROR BRANCH ADDRESS.				
BJ54	*	DISPLAY ADDR LABELEO-FOUND-TO SEE THE FUNCTION FOUND				
BJ55	GF4	BCE	GF1,TA01,1	12	22730	B 22611 01001 I
BJ56	B	SC1		7	22742	J 27380
			STEP ROUTINE COUNTER T0138			

INSTRUCTION

CT

ADDRS

INSTRUCTION

PGLIN

LABEL

OPCODE

CT

ADDRS

INSTRUCTION

*ROUTINE138--SIMULATE EDIT OPERATION OF NEXT ROUTINE WITHOUT USING

* EDIT INSTRUCTION.

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BJ58	GG1	BNQ	ITR	7	22749	J 01334 Q
BJ59		MLCWA	CS1-9,EDITDA	12	22756	D 28659 24489 X
BJ60		MLCWA	RB,EOTDA	12	22768	D 01889 24489 X
BJ61		MLNWA	AA,EOTCTL	12	22780	D 01878 24457 V
BJ62		MLZ8	CC,EOTCTL	12	22792	D 01900 24457 K
BJ63		BZN	GG24,DD,-	12	22804	V 23092 01911 K
BJ64		BZN	GG26,DD-1,†	12	22816	V 23135 01910 S
BJ65	GG25	BZN	GG27,DD-2,ε	12	22828	V 23178 01909 B
BJ66	GG28	MLCWA	CS3,EOTSM	12	22840	D 28699 24478 X
BJ67		MLCWA	EOTCTL,EOTSM	12	22852	D 24457 24478 X
BJ68		MLCWA	CS1-12,RCHAR	12	22864	D 28656 28549 X
BJ69		MLCWA	εEDITDA,X1	12	22876	D 29287 00029 X
BJ70		MLCWA	εEDITSM,X2	12	22888	D 29292 00034 X
BJ71		MLCS	203,NOTZS	12	22900	D 29166 28539 3

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BJ75	***		START FIRST SCAN-LEFT			
BJ76		MLNS	010,UNITS	12	22912	D 29167 28547 1
BJ77		BZN	GG2,EDITDA,-	12	22924	V 22948 24489 K
BJ78		MLNS	010,PLUS	12	22936	D 29167 28546 1
BJ79	GG2	S	01,X2	11	22948	S 29202 00034
BJ80		MLCWS	10X2,8CHAR	12	22959	D 000,1 28549 7
BJ81		MLCWS	00X1,ACHAR	12	22971	D 000*0 28548 7
BJ82		CW	FIRSTO	6	22983	D 28540
BJ83		BCE	GG3,10X2,0	12	22989	B 23362 000,1 0
BJ84		BCE	TW01,10X2,0	12	23001	B 23500 000,1 0
BJ85	GG5	BCE	GG4,UNITS,1	12	23013	B 23399 28547 1
BJ86		BCE	GG6,BODY,1	12	23025	B 23240 28544 1
BJ87	ERP8	BCE	TW01,10X2,0	12	23037	B 23500 000,1 0
BJ88		BCE	GG7,10X2,-	12	23049	B 23221 000,1 -
BJ89		BCE	GG7,10X2,C	12	23061	B 23221 000,1 C
BJ90		BCE	GG7,10X2,R	12	23073	B 23221 000,1 R
BJ91		B	ONE1	7	23085	J 23432
BJ92	GG24	MLCWA	00000,0,X1	12	23092	D 29297 00029 X
BJ93		MLNS	FF,X1	12	23104	D 01921 00029 1
BJ94		MLCS	000,EDITCTL0X1	12	23116	D 29298 244V7 3
BJ95		B	GG25	7	23128	J 22816
BJ96	GG26	MLCWA	00000,0,X1	12	23135	D 29297 00029 X
BJ97		MLNS	EE,X1	12	23147	D 01916 00029 1
BJ98		MLCS	000,EDITCTL0X1	12	23159	D 29299 244V7 3
BJ99		B	GG25012	7	23171	J 22828

SET UNITS LATCH

SET PLUS LATCH

SET X2 FOR NEXT 8 CHAR

STORE THIS 8 CHARACTER

STORE THIS A CHARACTER

CLEAR FIRST ZERO INDICATOR

BRANCH-THIS 8 CHAR IS ZERO

BRANCH-THIS 8 CHAR IS 0

BRANCH-UNITS LATCHSET

BRANCH-BODY LATCH SET

GO IF THIS 8 CHAR IS A COMMA

BRANCH-THIS 8 CHAR IS A -

BRANCH-THIS 8 CHAR IS A C

BRANCH-THIS 8 CHAR IS AN R

BRANCH-THIS 8 CHAR NOT -,C,R OR,

NEGATIVE ZERO TO INDEX REG ONE

SET INDEX REG ONE FROM FF

INSERT DOLLAR IN CONTROL CONSTANT

NEGATIVE ZERO TO INDEX REG ONE

SET INDEX REG ONE FROM EE

INSERT ASTERISK IN CTL CONSTANT

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BK01	GG27	MLCWA	00000.0,X1	12	23178	D 29297 00029 X
BK02		MLNS	EE-1,X1	12	23190	D 01915 00029 1
BK03	ERPC	MLCS	0.0,EDIC1LX1	12	23202	D 29300 244V7 3
BK04		B	GG28L12	7	23214	J 22840
BK05	GG7	BCE	TWO1,PLUS,1	12	23221	B 23500 28546 1
BK06		B	ONE1	7	23233	J 23432
BK07	GG6	BCE	THREE1,1LX2,0	12	23240	B 23633 000.1 0
BK08		BCE	THREE1,1LX2,	12	23252	B 23633 000.1
BK09		BCE	GG8,1LX2,*	12	23264	B 23295 000.1 *
BK10		BCE	GG8,1LX2,\$	12	23276	B 23295 000.1 \$
BK11		B	ONE1	7	23288	J 23432
BK12	GG8	BCE	THREE1,SUPPR,0	12	23295	B 23633 28545 0
BK13		B8E	THREE1,ASTDOL,5	12	23307	W 23633 28543 5
BK14		MLCS	010,ASTDOL	12	23319	D 29167 28543 3
BK15		BCE	THREE1,1LX2,*	12	23331	B 23633 000.1 *
BK16		MLCS	040,ASTDOL	12	23343	D 29301 28543 3
BK17		B	THREE1	7	23355	J 23633
BK18	GG3	BCE	GG5,SUPPR,1	12	23362	B 23013 28545 1
BK19		MLCS	010,SUPPR	12	23374	D 29167 28545 3
BK20		SW	FIRST0	6	23386	, 28540
BK21		H	GG5	7	23392	J 23013
BK22	GG4	MLCS	BCHAR,GG9L11	12	23399	D 28549 23422 3
BK23	GG9	BCE	FOUR1,0-CR 00,	12	23411	B 23519 29306
BK24		BCE		1	23423	B
BK25		BCE	GG7	6	23424	B 23221
BK26		BCE		1	23430	B
BK27		BCE		1	23431	B
BK28	ONE1	CW	1LX2	6	23432	D 000.1
BK29		MLWS	FIRST0,1LX2	12	23438	D 28540 000.1 4
BK30		BW	GG11,8CHAR	12	23450	V 23469 28549 1
BK31		B	GG2	7	23462	J 22948
BK32	GG11	BCE	SCAN2,SUPPR,1	12	23469	B 23652 28545 1
BK33		BCE	SCAN2,8CHAR,0	12	23481	B 23652 28549 0
BK34		B	EDITEND	7	23493	J 24330

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BK36	TWO1	MLCS	2 2,1EX2	12	23500	D 29208 000.1 3
BK37		B	ONE1	7	23512	J 23432
BK38	FOUR1	MLNS	0EX1,1EX2	12	23519	D 000#0 000.1 1
BK39		MLZS	2 2,1EX2	12	23531	D 29208 000.1 2
BK40	GG12	MLWS	FIRST0,1EX2	12	23543	D 28540 000.1 4
BK41		S	21,X1	11	23555	S 29202 00029
BK42		MLCS	202,UNITS	12	23566	D 29166 28547 3
BK43		BW	GG11,BCHAR	12	23578	V 23469 28549 1
BK44		MLCS	202,BODY	12	23590	D 29166 28544 3
BK45		BW	GG2,ACHAR	12	23602	V 22948 28548 1
BK46		MLCS	212,BODY	12	23614	D 29167 28544 3
BK47		B	GG2	7	23626	J 22948
BK48	THREE1	MLCS	0EX1,1EX2	12	23633	D 000#0 000.1 3
BK49		B	GG12	7	23645	J 23543
BK50	***		START SECOND SCAN-RIGHT			
BK51	SCAN2	A	21,X2	11	23652	A 29202 00034
BK52		MLCWS	0EX2,8CHAR	12	23663	D 000.0 28549 7
BK53		MLCS	0EX2,GG13211	12	23675	D 000.0 23710 3
BK54		MLCS	212,SIGDIG	12	23687	D 29167 28541 3
BK55	GG13	BCE	GG14,CR6,0	12	23699	B 23800 01779 0
BK56		BCE		1	23711	B
BK57		BCE		1	23712	B
BK58		BCE		1	23713	B
BK59		BCE		1	23714	B
BK60		BCE		1	23715	B
BK61		BCE		1	23716	B
BK62		BCE		1	23717	B
BK63		BCE		1	23718	B
BK64		MLCS	202,SIGDIG	12	23719	D 29166 28541 3
BK65		MLCS	0EX2,GG17211	12	23731	D 000.0 23754 3
BK66	GG17	BCE	ONE2,2,0 -2	12	23743	B 23893 29262
BK67		BCE	GG15	6	23755	B 23819
BK68		BCE	GG16	6	23761	B 23850
BK69		BCE		1	23767	B
BK70		BCE		1	23768	B

STEP IX2 FOR NEXT B CHAR RIGHT
 STORE THIS B CHARACTER
 B CHAR TO D MOD OF RCE INSTRUCT
 SET SIGNIFICANT DIGIT INDICATOR
 BRANCH-THIS B CHAR IS SIG DIG 1-9
 DITTO
 DITTO
 DITTO
 DITTO
 DITTO
 DITTO
 DITTO
 DITTO
 CLEAR SIGNIFICANT DIGIT INDICATOR
 B CHAR TO BCE D MODIFIER
 BRANCH-B CHAR IS A MINUS
 BRANCH-B CHAR IS A PERIOD
 BRANCH-B CHAR IS A BLANK
 BRANCH-B CHAR IS A ZERO
 BRANCH-B CHAR IS A COMMA

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
BK72		BCE	CKNZS,DECCTL,1	12	23769	B 23956 28542 1
BK73		MLCS	010,SUPPR	12	23781	D 29167 28545 3
BK74		B	ONE2	7	23793	J 23893
BK75	GG14	MLCS	002,SUPPR	12	23800	D 29166 28545 3
BK76		B	ONE2	7	23812	J 23893
BK77	GG15	BCE	ONE2,SUPPR,0	12	23819	B 23893 28545 0
BK78		MLCS	012,DECCTL	12	23831	D 29167 28542 3
BK79		B	ONE2	7	23843	J 23893
BK80	GG16	BCE	ONE2,SUPPR,0	12	23850	B 23893 28545 0
BK81		BCE	ONE2,DECCTL,1	12	23862	B 23893 28542 1
BK82		BCE	THREE2,ASTDOL,1	12	23874	B 23937 28543 1
BK83		B	TWO2	7	23886	J 23918
BK84	ONE2	CW	0EX2	6	23893	D 000,0
BK85	GG18	BW	GG19,8CHAR	12	23899	V 23987 28549 1
BK86		B	SCAN2	7	23911	J 23652
BK87	TWO2	MLCWS	0 0,0EX2	12	23918	D 29308 000,0 7
BK88		B	GG18	7	23930	J 23899
BK89	THREE2	MLCWS	0 *0,0EX2	12	23937	D 29310 000,0 7
BK90		B	GG18	7	23949	J 23899
BK91	CKNZS	BCE	ONE2,SUPPR,1	12	23956	B 23893 28545 1
BK92		MLCS	010,NOTZS	12	23968	D 29167 28539 3
BK93		B	ONE2	7	23980	J 23893
BK94	GG19	BCE	SCAN3,ASTDOL,4	12	23987	B 24073 28543 4
BK95		BCE	EDTEND,DECCTL,0	12	23999	B 24330 28542 0
BK96		BCE	ICHKAA,SUPPR,1	12	24011	B 24061 28545 1
BK97		BCE	EDTEND,NOTZS,0	12	24023	B 24330 28539 0
BK98		BCE	EDTEND,SIGDIG,1	12	24035	B 24330 28541 1
BK99		B	SCI	7	24047	J 27380
BL00		B	SKPEDT	7	24054	J 24528
BL01	ICHKAA	BCE	EDTEND,SIGDIG,1	12	24061	B 24330 28541 1

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BL03	***		START THIRD SCAN-LEFT			
BL04	SCAN3	S	11,X2	11	24073	S 29202 00034
BL05		MLCWS	16X2,8CHAR	12	24084	D 000.1 28549 7
BL06		MLCS	16X2,*12	12	24096	D 000.1 24119 3
BL07		BCE	GG20,2.0 2,	12	24108	B 24275 29265
BL08		BCE	GG21	6	24120	B 24208
BL09		BCE	GG21	6	24126	B 24208
BL10		B	SCAN3	7	24132	J 24073
BL11	GG29	BCE	NOTDEC,8CHAR,0	12	24139	B 24189 28549 0
BL12		BCE	SCAN3,0ECCTL,0	12	24151	B 24073 28542 0
BL13		BCE	EDTEN0,NOTZS,0	12	24163	B 24330 28539 0
BL14	SCAN3X	B	SCI	7	24175	J 27380
BL15		B	SKPEDT	7	24182	J 24528
BL16	NOTDEC	BCE	SCAN3,NOTZS,0	12	24189	B 24073 28539 0
BL17		B	SCAN3X	7	24201	J 24175
BL18	GG21	BCE	GG29,SUPPR,0	12	24208	B 24139 28545 0
BL19		MLCS	2*2,16X2	12	24220	D 29299 000.1 3
BL20		BCE	GG22,ASTDOL,1	12	24232	B 24256 28543 1
BL21		MLCS	2 2,16X2	12	24244	D 29208 000.1 3
BL22	GG22	BCE	EDTEND,8CHAR,.	12	24256	B 24330 28549 .
BL23		B	SCAN3	7	24268	J 24073
BL24	GG20	B8E	GG23,ASTDOL,5	12	24275	W 24294 28543 5
BL25		B	SCAN3	7	24287	J 24073
BL26	GG23	MLCS	2*2,16X2	12	24294	D 29299 000.1 3
BL27		BCE	EDTEND,ASTDOL,1	12	24306	B 24330 28543 1
BL28		MLCS	2*2,16X2	12	24318	D 29298 000.1 3
BL29	EOTEND	MLWA	CS1,EOTSM	12	24330	D 28668 24478 U
BL30		HCE	GG1,TA01,1	12	24342	B 22749 01001 1
BL31		B	SCI	7	24354	J 27380
			STEP ROUTINE COUNTER TO139			

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
BL33			*ROUTINE139-CHECK EDIT INSTRUCTION AGAINST RESULT OF EDIT			
BL34	*		PERFORMED BY LAST ROUTINE.			
BL35	GH1	BNQ	ITR	7	24361	J 01334 Q
BL36		MLCWA	CS3,0EX5	12	24368	D 28699 00#0 X
BL37		MLCWA	EDICTL,0EX5	12	24380	D 24457 00#0 X
BL38		MLCWA	EDTDA,0EX6	12	24392	D 24489 00#0 X
BL39		MCE	0EX6,0EX5	11	24404	E 00#0 00#0
BL40	GH4	C	0EX5,EDTSM	11	24415	C 00#0 24478
BL41		BE	GH2	7	24426	J 24490 S
BL42		B	SE1	7	24433	J 27220
BL43		H		1	24440	.
BL44	*		THE RESULT OF THE EDIT INSTRUCTION, AT ADDRESS EE			
BL45	*		LEFT, DID NOT COMPARE WITH THE RESULT OF THE			
BL46	*		SIMULATED EDIT PERFORMED BY THE LAST ROUTINE.			
BL47		B	GH2	7	24441	J 24490
BL48	EDICTL	DCW	a	10	24457	
BL49	EDTSM	DCW	a	21	24478	
BL50	EDTDA	DCW	a	11	24489	
BL51	GH2	C	EDTDA,0EX6	11	24490	C 24489 00#0
BL52		BE	GH3	7	24501	J 24516 S
BL53		B	SE1	7	24508	J 27220
BL54		H		1	24515	.
BL55	*		THE DATA IN THE A FIELD, ADDRESS FF LEFT, OF THE			
BL56	*		EDIT INSTRUCTION WAS CHANGED BY THE OPERATION OF THE			
BL57	*		EDIT INSTRUCTION.			
BL58	GH3	BCE	GH1,IAD1,1	12	24516	B 24361 01001 I
BL59	SKPEDT	B	SC1	7	24528	J 27380

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADRS	INSTRUCTION
BL61	*ROUTINE140-IF PRESENT IN THIS SYSTEM, CHECK FOR PROPER INTERRUPT					
BL62	*	OF CPU INSTRUCTIONS.				
BL63		BCE	LE4,TAD8,1	12	24535	B 25285 01008 1
BL64		BCE	LAL,SYS1&8,1	12	24547	B 24566 01264 1
BL65		B	LE4	7	24559	J 25285
BL66	LAL	C BCE	*&8,SYS1&7,1	12	24566	B 24585 01263 1
BL67		C B	LE4	7	24578	J 25285
BL68		C BCE	LA2,CN4,0	12	24585	B 24609 01402 0
BL69		MLCS	212,CT2	12	24597	O 29167 28721 3
BL70	LA2	BW	FASTA,00997	12	24609	V 24738 00997 1
BL71		C	C01,2492	11	24621	C 28538 29312
BL72		BE	LA5	7	24632	J 24681 S
BL73		C	C01,2992	11	24639	C 28538 29281
BL74		BE	LA5	7	24650	J 24681 S
BL75		B	LE4	7	24657	J 25285
BL76		C OCW	2N	11	24674	
BL77		C SW	22472	6	24675	* 29316
BL78	LA5	NOPWM		1	24681	N
BL79		B	LA3	7	24682	J 24707
BL80		SW	*-12	6	24689	* 24682
BL81		MRCWG	R00101,101	12	24695	O 01010 00101 L
BL82	LA3	BCE	LA4,CT2,0	12	24707	B 24769 28721 0
BL83		MLCS	202,CT2	12	24719	O 29166 28721 3
BL84		B	LE4	7	24731	J 25285
BL85	FASTA	BCE	LA5,C01,9	12	24738	B 24681 28538 9
BL86		BCE	LA5,C01,4	12	24750	B 24681 28538 4
BL87		B	LE4	7	24762	J 25285
BL88		BCE	*&13,TAD7,1	12	24769	B 24793 01007 1
BL89	LA4	MLNA	CT4,LB3&5	12	24781	O 28726 24976 /
BL90		BDV	*&1	7	24793	J 24800 W
BL91		C	CT4,&RUPTOP	11	24800	C 28726 29321
BL92		BH	LB1	7	24811	J 24830 U
BL93		MLNA	&RUPB0T,LB3&5	12	24818	O 29191 24976 /

BRANCH-BYPASS PRIORITY ALERT CHK
BRANCH-PRIORITY MODE PRESENT
THIS SYSTEM MINUS PRIORITY MODE
BRANCH IF OVERLAP PRESENT
THIS SYSTEM MINUS OVERLAP MODE
BRANCH -PASS SUCCESSFUL SO FAR
SET 50 PASS ERROR INDICATOR
GO IF RELIABILITY MODE
IS THIS PASS MULTIPLE OF 50
BRANCH-YES
YES
NO
UNNECESSARY-REMOVE LATER
UNNECESSARY-REMOVE LATER
MOVE INTERRUPT ROUTINE
BRANCH-CPU OK-CHECK INTERRUPT
CLEAR 50 PASS ERROR INDICATOR
CPU FAILING-BYPASS INTERRUPT CHK
GO CHECK INTERRUPT EVERY 5 PASSES
NOT THIS TIME
BRANCH-MAINTAIN PRESENT CONSTANTS
STORE FIRST INTRUP OP ADDRESS
TURN OFF OVIDE OVERFLOW
ARE ALL PRIORITY OPS CHECKED
BRANCH-NO
RESET OP SELECTION

1410/7010 CPU RELIABILITY TEST-40K & UP

PAGE 126

CU01 INSTRUCTION

PGLIN	LABEL	OPCOD	OPERAND	BRANCH INQUIRY	CT	AOORS	CU01 INSTRUCTION
BL95	L81	BNQ	ITR	BRANCH INQUIRY	7	24830	J 01334 Q
BL96		MLCWA	CP2E8,4EX6	CLEAR ADDRESS FF	12	24837	0 01564 00*4 X
BL97		MLCWS	AM0,1EX6		12	24849	D 29255 00*1 7
BL98		SW	0EX6,3EX6		11	24861	00*0 00*3
BL99		SW			1	24872	.
BM00		SW			1	24873	.
BM01		MLCWA	CP2E8,4EX5	CLEAR ADDRESS EE	12	24874	0 01564 00*4 X
BM02		SW	0EX5,3EX5		11	24886	00*0 00*3
BM03		SW			1	24897	.
BM04		SW			1	24898	.
BM05		SW			1	24899	.
BM06		CW	58EX6		6	24900	00*V8
BM07		SAR	LCC1E5	SET B0L1 ADDRESS	7	24906	G 25091 A
BM08		CW	66EX6		6	24913	00*06
BM09		SAR	LCC2E5	SET 8BE ADDRESS	7	24919	G 28995 A
BM10		SAR	LCC3E5	SET BZN ADDRESS	7	24926	G 29016 A
BM11		CW	67EX6		6	24933	00*J7
BM12		SAR	LCC5E5	SET CW ADDRESS	7	24939	G 29100 A
BM13		MLCWA	0 @,X2	CLEAR X2	12	24946	D 29165 00034 X
BM14		CW	72EX6	CALCULATE INTERRUPT ADDRESS	6	24958	00*P2
BM15		SAR	X2	STORE FOR CHECK	7	24964	G 00034 A

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 127

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRES	INSTRUCTION	
BM17	LB3	MRCWG	0,LC10	12	24971	D 00000 25094 L	
BM18		SAR	CT4	7	24983	G 28726 A	
BM19		SBR	*E11	7	24990	G 25007 B	
BM20		MLWB	*0	12	24997	0 25008 00000 M	
BM21		SCNLA	INTRUP,INTRUP&1	12	25009	D 25107 25108 B	
BM22		SBR	*E6	7	25021	G 25033 B	
BM23		MLCS	0,0&X6	12	25028	0 00000 00&0 3	
BM24		BEP4	*E1	7	25040	Y 25047 E	
BM25	LC5	MRCWG	LC6,30&X6	12	25047	0 25066 00&L0 L	
BM26		B	30&X6	7	25059	J 00&L0	
BM27		***THIS WILL BE LOCATED AT FF&30 THRU FF&87*****					
BM28	LC6	WCPO	0&X6	10	25066	M 20 00&0 M	
BM29		OCW	2N00002	5	25080		
BM30		OCW	2N00002	5	25085		
BM31	LCC1	BOL1	*E1	7	25086	J 25093 1	
BM32	LC7	OCW	2J2	1	25093		
BM33	LC10	OCW	2	7	25094		
BM34	LC11	OC	2	6	25101		
BM35	INTRUP	OC	2,2	1	25107		
BM36	LC12	OC	2	8	25108		
BM37		B	*E2	7	25116	J 25124	
BM38		OCW	2M2	1	25123		
BM39		*****					
BM40	LC13	BCB1	LC5	7	25124	R 25047 2	
BM41		BXPA	*E1	7	25131	Y 25138 X	
BM42		BAL	LD1	7	25138	R 25172 M	
BM43		BCE	RUPTOK,LC12&1,2	12	25145	B 25254 25109 #	
BM44		B	SEL	7	25157	J 27220	
BM45		H		1	25164	.	
BM46		ROUTINE140 ERROR					
BM47		INTERUPT FAILED TO OCCUR FOLLOWING AN OVERLAPPED					
BM48		WCP OPERATION IN PRIORITY ALERT MODE. INTERUPT SHOULD					
BM49		HAVE OCCURRED AT ADDRESS FF PLUS 71. THIS ADDRESS IS					
BM50		STORED IN INDEK REGISTER 2.					
		B	LOB	7	25165	J 25252	
		ROUTINE ENDED WITH ERROR					

PGLIN	LABEL	OPC00	OPERAND	CT	ADDRS	INSTRUCTION
BM52	LD1	SBR	LD265	7	25172	G 25199 B
BM53		SBR	X2	7	25179	G 00034 B
BM54		B	SE1	7	25186	J 27220
BM55		H		1	25193	ROUTINE140 ERROR
BM56	*		WCPO INSTRUCTION CAUSED BAI TO BRANCH.			
BM57	LD2	B	0	7	25194	J 00000 G
BM58	LC14	BA1	LD1	7	25201	R 25172 M
BM59		BXPA	*E1	7	25208	Y 25215 X
BM60		B	SE1	7	25215	J 27220
BM61		H		1	25222	ROUTINE140 ERROR
BM62	*		THE OP CODE BEING TESTED FOR INTERRUPTING ON THIS PASS			
BM63	*		IS EITHER A BAI OR BXPA INSTRUCTION. NO INTERRUPT SHOULD			
BM64	*		HAVE OCCURRED. HOWEVER, AN INTERRUPT DID OCCUR AT THE			
BM65	*		ADDRESS NOW STORED IN INDEX REGISTER 1.			
BM66		B	LD8	7	25223	J 25252
BM67	RUP8AD	BXPA	*E1	7	25230	Y 25237 X
BM68		BA1	LD1	7	25237	R 25172 M
BM69		B	SE1	7	25244	J 27220
BM70		H		1	25251	ROUTINE140 ERROR
BM71	*		THE OVERLAPPED WCP INSTRUCTION AT ADDRESS FF PLUS 30			
BM72	*		SHOULD HAVE CAUSED AN INTERRUPT AT ADDRESS FF PLUS 71.			
BM73	*		THE INTERRUPT OCCURRED INSTEAD AT THE ADDRESS NOW STORED			
BM74	*		IN INDEX REGISTER 1. ADDRESS FF E71 IS IN INDEX REG. 2.			
BM75	LD8	B	LE3	7	25252	J 25273
BM76	RUP10K	BXPA	*E1	7	25259	Y 25266 X
BM77		BA1	LD1	7	25266	R 25172 M
BM78	LE3	BCE	LB1,TAD1,1	12	25273	B 24830 01001 I
BM79	LE4	B	SC1	7	25285	J 27380
			STEP ROUTINE COUNTER TO141			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BM81			*ROUTINE141-CHECK RESTORE AND STORE INTERNAL STATUS INDICATORS			
BM82			* INSTRUCTIONS.			
BM83	LG1	BCE	LG2,SYSL,X	12	25292	B 25311 01256 X
BM84		B	LG5	7	25304	J 25450
BM85	LG2	BNQ	ITR	7	25311	J 01334 Q
BM86		MLCWA	a a,X1	12	25318	D 29165 00029 X
BM87		MLCS	DD,X1-2	12	25330	D 01911 00027 3
BM88		RSCPU	X1-2	7	25342	S 00027 R
BM89		MLNS	BB,X1	12	25349	D 01889 00029 1
BM90		MLZWS	CC,X1	12	25361	D 01900 00029 6
BM91		MLCS	X1,LG3E11	12	25373	D 00029 25417 3
BM92		DCW	a\$a	1	25385	
BM93		OC	X1	5	25390	00029
BM94		DC	a\$a	1	25391	
BM95		B	*E1	7	25392	J 25399
BM96		DCW	a\$a	1	25399	
BM97		DC	X1-1	5	25404	00028
BM98		DC	a\$a	1	25405	
BM99	LG3	BCE	LG4,X1-1.	12	25406	B 25426 00028
BN00		B	SE1	7	25418	J 27220
BN01		H		1	25425	.
BN02			THE CHARACTER IN ADDRESS 29 OF X1 WAS USED TO			
BN03			RESTORE THE INTERNAL STATUS INDICATORS. THE CONTENTS			
BN04			OF THE INDICATORS WERE THEN STORED IN ADDRESS 28 OF			
BN05			X1. THE TWO CHARACTERS ARE NOT EQUAL.			
BN06	LG4	BCE	LG2,TAD1,1	12	25426	B 25311 01001 1
BN07		MLCWA	a a,X1	12	25438	D 29165 00029 X
BN08	LG5	B	SCI	7	25450	J 27380
			STEP ROUTINE COUNTER TO142			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BN10			*ROUTINE142-CHECK RESTORE AND STORE CHANNEL 1 STATUS INDICATORS IF			
BN11			*7010 MACHINE.			
BN12	WXE1	BCE	*E8,SYS1,X	12	25457	B 25476 01256 X
BN13	B	WXE8	GO-NOT 7010 SYSTEM	7	25469	J 25712
BN14	BCE	WXE2,SYS12,1	GO OPERATE ROUTINE IF CHNL PRESENT	12	25476	B 25495 01268 1
BN15	B	WXE8	GO-CHANNEL MISSING	7	25488	J 25712
BN16	WXE2	BNQ	ITR1	7	25495	J 01341 Q
BN17		MLCWA	2, X1	12	25502	D 29165 00029 X
BN18		MLCS	00,X1-2	12	25514	D 01911 00027 3
BN19		BAI	*E1	7	25526	R 25533 M
BN20		REC	X1-2	7	25533	\$ 00027 1
BN21		MLZWS	CC-1,X1	12	25540	D 01899 00029 6
BN22		MLNS	88,X1	12	25552	D 01889 00029 1
BN23		REC	X1	7	25564	\$ 00029 1
BN24		B	*E1	7	25571	J 25578
BN25		SEC	X1-1	7	25578	\$ 00028 E
BN26		MLCS	X1,WXE3E11	12	25585	D 00029 25608 3
BN27	WXE3	BCE	WXE4,X1-1,	12	25597	B 25617 00028
BN28		B	SE1	7	25609	J 27220
BN29		H		1	25616	.
BN30	*		THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 1 INDICATORS.			
BN31	*		THE CHANNEL 1 INDICATORS WERE THEN STORED IN X1-1. THE			
BN32	*		CHARACTER IN X1-1 DOES NOT EQUAL THE CHARACTER IN X1.			
BN33	WXE4	MLWS	X1,WXE6	12	25617	D 00029 25630 4
BN34		NOP		1	25629	N
BN35	WXE6	BW	WXE5,X1-1	12	25630	V 25681 00028 1
BN36		BW	WXE7,X1	12	25642	V 25673 00029 1
BN37		BW	WXE7,X1-1	12	25654	V 25673 00028 1
BN38		B	WXE5	7	25666	J 25681
BN39	WXE7	B	SE1	7	25673	J 27220
BN40		H		1	25680	.
BN41	*		THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 1 INDICATORS.			
BN42	*		THE CHANNEL 1 INDICATORS WERE THEN STORED IN X1-1. X1 AND			
BN43	*		X1-1 DO NOT BOTH HAVE A WORD MARK, OR DO NOT BOTH NOT			
BN44	*		HAVE A WORD MARK.			

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 131

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRES	INSTRUCTION
BN46	WXE5	BA1	*E1	7	25681	R 25688 G
BN47	BCE	WXE2,TAD1,1		12	25688	B 25495 01001 1
BN48	MLCWA	a a,X1		12	25700	D 29165 00029 X
BN49	WXEB	B SCI		7	25712	J 27380
BN50	*ROUTINE143-CHECK RESTORE AND STORE CHANNEL 2 STATUS INDICATORS IF					
BN51	*7010 MACHINE.					
BN52	WXF1	BCE	*E8,SYS1,X	12	25719	B 25738 01256 X
BN53	B	WXF8		7	25731	J 25974
BN54	BCE	WXF2,SYS1E13,1		12	25738	B 25757 01269 1
BN55	B	WXF8		7	25750	J 25974
BN56	WXF2	BNQ ITR1		7	25757	J 01341 Q
BN57	MLCWA	a a,X1		12	25764	D 29165 00029 X
BN58	MLCS	OD,X1-2		12	25776	D 01911 00027 3
BN59	BA2	*E1		7	25788	X 25795 M
BN60	RFC	X1-2		7	25795	\$ 00027 2
BN61	MLZWS	CC-2,X1		12	25802	D 01898 00029 6
BN62	MLNS	8B-1,X1		12	25814	D 01888 00029 1
BN63	RFC	X1		7	25826	\$ 00029 2
BN64	B	*E1		7	25833	J 25840
BN65	SFC	X1-1		7	25840	\$ 00028 F
BN66	MLCS	X1,WXF3E11		12	25847	D 00029 25870 3
BN67	BCE	WXF4,X1-1,		12	25859	B 25879 00028
BN68	B	SE1		7	25871	J 27220
BN69	H			1	25878	.
BN70	*	THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 2 INDICATORS.				
BN71	*	THE CHANNEL 2 INDICATORS WERE THEN STORED IN X1-1. THE				
BN72	*	CHARACTER IN X1-1 DOES NOT EQUAL THE CHARACTER IN X1.				

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 132

CT ADDR INSTRUCTION

PGLIN LABEL

OPCODE OPERAND

BN74	W XF4	MLWS	X1, W XF6	SET FOR CHECKING WM/IO INTRLK	12	25879	D	00029	25892	4
BN75		NOP			1	25891	N			
BN76	W XF6	BW	W XF5, X1-1	GO IF X1 & X1-1 HAVE WMS,OK	12	25892	V	25943	00028	1
BN77		BW	W XF7, X1	GO IF EITHER HAS WM-ERROR	12	25904	V	25935	00029	1
BN78		BW	W XF7, X1-1		12	25916	V	25935	00028	1
BN79		B	W XF5	GO-NIETHER X1 OR X1-1 HAS WM-OK	7	25928	J	25943		
BN80	W XF7	B	SEL	BRANCH TO ERROR ROUTINE	7	25935	J	27220		
BN81		H		ROUTINE143 ERROR	1	25942	.			
BN82	*			THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 2 INDICATORS.						
BN83	*			THE CHANNEL 2 INDICATORS WERE THEN STORED IN X1-1. X1 AND						
BN84	*			X1-1 DO NOT BOTH HAVE A WORD MARK, OR DO NOT BOTH NOT						
BN85	*			HAVE A WORD MARK.						
BN86	W XF5	BA2	* &1	RESET IO INTERLOCK	7	25943	X	25950	G	
BN87		BCE	W XF2, TAD1, 1	LOOP ROUTINE143	12	25950	B	25757	01001	1
BN88		MLCWA	2 2, X1	CLEAR INDEX REG ONE	12	25962	D	29165	00029	X
BN89	W XF8	B	SC1	STEP ROUTINE COUNTER TO144	7	25974	J	27380		

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADDR INSTRUCTION

PGLIN LABEL OPCOD OPERAND

*ROUTINE144-CHECK RESTORE AND STORE CHANNEL 3 STATUS INDICATORS IF

*7010 MACHINE.

BN91	WXG1	BCE	*68,SYS1,X	GO IF 7010 SYSTEM	12	25981	B	26000	01256	X
BN92		B	WXGB	GO-NOT 7010 SYSTEM	7	25993	J	26236		
BN93		BCE	WXG2,SYS1&14,1	GO OPERATE ROUTINE IF CHNL PRESENT	12	26000	B	26019	01270	1
BN94		B	WXG8	GO-CHANNEL MISSING	7	26012	J	26236		
BN95		BNQ	ITR1	BRANCH INQUIRY	7	26019	J	01341	Q	
BN96	WXG2	MLCWA	2, X1	CLEAR INDEX REG 1	12	26026	D	29165	00029	X
BN97		MLCS	DD,X1-2	RANDOM CHARACTER TO X1-2	12	26038	D	01911	00027	3
BN98		BA3	*E1	RESET INTERLOCK	7	26050	3	26057	M	
BN99		RGC	X1-2	RESTORE CHANNEL STATUS RANDOMLY	7	26057	\$	00027	3	
B000		MLZWS	CC-3,X1	RANDOM CHAR. & WM TO X1 UNITS	12	26064	D	01897	00029	6
B001		MLNS	BB-2,X1	RANDOM CHAR. & WM TO X1 UNITS	12	26076	D	01887	00029	1
B002		RGC	X1	RESTORE CHANNEL STATUS RANDOMLY	7	26088	\$	00029	3	
B003		B	*E1	FILLER	7	26095	J	26102		
B004		SGC	X1-1	STORE CHANNEL STATUS IN X1-1	7	26102	\$	00028	G	
B005		MLCS	X1,WXG3&11	SET BCE D MOD	12	26109	D	00029	26132	3
B006	WXG3	BCE	WXG4,X1-1.	GO IF ZONE-NUMERICS OK	12	26121	B	26141	00028	
B007		B	SE1	BRANCH TO ERROR ROUTINE	7	26133	J	27220		
B008		H		ROUTINE144 ERROR	1	26140	.			

* THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 3 INDICATORS.

* THE CHANNEL 3 INDICATORS WERE THEN STORED IN X1-1. THE

* CHARACTER IN X1-1 DOES NOT EQUAL THE CHARACTER IN X1.

1410/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 134

PGLIN	LABEL	OPCODE	OPERAND	CT	ADORS	INSTRUCTION
B015	WXG4	MLWS	X1, WXG6	12	26141	O 00029 26154 4
B016		NOP		1	26153	N
B017	WXG6	BW	WXG5, X1-1	12	26154	V 26205 00028 1
B018		BW	WXG7, X1	12	26166	V 26197 00029 1
B019		BW	WXG7, X1-1	12	26178	V 26197 00028 1
B020		B	WXG5	7	26190	J 26205
B021	WXG7	B	SE1	7	26197	J 27220
B022		H		1	26204	.
B023	*		ROUTINE144 ERROR			
B024	*		THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 3 INDICATORS.			
B025	*		THE CHANNEL 3 INDICATORS WERE THEN STORED IN X1-1. X1 AND			
B026	*		X1-1 DO NOT BOTH HAVE A WORD MARK. OR DO NOT BOTH NOT			
			HAVE A WORD MARK.			
B027	WXG5	BA3	*E1	7	26205	3 26212 M
B028		BCE	WXG2, TAD1, 1	12	26212	B 26019 01001 1
B029		MLCWA	@ 2, X1	12	26224	O 29165 00029 X
B030	WXG8	B	SC1	7	26236	J 27380
			RESET IO INTERLOCK			
			LOOP ROUTINE144			
			CLEAR INDEX REG CNE			
			STEP ROUTINE COUNTER TO145			

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8032			*ROUTINE145-CHECK RESTORE AND STORE CHANNEL 4 STATUS INDICATORS IF			
8033			*7010 MACHINE.			
8034	WXH1	BCE	*E8,SYS1,X	12	26243	B 26262 01256 X
8035		B	WXH8	7	26255	J 26498
8036		BCE	WXH2,SYS1&15,1	12	26262	B 26281 01271 1
8037		B	WXH8	7	26274	J 26498
8038	WXH2	BNQ	ITR1	7	26281	J 01341 Q
8039		MLCWA	2 2,X1	12	26288	D 29165 00029 X
8040		MLCS	DD,X1-2	12	26300	D 01911 00027 3
8041		8A4	*E1	7	26312	1 26319 M
8042		RHC	X1-2	7	26319	\$ 00027 4
8043		MLZWS	CC-4,X1	12	26326	D 01896 00029 6
8044		MLNS	B8-3,X1	12	26338	D 01886 00029 1
8045		RHC	X1	7	26350	\$ 00029 4
8046		8	*E1	7	26357	J 26364
8047		SHC	X1-1	7	26364	\$ 00028 H
8048		MLCS	X1,WXH3&11	12	26371	D 00029 26394 3
8049	WXH3	BCE	WXH4,X1-1,	12	26383	B 26403 00028
8050		8	SE1	7	26395	J 27220
8051		H		1	26402	.
8052	*		ROUTINE145 ERROR			
8053	*		THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 4 INDICATORS.			
8054	*		THE CHANNEL 4 INDICATORS WERE THEN STORED IN X1-1. THE CHARACTER IN X1-1 DOES NOT EQUAL THE CHARACTER IN X1.			

I410/7010 CPU RELIABILITY TEST-4OK & UP

CU01 PAGE 136

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
8056	WXH4	MLWS	X1, WXH6	12	26403	D 00029 26416 4
8057		NOP		1	26415	N
8058	WXH6	BW	WXH5, X1-1	12	26416	V 26467 00028 1
8059		BW	WXH7, X1	12	26428	V 26459 00029 1
8060		BW	WXH7, X1-1	12	26440	V 26459 00028 1
8061		B	WXH5	7	26452	J 26467
8062	WXH7	B	SE1	7	26459	J 27220
8063		H		1	26466	.
8064	*		THE CHARACTER IN X1 WAS RESTORED TO CHANNEL 4 INDICATORS.			
8065	*		THE CHANNEL 4 INDICATORS WERE THEN STORED IN X1-1. X1 AND			
8066	*		X1-1 DO NOT BOTH HAVE A WORD MARK, OR DO NOT BOTH NOT			
8067	*		HAVE A WORD MARK.			
8068	WXH5	BA4	*C1	7	26467	1 26474 M
8069		BCE	WXH2, IAD1, 1	12	26474	8 26281 01001 1
8070		MLCWA	2 2, X1	12	26486	D 29165 00029 X
8071	WXH8	B	SC1	7	26498	J 27380

SET FOR CHECKING WM/IO INTRLK

GO IF X1 & X1-1 HAVE WMS, OK

GO IF EITHER HAS WM-ERROR

GO-NIETHER X1 OR X1-1 HAS WM-OK

BRANCH TO ERROR ROUTINE

ROUTINE145 ERROR

RESET IO INTERLOCK

LOOP ROUTINE145

CLEAR INDEX REG ONE

STEP ROUTINE COUNTER IO146

PGLIN	LABEL	OPC00	OPERANO	CT	ADORS	INSTRUCTION
B073						
B074	CSZERO	BNQ	ITR			BRANCH INQUIRY
B075		CS	00000	7	26505	J 01334 Q
B076		B	*E1	6	26512	/ 00000
B077		CS	C*00P,00000	7	26518	J 26525
B078		B	SET	11	26525	/ 26544 00000
B079		H		7	26536	J 27220
B080				1	26543	.
B081	CSLOOP	BCE	CSZERO,TA01,1			ROUTINE146 ERROR
B082		B	SCI	12	26544	B 26505 01001 1
B083				7	26556	J 27380
B084		BCE	CBTAA,SYSL,X			
B085		B	CBTEND	12	26563	B 26614 01256 X
B086		OCW	212478#*TAB0GH.BT ^{LL}	7	26575	J 26888
B087		OC	21NOR.*L-BTVWZ*ZM ^{LL}	16	26597	
B088	CBTAA	MLCWA	2 2,X1	16	26613	
B089		MLZWS	CC,X1	12	26614	0 29165 00029 X
B090		MLNS	BB,X1	12	26626	0 01900 00029 6
B091	CBTRP	BNQ	ITRI	12	26638	0 01889 00029 1
B092		CW	CBTEVN&1,CBTO00&1	7	26650	J 01341 Q
B093		SAR	CBCHK&5	11	26657	26802 26783
B094		SBR	CBCHK&17	7	26668	G 26756 A
B095		BW	CBTAC,X1	7	26675	G 26768 B
B096		CW	CBTO00&1,CBTEVN&1	12	26682	V 26719 00029 1
B097		SAR	CBCHK&5	11	26694	26783 26802
B098		SBR	CBCHK&17	7	26705	G 26756 A
B099	CBTAC	SW	CBTAA	7	26712	G 26768 B
B000		SAR	*E6	6	26719	26614
B001	CBTAB	MLCS	00000,CBCHK&11	7	26725	G 26731 A
B002		SAR	*-13	12	26732	D 00000 26762 3
B003	CBCHK	BCE	00000,X1,	7	26744	G 26737 A
B004		BCE	00000,CBCHK&11,1	12	26751	B 00000 00029
B005		B	CBTAB	12	26763	B 00000 26762 1
B006	CBTO00	CW	CBTYESE1	7	26775	J 26732
B007		SW	CBTNO&1	6	26782	26849
B008		B	CBITXX	6	26788	26826
B009	CBTEVN	CW	CBTNO&1	7	26794	J 26813
				6	26801	26826

* ROUTINE146--CHECK CLEAR STORAGE AT LOCATION 00000.
BRANCH INQUIRY
TRY FOR SYSTEM CHECK

ENSURE ABILITY TO CLEAR & BRANCH
BRANCH TO ERROR ROUTINE

THE CS INSTRUCTION SHOULD HAVE BRANCHED AND OLD NOT.
LOOP ROUTINE146
STEP ROUTINE COUNTER T0147

* ROUTINE147--CHECK BRANCH ON C BIT OP IF THIS IS A 7010 MACHINE.
GO IF NOT 7010
000 PARITY CHARACTERS
CLEAR X1
SET RANDOM CHAR IN X1 UNITS
SET ROUTINE FOR WM OR NOT WM
GO IF RAN00M CHAR HAS WM
GO IF RAN00M CHAR IS ODD
GO IF RAN00M CHARACTER IS NOT ODD
GO CHK NEXT ONE
MOVE AN ODD BIT CHARACTER

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BP10		SW	C8TYES&1	6	26807	J 26849
BP11	CBITXX	BBC	C8TYES,X1	12	26813	J 26848 00029 4
BP12	CBTNO	NOP		1	26825	N
BP13		8	CBTOK	7	26826	J 26864
BP14		8	SEI	7	26833	J 27220
BP15		H		1	26840	.
BP16	*		THE BRANCH ON C BIT OP AT LABEL CBITXX SHOULD HAVE			
BP17	*		BRANCHED SINCE X1 UNITS POSITION HAS A CHECK BIT.			
BP18	*		HOWEVER,THE 88C INSTRUCTION DID NOT BRANCH			
BP19		8	CBTOK	7	26841	J 26864
BP20	CBTYES	NOP		1	26848	N
BP21		8	CBTOK	7	26849	J 26864
BP22		8	SEI	7	26856	J 27220
BP23		H		1	26863	.
BP24	*		THE BRANCH ON C BIT OP AT LABEL CBITXX SHOULD NOT HAVE			
BP25	*		BRANCHED SINCE X1 UNITS POSITION HAS NO CHECK BIT.			
BP26	*		HOWEVER,THE 88C OP DID BRANCH.			
BP27	CBTOK	BCE	CBTRP,IA01,1	12	26864	B 26650 01001 1
BP28		MLCWA	2 2,X1	12	26876	D 29165 00029 X
BP29	CBTEND	B	SCI	7	26888	J 27380
BP30			ROUTINE148-CHECK FOR PROPER PROGRAM SEQUENCING.			
BP31	KAL	BNQ	ITR	7	26895	J 01334 Q
BP32		C	CN3,PASCHK	11	26902	C 01401 26962
BP33		BE	KA2	7	26913	J 26940 S
BP34		MLCB	CN3,X1	12	26920	D 01401 00029 L
BP35		B	SEI	7	26932	J 27220
BP36		H		1	26939	.
BP37	*		THE ROUTINE COUNT AT CN3 IS STEPPED AT THE END OF			
BP38	*		EACH ROUTINE. CN3 SHOULD NOW CONTAIN THE NUMBER OF			
BP39	*		THIS ROUTINE. IT DOES NOT.			
BP40	KA2	BCE	KAL,IA01,1	12	26940	B 26895 01001 1
BP41		B	CL5	7	26952	J 26963
BP42	PASCHK	DCW	201482	4	26962	

THIS ROUTINES NUMBER

1410/7010 CPU RELIABILITY TEST-40K & UP

CT ADDRS INSTRUCTION

PGLIN	LABEL	OPCODE	OPERAND		CT	ADDRS	INSTRUCTION
BP44			*END OF ONE PROGRAM PASS.		7	26963	J 01334 Q
BP45	ZA1	BNQ	ITR	BRANCH INQUIRY	11	26970	A 29202 28538
BP46		A	£1,CO1	STEP PASS COUNTER	12	26981	B 27004 01402 1
BP47	T2	BCE	ZA2,CN4,1	BRANCH IF ERROR PASS	11	26993	A 29202 01477
BP48		A	£1,CO4	STEP SUCCESSFUL PASS COUNTER	11	27004	C 28538 28749
BP49	ZA2	C	CD1,FASTE	IS RELIABILITY RUN COMPLETE	12	27015	V 27038 00997 1
BP50		C BW	*£12,00997	BRANCH IF IN RELIABILITY MODE	11	27027	C 28538 28745
BP51		C	CD1,FASTF	IS NORMAL RUN COMPLETE	7	27038	J 27189 /
BP52		C BU	ZA5	BRANCH-NO RUN IS COMPLETE	11	27045	A 28538 27098
BP53		C A	CD1,CCTYPAE4	ADD # OF COMPTO PASSES TO TYP0UT	11	27056	A 01477 27111
BP54		C A	CD4,CCTYPB£4	ADD # OF SUCSFUL PASSES TO TYP0UT	12	27067	B 27116 01000 1
BP55		C BCE	CCNDTP,TAD0,1	BRANCH TO BYPASS ALL PRINTING	8	27086	
BP56		C DCW	£N a	UNNECESSARY NDP	7	27087	J 01289
BP57		C B	TYPI		13	27094	
BP58		CCTYP A	C DCW		8	27107	
BP59		CCTYP B	C DCW		12	27116	D 29186 28538 X
BP60		CCNOTP	C MLCWA		12	27128	D 29186 01477 P
BP61		C	MLCNB		11	27140	C 27097 29186
BP62		C	CCTYPAE3,£00000a		7	27151	J 27170 /
BP63		C BU	*£13		12	27158	O 27098 27111 T
BP64		C MLCA	CCTYPAE4,CCTYPB£4		12	27170	B 27189 01003 1
BP65		C BCE	ZA5,TAD3,1		7	27182	J 00400
BP66		C B	NEX1		12	27189	D 29156 01402 3
BP67	ZA5	MLCS	£0a,CN4		12	27201	O 29326 01401 X
BP68		MLCWA	a 2a,CN3		7	27213	J 02512
BP69		B	AC1				

CLEAR PASS COUNTER
 CLEAR SUCCESS PASS COUNTER
 100,000 PASSES YET
 BRANCH IF NOT
 ZERO OK TYP0UT
 BRANCH-DO NOT END PROGRAM
 TO LOAD ROUTINE
 CLEAR ERROR INDICATOR
 SET ROUTINE COUNTER TO TWO
 REPEAT PROGRAM

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPC00	OPERANO	CU01	ADORS	INSTRUCTION
BP71	*CLOSED ERROR SUBROUTINE					
BP72	SE1	SBR	SE2&13	7	27220	G 27289 B
BP73		SBR	SE5&5	7	27227	G 27336 B
BP74		SBR	SE4&5	7	27234	G 27359 B
BP75		BCE	SE6,TA00,1	12	27241	B 27307 01000 1
BP76		MLNB	CN3,SE2&2	12	27253	0 01401 27278 J
BP77		B	TYPE1	7	27265	J 01289
BP78		OCW	@*RT @	4	27275	
BP79	SE2	OCW	@ ,ADOR	18	27276	
BP80	SE3	BCE	DATA,TA05,1	12	27295	B 28051 01005 1
BP81	SE6	BCE	SE7,TA04,1	12	27307	B 27361 01004 1
BP82		MLCS	@1&,CN4	12	27319	D 29167 01402 3
BP83	SE5	BCE	0,TA02,1	12	27331	B 00000 01002 1
BP84		A	&1,SE4&5	11	27343	A 29202 27359
BP85	SE4	B	0	7	27354	J 00000
BP86	SE7	MLCS	@1&,TA01	12	27361	0 29167 01001 3
BP87		B	SE5	7	27373	J 27331
BP88	*CLOSED STEP ROUTINE COUNTER SUBROUTINE					
BP89	SC1	SBR	SC2&5	7	27380	G 27403 B
BP90		A	&1,CN3	11	27387	A 29202 01401
BP91	SC2	B	0	7	27398	J 00000

BRANCH-BYPASS ALL TYPING
MOVE ROUTINE NUMBER FOR ERROR PRT
PRINT ERROR MESSAGE
*ERR&.G
BRANCH-PRINT ADDITIONAL ERR DATA
BRANCH-SET TA0 1
SET ERROR INDICATOR
BRANCH-TO ERROR HALT
MODIFY RETURN ADDRESS
BRANCH TO NEXT ROUTINE
SET TA0 1
RETURN TO PROGRAM

1410/7010 CPU RELIABILITY TEST-40K & UP

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BP93				7	27405	J 01289
BP94	SD1	B	TYPI	15	27426	
BP95		DCW	ENTER CONSTANT a,g	7	27428	J 01289
BP96	SD2	B	TYPI	2	27436	
BP97		DCW	AAAa,g	12	27438	D 01642 01630 L
BP98		MRCWG	CQ65E1,CQ6-9	6	27450	* 01630
BP99		SW	CQ6-9	10	27456	M 810 01630 R
BQ00	SD20	RCP	CQ6-9	7	27466	G 27497 B
BQ01		SBR	S03E10	7	27473	R 27456 M
BQ02		BEX1	SD20,M	7	27480	R 27487 M
BQ03		BAL	*E1	12	27487	O 29328 00000 X
BQ04	SD3	MLCWA	AM a,0	7	27499	G 27518 B
BQ05		SBR	SD4E5	7	27506	G 27611 B
BQ06		SBR	SD6E5	12	27513	O 00000 01878 X
BQ07	SD4	MLCWA	O,AA	7	27525	J 01289
BQ08	SD5	B	TYPI	2	27533	
BQ09		OCW	ACCa,g	12	27535	O 29208 01630 7
BQ10		MLCWS	a a,CQ6-9	10	27547	M 810 01630 R
BQ11	SD15	RCP	CQ6-9	7	27557	R 27547 M
BQ12		BEX1	*-16,M	7	27564	R 27525 M
BQ13		BAL	SD5	12	27571	D 01878 01900 U
BQ14		MLWA	AA,CC	11	27583	Q 01878 01900
BQ15		ZA	AA,CC	12	27594	D 01900 01878 S
BQ16		MLZA	CC,AA	12	27606	D 00000 01900 K
BQ17	SD6	MLZB	O,CC	7	27618	J 01289
BQ18	SD7	B	TYPI	2	27626	
BQ19		DCW	ABBa,g	12	27628	D 01642 01630 L
BQ20		MRCWG	CQ65E1,CQ6-9	6	27640	* 01630
BQ21		SW	CQ6-9	10	27646	M 810 01630 R
BQ22	SD16	RCP	CQ6-9	7	27656	G 27687 B
BQ23		SHR	S014E10	7	27663	R 27646 M
BQ24		BEX1	SD16,M	7	27670	R 27677 M
BQ25		BAL	*E1	12	27677	O 29328 00000 X
BQ26	SD14	MLCWA	AM a,0	7	27689	G 27708 H
BQ27		SBR	S09E5	7	27696	G 27801 H
BQ28		SHR	S01E5			

*SUBROUTINE TO RECEIVE CONSTANTS ON REQUEST.

CLEAR POSSIBLE G/M,W/M

READ CONSTANT CC

BRANCH ANY

AA W/M TO CC

AA NUMERIC TO CC

SET AA SIGN

STORE CC ZONE

CLEAR STORAGE AREA

READ CONSTANT BB

BRANCH ANY BUT WLR

TERMINATE BB

PGLIN	LABEL	OPCOO	OPERANO	CT	AOORS	INSTRUCTION
BQ30	S09	MLCWA	O,BB	12	27703	D 00000 01889 X
BQ31	S010	B	TYPI	7	27715	J 01289
BQ32		OCW	00000,G	2	27723	
BQ33		MLCWS	@ @,CQ6-9	12	27725	D 29208 01630 7
BQ34	S017	RCP	CQ6-9 S	10	27737	M 270 01630 R
BQ35		BEX1	*-16,M	7	27747	R 27737 M
BQ36		BAL	SD10	7	27754	R 27715 M
BQ37		MLWA	BB,00	12	27761	D 01889 01911 U
BQ38		ZA	BB,00	11	27773	M 01889 01911
BQ39		MLZA	00,BB	12	27784	D 01911 01889 S
BQ40	S011	MLZB	0,00	12	27796	O 00000 01911 K
BQ41	S012	B	TYPI	7	27808	J 01289
BQ42		OCW	0EE0,G	2	27816	
BQ43		MLCWA	0000000,CQ7	12	27818	D 29196 01657 X
BQ44	S018	RCP	CQ7-4 S	10	27830	M 270 01653 K
BQ45		BEX1	*-16,M	7	27840	R 27830 M
BQ46		BAL	S012	7	27847	R 27808 M
BQ47		MLNWA	CQ7,EE	12	27854	O 01657 01916 V
BQ48	S013	B	TYPI	7	27866	J 01289
BQ49		OCW	0FF0,G	2	27874	
BQ50		MLCWA	0000000,CQ7	12	27876	D 29196 01657 X
BQ51	S019	RCP	CQ7-4 S	10	27888	M 270 01653 K
BQ52		BEX1	*-16,M	7	27898	R 27888 M
BQ53		BAL	SD13	7	27905	R 27866 M
BQ54		MLNWA	CQ7,FF	12	27912	D 01657 01921 V
BQ55		SCNLA	AA,1011	12	27924	D 01878 01011 B
BQ56		SBR	C02	7	27936	G 01467 B
BQ57		A	-1011,C02	11	27943	A 29207 01467
BQ58		MLZS	@ @,C02	12	27954	O 29208 01467 2
BQ59		SCNLA	BB,1011	12	27966	D 01889 01011 B
BQ60		SBR	C025	7	27978	G 01472 B
BQ61		A	-1011,C025	11	27985	A 29207 01472
BQ62		MLZS	@ @,C025	12	27996	O 29208 01472 2
BQ63		MLCS	@ @,IAD6	12	28008	D 29208 01006 3
BQ64		MLCS	@1@,IAD7	12	28020	D 29167 01007 3

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BQ66	*SETUP FOR SKIPPING CONSTANT GENERATION ROUTINES.					
BQ67	SD8	MLCA	2000462,CN3	12	28032	0 29333 01401 T
BQ68		B	BT1	7	28044	J 07192
BQ69	*SU8-SUBROUTINE TO PRINT ADDITIONAL ERROR DATA.					
BQ70	DATA	SBR	DATA7E5	7	28051	G 28532 B
BQ71		MLC	CO1,DATA1	12	28058	0 28538 28098 C
BQ72		A	E1,DATA1	11	28070	A 29202 28098
BQ73		B	TYPI	7	28081	J 01289
BQ74		DCW	a PASS a	7	28094	
BQ75	DATA1	DCW	a a,G	4	28098	
BQ76		MLC8	X10,DATA2	12	28100	0 00074 28276 L
BQ77		MLC8	X9,DATA2-10	12	28112	0 00069 28266 L
BQ78		MLC8	X8,DATA2-19	12	28124	0 00064 28257 L
BQ79		MLC8	X7,DATA2-28	12	28136	0 00059 28248 L
BQ80		MLC8	X6,DATA2-37	12	28148	0 00054 28239 L
BQ81		MLC8	X5,DATA2-46	12	28160	0 00049 28230 L
BQ82		MLC8	X2,DATA2-55	12	28172	0 00034 28221 L
BQ83		MLC8	X1,DATA2-64	12	28184	0 00029 28212 L
BQ84		B	TYPI	7	28196	J 01289
BQ85		DCW	a X1-a	5	28207	
BQ86		DCW	a ,X2-a	9	28216	
BQ87		DCW	a ,X5-a	9	28225	
BQ88		DCW	a ,X6-a	9	28234	
BQ89		DCW	a ,X7-a	9	28243	
BQ90		DCW	a ,X8-a	9	28252	
BQ91		DCW	a ,X9-a	9	28261	
BQ92		DCW	a ,X10-a	10	28271	

PRINT INDEX REGISTERS

PGLIN	LABEL	OPCODE	OPERAND	CLEAR W/MS IN PRINT AREA	CT	ADDRS	INSTRUCTION
8Q94	DATA2	DCW	a a.G		5	28276	
8Q95		SW	DATA5,DATA5&1		11	28278	28442 28443
8Q96		CW	DATA6		6	28289	28516
8Q97		MLW	DATA6,DATA6-1		12	28295	D 28516 28515 D
HQ98		MLW	G		1	28307	D
8Q99		MLCWS	aMa,DATA6&1		12	28308	D 29255 28517 7
8R00		MLCA	FF,DATA6	SET UP CONSTANT STATEMENT	12	28320	D 01921 28516 I
8R01		MLCA	a,FF-a		6	28332	D 29337
8R02		MLCA	EE		6	28338	D 01916
8R03		MLCA	a,EE-a		6	28344	D 29341
8R04		MLCA	DD		6	28350	D 01911
8R05		MLCA	a,DD-a		6	28356	D 29345
8R06		MLCA	CC		6	28362	D 01900
8R07		MLCA	a,CC-a		6	28368	D 29349
8R08		MLCA	88		6	28374	D 01889
8R09		MLCA	a,88-a		6	28380	D 29353
8R10		MLCA	AA		6	28386	D 01878
8R11		MLCA	a AA-a		6	28392	D 29357
8R12		SBR	DATA3&5		7	28398	G 28410 H
8R13	DATA3	MRCWG	D,DATA5		12	28405	D 00000 28442 L
8R14		MRCWG	DATA7		6	28417	D 28527
8R15		MLCWS	a a,DATA5		12	28423	D 29208 28442 7
8R16		B	TYPI	PRINT CONSTANTS	7	28435	J 01289
8R17	DATA5	DCW	a		50	28442	
8R18	DATA6	DC	a	a.G	25	28516	
8R19		DC	a		9	28526	
8R20	DATA7	B	SE6		7	28527	J 27307
8R21		DCW	aMa		1	28534	

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BR56	RUPBOT	ZA	0EX5	6	28750	Q 00*0
BR57		ZA		1	28756	M 00*0
BR58		ZA		11	28757	M 00*0 00*0
BR59		DCW	0EX5,0EX6	1	28768	00*0 00*0
BR60		ZS	0EX5	6	28770	00*0
BR61		ZS	0EX5	1	28776	00*0
BR62		ZS	0EX5,0EX6	11	28777	00*0 00*0
BR63		DCW	0EX5,0EX6	1	28788	00*0
BR64		A	0EX5	6	28790	A 00*0
BR65		A	0EX5	1	28796	A
BR66		A	0EX5,0EX6	11	28797	A 00*0 00*0
BR67		DCW	0EX5,0EX6	1	28808	00*0
BR68		S	0EX5	6	28810	S 00*0
BR69		S	0EX5,0EX6	1	28816	S
BR70		S	0EX5,0EX6	11	28817	S 00*0 00*0
BR71		DCW	0EX5,0EX6	1	28828	00*0
BR72		M	0EX5	6	28830	00*0
BR73		M	0EX5	1	28836	00*0
BR74		M	0EX5,0EX6	11	28837	00*0 00*0
BR75		DCW	0EX5,0EX6	1	28848	00*0
BR76		D	0EX5	6	28850	% 00*0
BR77		D	0EX5	1	28856	%
BR78		D	0EX5,0EX6	11	28857	% 00*0 00*0
BR79		DCW	0EX5,0EX6	1	28868	00*0
BR80						

*TABLE OF INTERRUPTABLE AND NON INTERRUPTABLE INSTRUCTIONS.

141C/7010 CPU RELIABILITY TEST-40K & UP

CU01 PAGE 147

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDRS	INSTRUCTION
BR82		MCE	0EX5	6	28870	E 00*0
BR83		MCE		1	28876	E
BR84		MCE	0EX5,0EX6	11	28877	E 00*0 00*0
BR85		DCW	AN2,G	1	28888	
BR86		MCS	0EX5	6	28890	Z 00*0
BR87		MCS		1	28896	Z
BR88		MCS	0EX5,0EX6	11	28897	Z 00*0 00*0
BR89		DCW	AN2,G	1	28908	
BR90		C	0EX5	6	28910	C 00*0
BR91		C		1	28916	C
BR92		C	0EX5,0EX6	11	28917	C 00*0 00*0
BR93		DCW	AN2,G	1	28928	
BR94		CS	39999	6	28930	/ 39999
BR95		CS		1	28936	/
BR96		CS	LC13,0EX5	11	28937	/ 25124 00*0
BR97		DCW	AN2,G	1	28948	
BR98		SW	0EX5	6	28950	, 00*0
BR99		SW		1	28956	,
BS00		SW	0EX5,0EX6	11	28957	, 00*0 00*0
BS01		DCW	AN2,G	1	28968	
BS02		CW	0EX5	6	28970	□ 00*0
BS03		CW		1	28976	□
BS04		CW	0EX5,0EX6	11	28977	□ 00*0 00*0
BS05		DCW	AN2,G	1	28988	
BS06		BBE	00000	6	28990	W 00000
BS07		BBE		1	28996	W
BS08		BBE	LC13,0EX5	12	28997	W 25124 00*0
BS09		DCW	AN2,G	1	29009	

LCC2

PGLIN	LABEL	OPCCD	OPERAND	CT	ADDRS	INSTRUCTION
BS11	LCC3	BZN	00000	6	29011	V 00000
BS12		BZN		1	29017	V
BS13		BZN	LC13,0EX5,-	12	29018	V 25124 00*0 K
BS14		DCW	AN2,G	1	29030	
BS15		MLCWS	0EX5	6	29032	D 00*0
BS16		MLCWS		1	29038	D
BS17		MLCWA	0EX5,0EX6	12	29039	D 00*0 00*0 X
BS18		DCW	AN2,G	1	29051	
BS19		BCE	00000	6	29053	B 00000
BS20	LCC4	BCE		1	29059	B
BS21		BCE	LC13,0EX5,X	12	29060	B 25124 00*0 X
BS22		DCW	AN2,G	1	29072	
BS23		LLH	0EX5	6	29074	T 00*0
BS24		LLH		1	29080	T
BS25		LLE	0EX5,0EX6	12	29081	T 00*0 00*0 3
BS26		DCW	AN2,G	1	29093	
BS27	LCC5	CW	00000	6	29095	B 00000
BS28		DCW	AY2	1	29101	
BS29		BDV	LC13	7	29102	J 25124 W
BS30		DCW	AN 2,G	5	29113	
BS31		SBR	39999	7	29115	G 39999 B
BS32		BA1	LC13	7	29122	R 25124 M
BS33		DCW	AN# 2,G	5	29133	
BS34		DCW	AN 2	7	29141	
BS35		BXPA	LC13	7	29142	V 25124 X
BS36	RUPTOP	DCW	AN# 2,G	5	29153	
BS37	ALAST	C	2Z2	6	29155	C 29358

NO *
NO *
YES *
NO *
* NO
* NO
* YES
* NO
NO *
NO *
YES *
NO *
* NO
* NO
* YES
* NO
NO *
NO *
NO *
YES *
NO *
* NO
* NO
* YES
* NO
NO *
NO *
NO *
YES *
NO *
NO *
* NO
* NO
* NO-TURN OFF INTERRUPT REQUEST
* NO
* NO
NO *
NO *
TURN OFF PRIORITY MODE
NO *
LAST INSTRUCTION OF PROGRAM

CU01 INSTRUCTION

PGLIN	LABEL	OPCODE	OPERAND	CT	ADDRS	INSTRUCTION
BS39	•LITERAL CONSTANTS.					
BS40						
BS40			a a	5	29161	
BS40			a0a	1	29165	
BS40			a1a	1	29166	
BS40			a 0a	5	29167	
BS40			E99993	5	29172	
BS40			E00006	5	29177	
BS40			a0000a	5	29182	
BS40			RUP80T	4	29186	
BS40			a00000a	5	29191	28750
BS40			a00011a	5	29196	
BS40			E1	5	29201	
BS40			-1	1	29202	
BS40			-1011	1	29203	
BS40			a a	4	29207	
BS40			a00010a	1	29208	
BS40			a00001a	5	29213	
BS40			E5000	5	29218	
BS40			a00150a	4	29222	
BS40			E150	5	29227	
BS40			-00023	3	29230	
BS40			a00023a	5	29235	
BS40			E50	5	29240	
BS40			E350	2	29242	
BS40			a00100a	3	29245	
BS40			E200	5	29250	
BS40			a+a	3	29253	
BS40			aMa	1	29254	
BS40			a+a	1	29255	
BS40			a+a	2	29257	
BS40			a,0 --a	5	29262	
BS40			a,0 a	3	29265	
BS40			a+a	1	29266	
BS40			E712	5	29271	15674
BS40			005	5	29276	16610
BS40			a-a	1	29277	
BS40			a+a	1	29278	

CT ADDR INSTRUCTION

OPCOD OPERAND

LABEL

PGLIN

PGLIN	LABEL	OPCOD	OPERAND	CT	ADDR	INSTRUCTION
BS40		09a		1	29279	
BS40		099a		2	29281	
BS40		05		1	29282	
BS40		EDTDA		5	29287	24489
BS40		EDTSM		5	29292	24478
BS40		00000a		5	29297	
BS40		05a		1	29298	
BS40		0aa		1	29299	
BS40		0aa		1	29300	
BS40		04a		1	29301	
BS40		0-CR 0a		5	29306	
BS40		0a		2	29308	
BS40		0aa		2	29310	
BS40		049a		2	29312	
BS40		024a		2	29314	
BS40		074a		2	29316	
BS40		RUPTOP		5	29321	29153
BS40		0a 2a		5	29326	
BS40		0a 0a		2	29328	
BS40		000046a		5	29333	
BS40		0,FF-0		4	29337	
BS40		0,EE-0		4	29341	
BS40		0,DD-0		4	29345	
BS40		0,CC-0		4	29349	
BS40		0,HH-0		4	29353	
BS40		0 AA-0		4	29357	
BS40		02a		1	29358	

D.E.B.

END START

END OF ASSEMBLY

J02000